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December 2014

P-Channel 1.5 V Specified PowerTrench[®] Thin WL-CSP MOSFET -20 V, -3.8 A, 64 m Ω

Features

RoHS Compliant

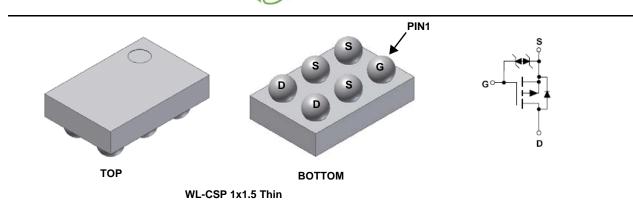
- Max $r_{DS(on)}$ = 64 m Ω at V_{GS} = -4.5 V, I_D = -2.0 A
- Max $r_{DS(on)} = 71 \text{ m}\Omega$ at $V_{GS} = -2.5 \text{ V}$, $I_D = -2.0 \text{ A}$
- Max $r_{DS(on)} = 79 \text{ m}\Omega$ at $V_{GS} = -1.8 \text{ V}$, $I_D = -1.0 \text{ A}$
- Max $r_{DS(on)}$ = 95 m Ω at V_{GS} = -1.5 V, I_D = -1.0 A
- Occupies only 1.5 mm² of PCB area.Less than 50% of the area of 2 x 2 BGA
- Ultra-thin package: less than 0.65 mm height when mounted to PCB
- HBM ESD protection level > 4400V (Note3)
 - 10(00)

General Description

Designed on Fairchild's advanced 1.5 V PowerTrench[®] process with state of the art "fine pitch" WLCSP packaging process, the FDZ197PZ minimizes both PCB space and $r_{DS(on)}$. This advanced WLCSP MOSFET embodies a breakthrough in packaging technology which enables the device to combine excellent thermal transfer characteristics, ultra-low profile packaging, low gate charge, and low $r_{DS(on)}$.

Applications

- Battery management
- Load switch
- Battery protection



MOSFET Maximum Ratings T_A = 25 °C unless otherwise noted

Symbol	Para	meter		Ratings	Units
V _{DS}	Drain to Source Voltage			-20	V
V _{GS}	Gate to Source Voltage			±8	V
I	-Continuous	$T_A = 25^{\circ}C$	(Note 1a)	-3.8	٨
D	-Pulsed			-15	Α
D	Power Dissipation	T _A = 25°C	(Note 1a)	1.9	w
P _D	Power Dissipation	T _A = 25°C	(Note 1b)	0.9	vv
T _J , T _{STG}	Operating and Storage Junction Temperature Range			-55 to +150	°C

Thermal Characteristics

R _{θJA}	Thermal Resistance, Junction to Ambient	(Note 1a)	65	°C ///
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	(Note 1b)	133	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
7	FDZ197PZ	WL-CSP 1x1.5 Thin	7 "	8 mm	5000 units

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Chara	octeristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = -250 \ \mu A, \ V_{GS} = 0 \ V$	-20			V
$\frac{\Delta BV_{DSS}}{\Delta T_J}$	Breakdown Voltage Temperature Coefficient	$I_D = -250 \ \mu$ A, referenced to 25 °C		-10		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = -16 V, V _{GS} = 0 V			-1	μA
I _{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 8 V, V_{DS} = 0 V$			±10	μΑ
$\frac{\Delta V_{GS(th)}}{\Delta T_{J}}$	Gate to Source Threshold Voltage Gate to Source Threshold Voltage Temperature Coefficient Coefficient	$V_{GS} = V_{DS}, I_D = -250 \ \mu A$ $I_D = -250 \ \mu A$, referenced to 25 °C		2.7		mV/°C
$\frac{\Delta V_{GS(th)}}{\Delta T}$	5	I_D = -250 μ A, referenced to 25 °C		2.7		mV/°C
		$V_{GS} = -4.5 \text{ V}, I_D = -2.0 \text{ A}$		46	64	mΩ
		$V_{GS} = -2.5 \text{ V}, I_D = -2.0 \text{ A}$		53	71	
r _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = -1.8 \text{ V}, I_D = -1.0 \text{ A}$		59 68	79 95	
		$V_{GS} = -1.5 \text{ V}, \text{ I}_{D} = -1.0 \text{ A}$		00	95	_
		V _{GS} = -4.5 V, I _D = -2.0 A, T _J =125 °C		54	84	mV/°C μA μA V mV/°C
9 _{FS}	Forward Transconductance	V _{DD} = -5 V, I _D = -3.8 A		21		S

C_{iss} 1180 1570 Input Capacitance $V_{DS} = -10 \text{ V}, \text{ V}_{GS} = 0 \text{ V},$ 190 255 C_{oss} **Output Capacitance** f = 1 MHz C_{rss} **Reverse Transfer Capacitance** 160 225

Switching Characteristics

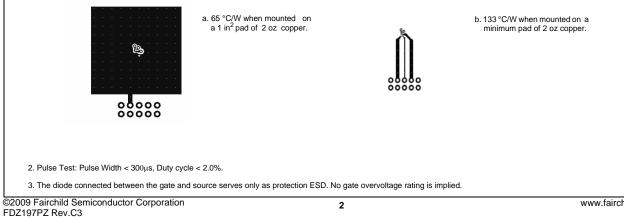
	0				
t _{d(on)}	Turn-On Delay Time		5.8	12	ns
t _r	Rise Time	V _{DD} = -10 V, I _D = -3.8 A,	5.9	12	ns
t _{d(off)}	Turn-Off Delay Time	$V_{GS} = -4.5 \text{ V}, \text{ R}_{GEN} = 6 \Omega$	311	498	ns
t _f	Fall Time		280	448	ns
Qg	Total Gate Charge	$V_{GS} = 0V \text{ to } -4.5V$ $V_{DD} = -10 \text{ V},$	18	25	nC
Q _{gs}	Gate to Source Charge	V _{DD} = -10 V, I _D = -3.8 A	1.5		nC
Q _{gd}	Gate to Drain "Miller" Charge		4.7		nC

Drain-Source Diode Characteristics

V _{SD}	Source to Drain Diode Forward Voltage	$V_{GS} = 0 V, I_S = -1.1 A$ (Note 2)	-0.6	-1.2	V
t _{rr}	Reverse Recovery Time	I _E = -3.8 A, di/dt = 100 A/μs	194	310	ns
Q _{rr}	Reverse Recovery Charge	$F_{\rm F} = -3.8 \text{A}, \text{d}/\text{d} = 100 \text{A}/\mu\text{s}$	344	550	nC

Notes:

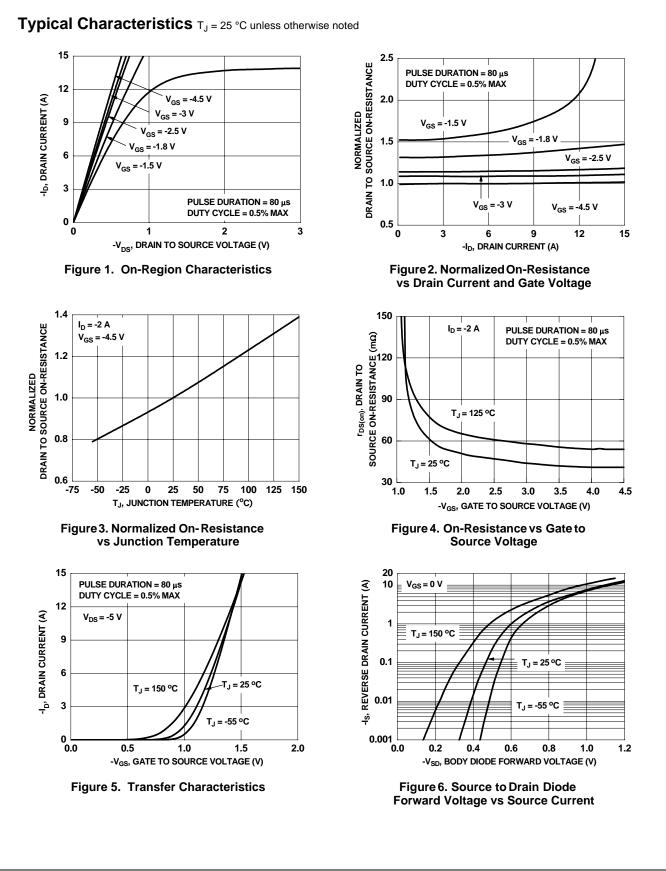
1. R_{0,J} is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. R_{0,JC} is guaranteed by design while R_{0CA} is determined by the user's board design.



pF

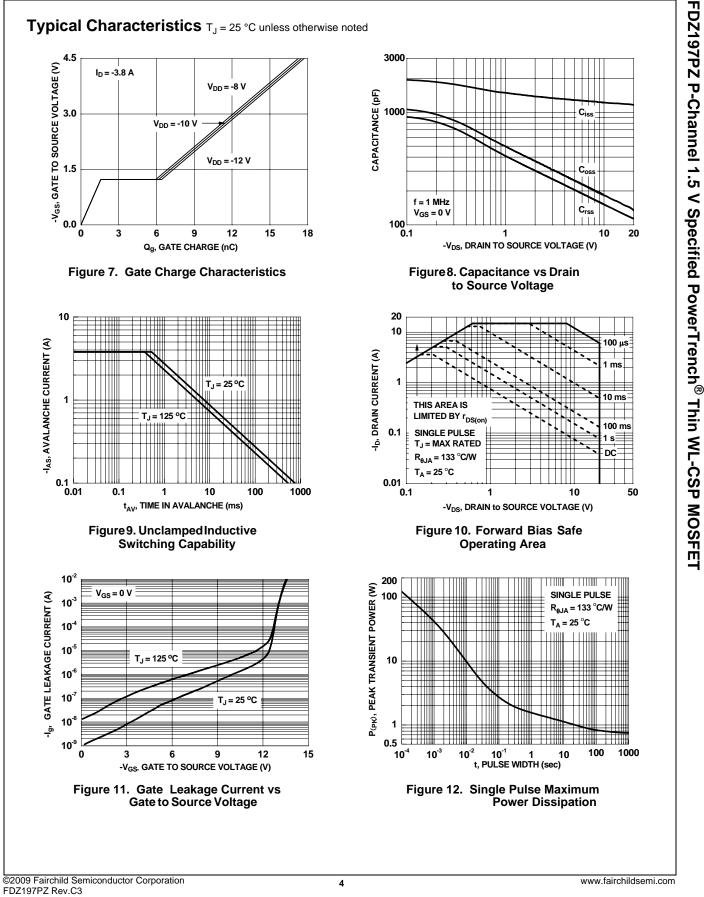
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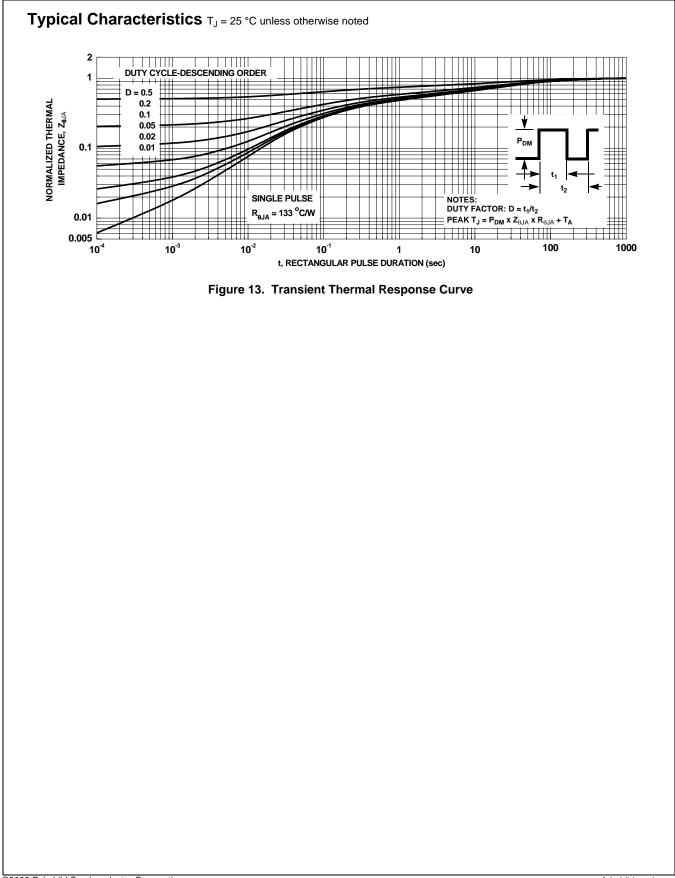
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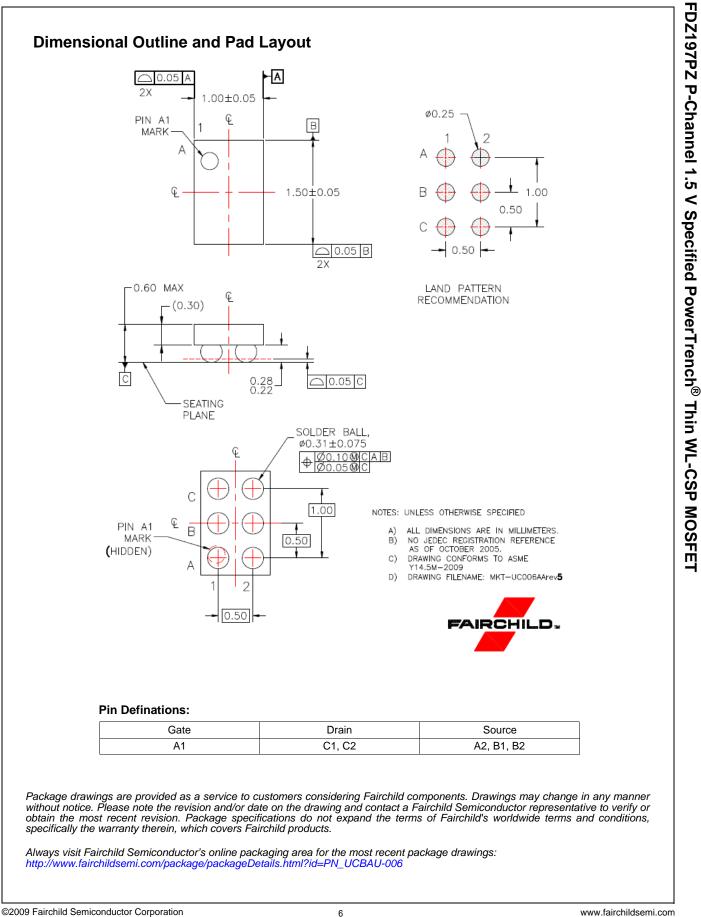


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FDZ197PZ P-Channel 1.5 V Specified PowerTrench[®] Thin WL-CSP MOSFET







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