# Silicon Carbide Schottky Diode

## 650 V, 20 A

#### Description

Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size & cost.

#### Features

- Max Junction Temperature 175°C
- Avalanche Rated 94 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery/No Forward Recovery
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

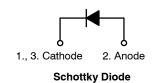
#### Applications

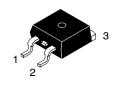
- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits



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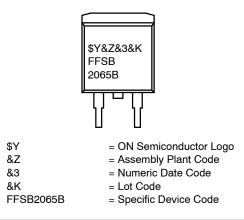
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D<sup>2</sup>PAK-2 (TO-263, 2 LEAD) CASE 418BK

#### MARKING DIAGRAM



#### ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

## FFSB2065B

Symbol	Parameter	Value	Unit	
V <sub>RRM</sub>	Peak Repetitive Reverse Voltage		650	V
E <sub>AS</sub>	Single Pulse Avalanche Energy (Note 1)		94	mJ
١ <sub>F</sub>	Continuous Rectified Forward Current @ $T_C < 142^{\circ}C$		20	A
	Continuous Rectified Forward Current @ T <sub>C</sub> < 135°C		22.8	
I <sub>F, Max</sub>	Forward Surge Current $T_{C} = 150^{\circ}C, 10 \ \mu s$	T <sub>C</sub> = 25°C, 10 μs	882	А
		798		
I <sub>F, SM</sub>	Non-Repetitive Forward Surge Current $T_{C} = 25^{\circ}C$	Half-Sine Pulse, t <sub>p</sub> = 8.3 ms	84	A
P <sub>tot</sub>	Power Dissipation	T <sub>C</sub> = 25°C	153	W
		T <sub>C</sub> = 150°C	25.5	
T <sub>J</sub> , T <sub>STG</sub>	Operating Junction and Storage Temperature Range		–55 to +175	°C

#### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = $25^{\circ}$ C unless otherwise noted)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. 1.  $E_{AS}$  of 94 mJ is based on starting  $T_J = 25^{\circ}C$ , L = 0.5 mH,  $I_{AS} = 19.4$  A, V = 50 V.

### **THERMAL CHARACTERISTICS**

Symbol	Parameter	Ratings	Unit
$R_{\thetaJC}$	Thermal Resistance, Junction to Case, Max	0.98	°C/W

#### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V <sub>F</sub>	Forward Voltage	$I_F = 20 \text{ A}, T_C = 25^{\circ}\text{C}$	-	1.38	1.7	V
		$I_F = 20 \text{ A}, \text{ T}_C = 125^{\circ}\text{C}$	-	1.6	2.0	1
		I <sub>F</sub> = 20 A, T <sub>C</sub> = 175°C	-	1.72	2.4	1
I <sub>R</sub>	Reverse Current	$V_{R} = 650 \text{ V}, \text{ T}_{C} = 25^{\circ}\text{C}$	-	0.5	40	μΑ
		$V_{R} = 650 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$	-	1	80	
		$V_{R} = 650 \text{ V}, \text{ T}_{C} = 175^{\circ}\text{C}$	-	2	160	
Q <sub>C</sub>	Total Capacitive Charge	V = 400 V	-	51	-	nC
C <sub>tot</sub>	Total Capacitance	V <sub>R</sub> = 1 V, f = 100 kHz	-	866	-	pF
		V <sub>R</sub> = 300 V, f = 100 kHz	-	80	-	]
		V <sub>R</sub> = 600 V, f = 100 kHz	_	70	-	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

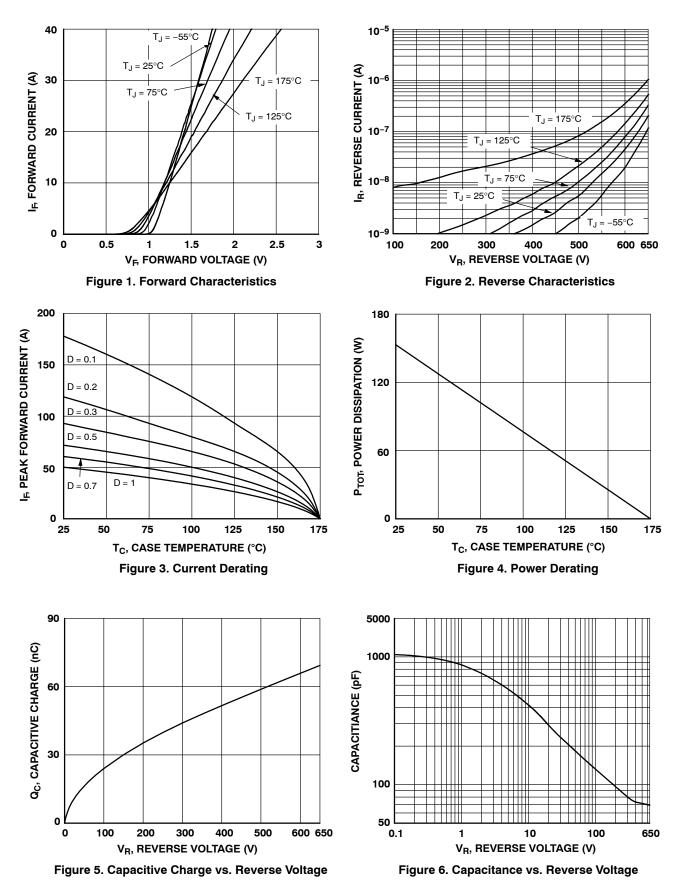
#### **ORDERING INFORMATION**

Part Number	Top Marking	Package	Shipping <sup>†</sup>
FFSB2065B	FFSB2065B	D <sup>2</sup> PAK-2	800/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

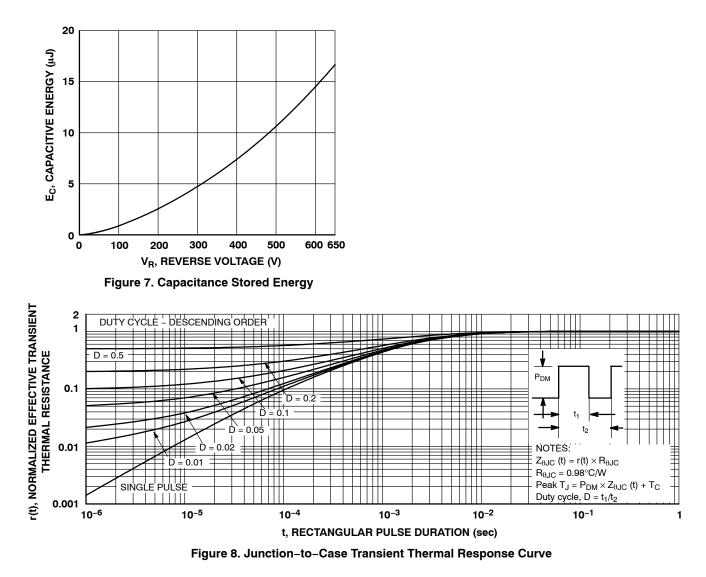
### FFSB2065B

#### TYPICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted)



#### FFSB2065B

**TYPICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$  unless otherwise noted) (continued)



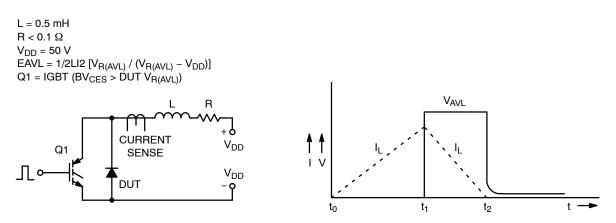
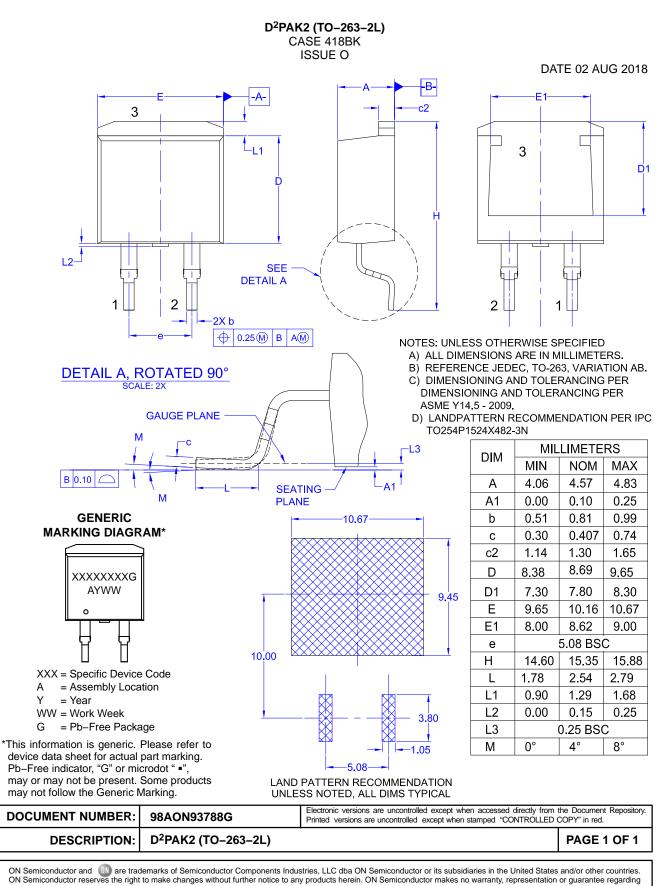


Figure 9. Unclamped Inductive Switching Test Circuit & Waveform





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