IGBT - Field Stop
650 V, 40 A

FGH40N65UFDTU, FGH40N65UFDTU-F085

Description

Using novel field stop IGBT technology, ON Semiconductor's field stop IGBTs offer the optimum performance for Automotive Chargers, Inverter, and other applications where low conduction and switching losses are essential.

Features

- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)}$ = 1.8 V @ I_C = 40 A
- High Input Impedance
- Fast Switching
- Qualified to Automotive Requirements of AEC−Q101 (FGH40N65UFDTU−F085)
- These Devices are Pb−Free and are RoHS Compliant

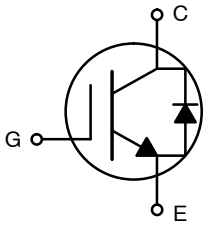
Applications

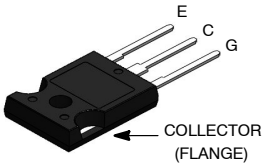
- Automotive Chargers, Converters, High Voltage Auxiliaries
- Inverters, PFC, UPS

ON Semiconductor®

www.onsemi.com

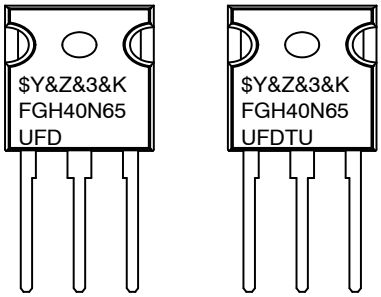
V_{CES}	I_C
650 V	40 A





TO−247−3LD
CASE 340CK

MARKING DIAGRAM



Industrial Automotive

$Y	= ON Semiconductor Logo
&Z	= Assembly Plant Code
&3	= Numeric Date Code
&K	= Lot Code
FGH40N65UFD / FGH40N65UFDTU	= Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS

Symbol	Description		Ratings	Unit
V_{CES}	Collector to Emitter Voltage		650	V
V_{GES}	Gate to Emitter Voltage		±20	V
I_C	Collector Current	T_C = 25°C	80	A
		T_C = 100°C	40	A
I_{CM} (Note 1)	Pulsed Collector Current	T_C = 25°C	120	A
P_D	Maximum Power Dissipation	T_C = 25°C	290	W
		T_C = 100°C	116	W
T_J	Operating Junction Temperature		−55 to +150	°C
T_{STG}	Storage Temperature Range		−55 to +150	°C
T_L	Maximum Lead Temp. for Soldering Purposes, 1/8” from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: Pulse width limited by max. junction temperature.

THERMAL CHARACTERISTICS

Symbol	Parameter	Typ.	Unit
$R_{\theta JC}$ (IGBT)	Thermal Resistance, Junction to Case	0.43	°C/W
$R_{\theta JC}$ (Diode)	Thermal Resistance, Junction to Case	1.45	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	40	°C/W

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Top Mark	Package	Packing Method	Qty per Tube
FGH40N65UFDTU	FGH40N65UFD	TO−247	Tube	30
FGH40N65UFDTU−F085*	FGH40N65UFDTU	TO−247	Tube	30

*Qualified to Automotive Requirements of AEC−Q101.

ELECTRICAL CHARACTERISTICS OF THE IGBT (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
OFF CHARACTERISTICS						
BV_{CES}	Collector to Emitter Breakdown Voltage	V_{GE} = 0 V, I_C = 250 μA	650	–	–	V
$\Delta BV_{CES} / \Delta T_J$	Temperature Coefficient of Breakdown Voltage	V_{GE} = 0 V, I_C = 250 μA	–	0.6	–	V/°C
I_{CES}	Collector Cut−Off Current	V_{CE} = V_{CES}, V_{GE} = 0 V	–	–	250	μA
I_{GES}	G−E Leakage Current	V_{GE} = V_{GES}, V_{CE} = 0 V	–	–	±400	nA
ON CHARACTERISTICS						
$V_{GE(th)}$	G−E Threshold Voltage	I_C = 250 μA, V_{CE} = V_{GE}	4.0	5.0	6.5	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	I_C = 40 A, V_{GE} = 15 V,	–	1.8	2.4	V
		I_C = 40 A, V_{GE} = 15 V, T_C = 125°C	–	2.0	–	V
DYNAMIC CHARACTERISTICS						
C_{ies}	Input Capacitance	V_{CE} = 30 V, V_{GE} = 0 V, f = 1 MHz	–	1860	–	pF
C_{oes}	Output Capacitance		–	200	–	pF
C_{res}	Reverse Transfer Capacitance		–	65	–	pF
SWITCHING CHARACTERISTICS						
$T_{d(on)}$	Turn−On Delay Time	V_{CC} = 400 V, I_C = 40 A, R_G = 10 Ω, V_{GE} = 15 V, Inductive Load, T_C = 25°C	–	23	–	ns
T_r	Rise Time		–	35	–	ns
$T_{d(off)}$	Turn−Off Delay Time		–	126	–	ns
T_f	Fall Time		–	26	60	ns
E_{on}	Turn−On Switching Loss		–	1.28	–	mJ
E_{off}	Turn−Off Switching Loss		–	0.50	–	mJ
E_{ts}	Total Switching Loss		–	1.78	–	mJ
$T_{d(on)}$	Turn−On Delay Time	V_{CC} = 400 V, I_C = 40 A, R_G = 10 Ω, V_{GE} = 15 V, Inductive Load, T_C = 125°C	–	21	–	ns
T_r	Rise Time		–	39	–	ns
$T_{d(off)}$	Turn−Off Delay Time		–	131	–	ns
T_f	Fall Time		–	72	–	ns
E_{on}	Turn−On Switching Loss		–	1.62	–	mJ
E_{off}	Turn−Off Switching Loss		–	0.79	–	mJ
E_{ts}	Total Switching Loss		–	2.41	–	mJ
Q_g	Total Gate Charge	V_{CE} = 400 V, I_C = 40 A, V_{GE} = 15 V	–	119	–	nC
Q_{ge}	Gate to Emitter Charge		–	14	–	nC
Q_{gc}	Gate to Collector Charge		–	64	–	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

ELECTRICAL CHARACTERISTICS OF THE DIODE (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions		Min	Typ	Max	Unit
V_{FM}	Diode Forward Voltage	I_F = 20 A	T_C = 25°C	–	1.80	2.6	V
			T_C = 125°C	–	1.71	–	
T_{rr}	Diode Reverse Recovery Time	I_F = 20 A, di_F/dt = 200 A/μs	T_C = 25°C	–	65	–	ns
			T_C = 125°C	–	215	–	
Q_{rr}	Diode Reverse Recovery Charge		T_C = 25°C	–	145	–	nC
			T_C = 125°C	–	775	–	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL PERFORMANCE CHARACTERISTICS

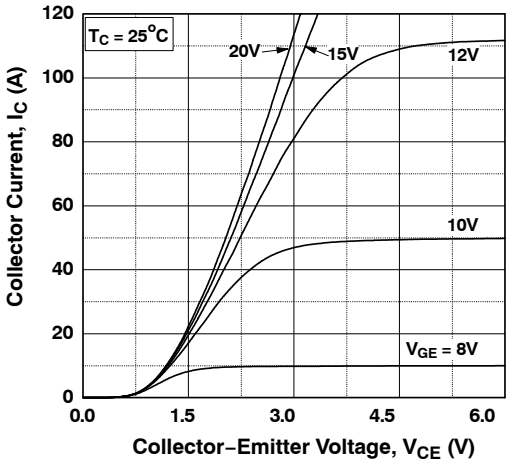


Figure 1. Typical Output Characteristics

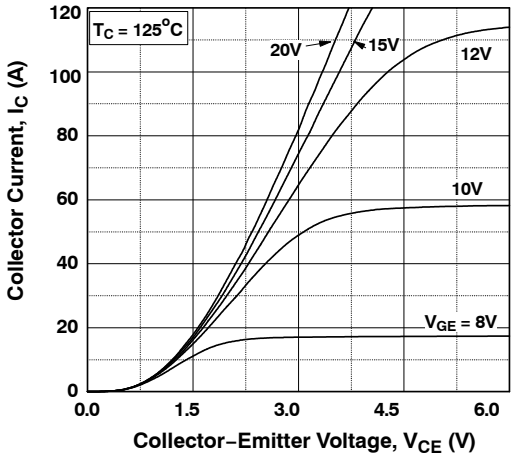


Figure 2. Typical Output Characteristics

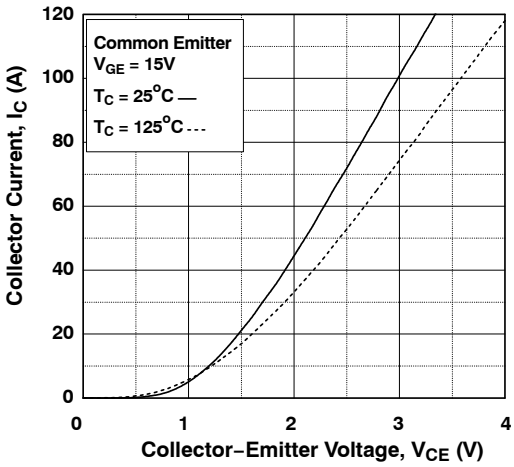


Figure 3. Typical Saturation Voltage Characteristics

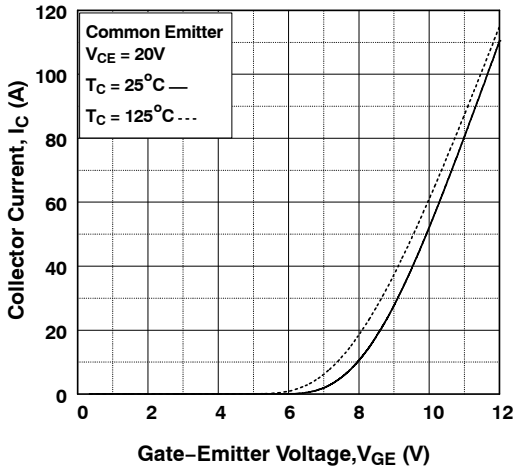


Figure 4. Transfer Characteristics

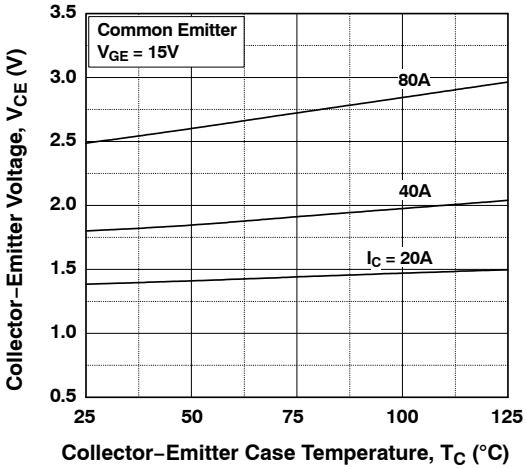


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

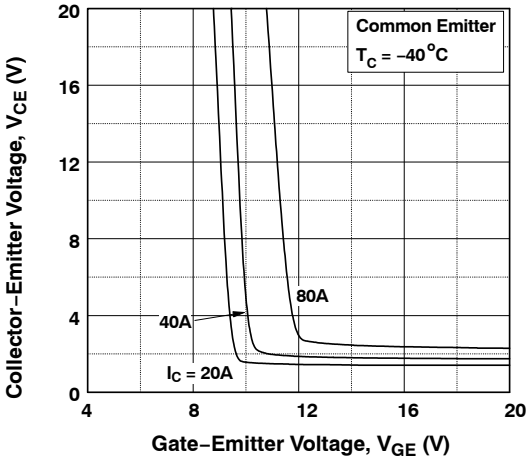


Figure 6. Saturation Voltage vs. V_{GE}

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

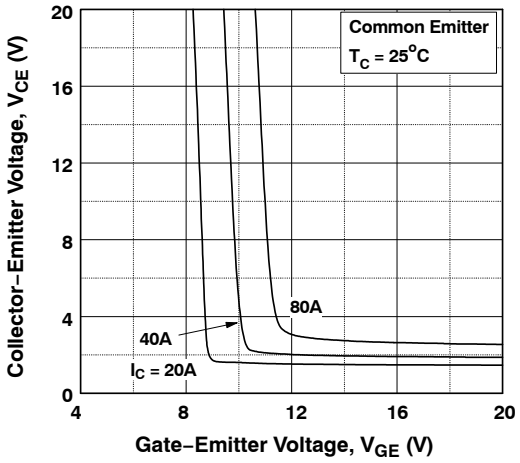


Figure 7. Saturation Voltage vs V_{GE}

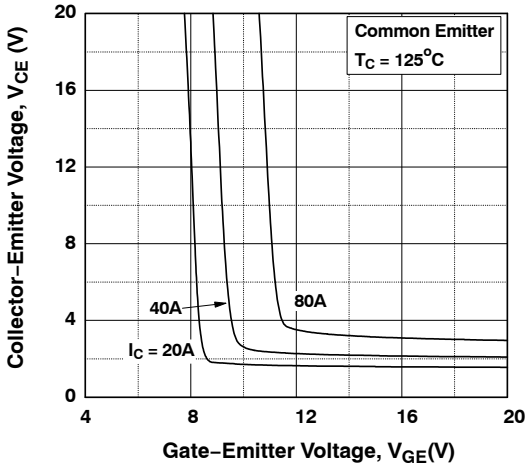


Figure 8. Saturation Voltage vs V_{GE}

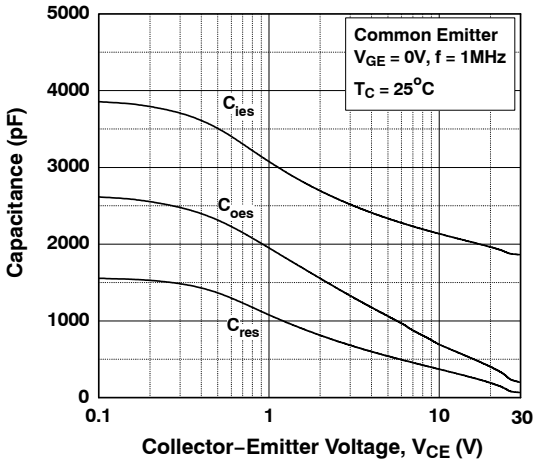


Figure 9. Capacitance Characteristics

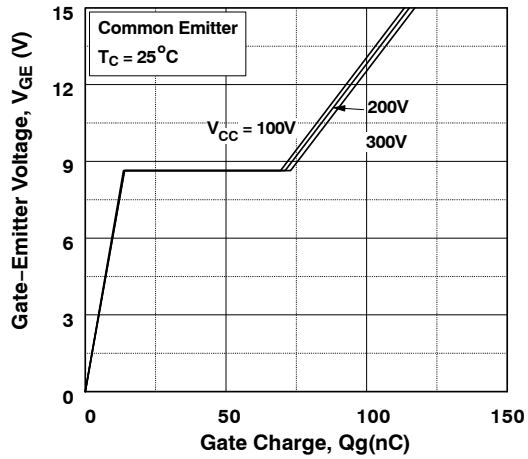


Figure 10. Gate Charge Characteristics

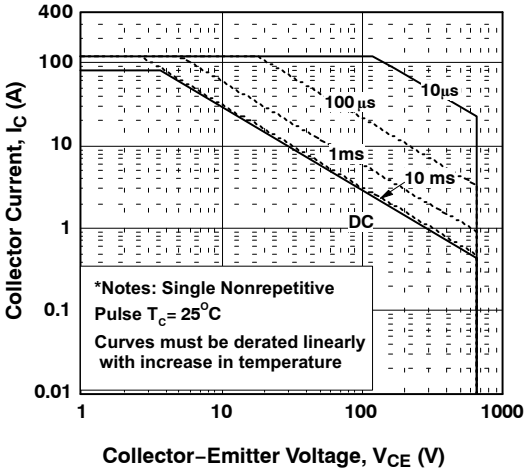


Figure 11. SOA Characteristics

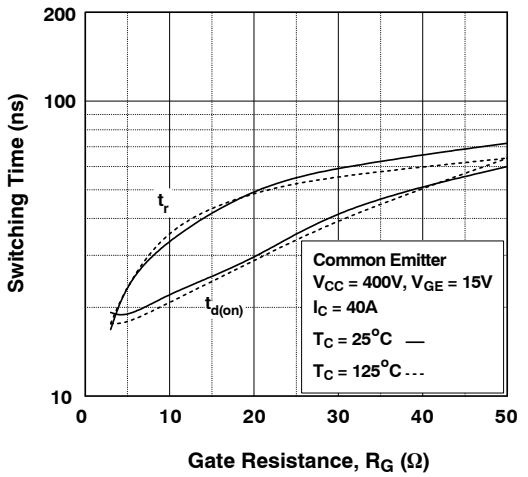


Figure 12. Turn−on Characteristics vs. Gate Resistance

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

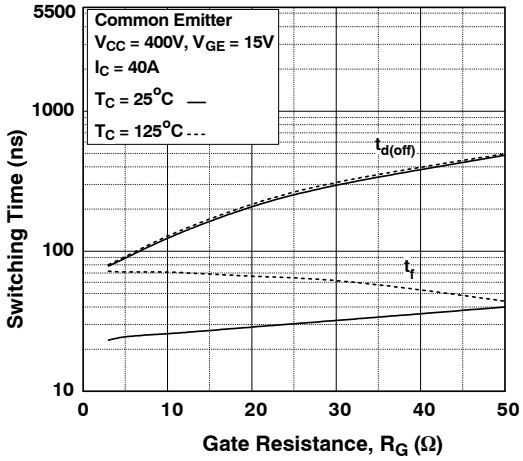


Figure 13. Turn−off Characteristics vs. Gate Resistance

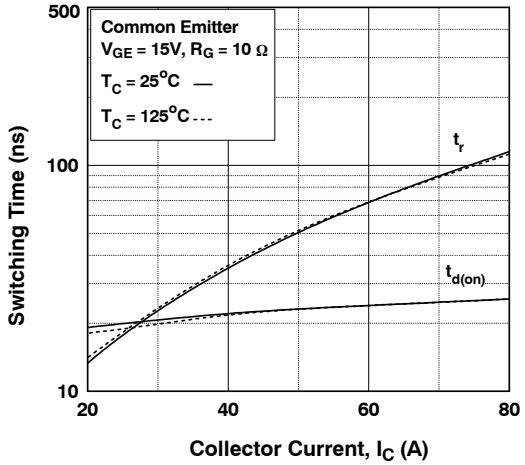


Figure 14. Turn−on Characteristics vs. Collector Current

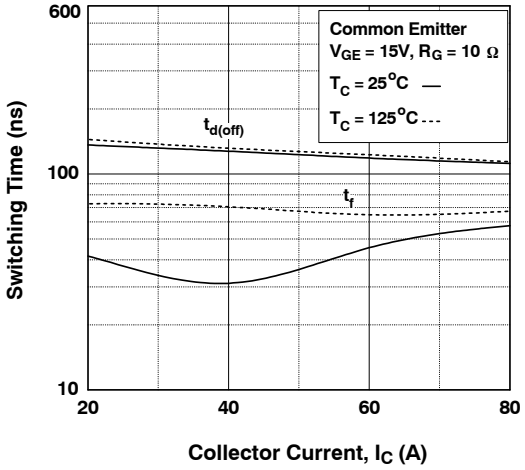


Figure 15. Turn−off Characteristics vs. Collector Current

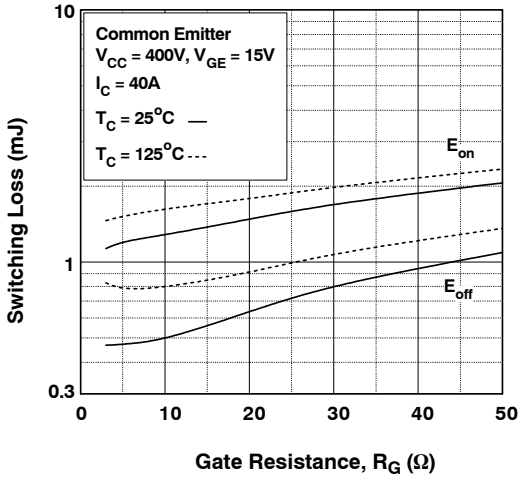


Figure 16. Switching Loss vs. Gate Resistance

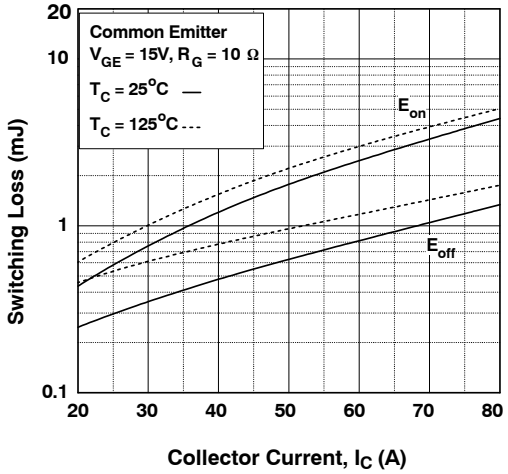


Figure 17. Switching Loss vs. Collector Current

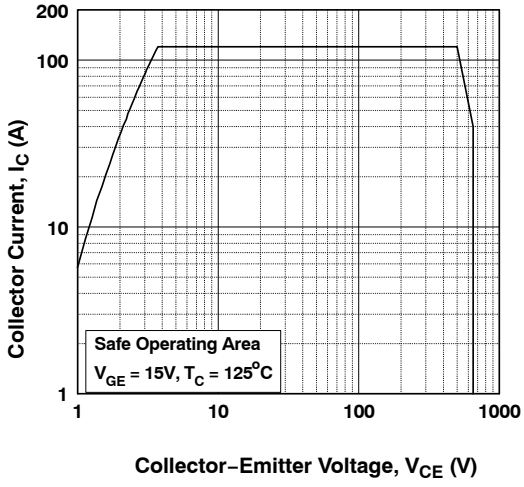


Figure 18. Turn Off Switching SOA Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

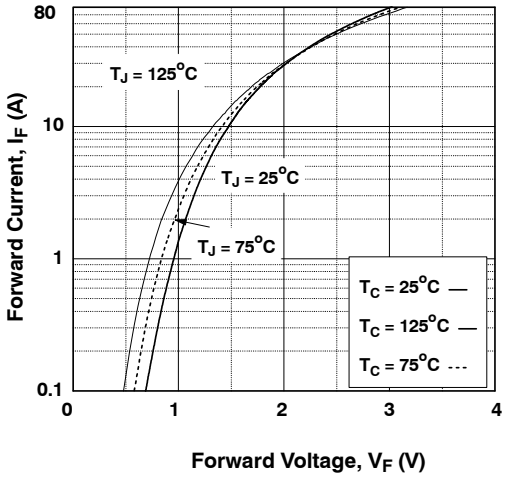


Figure 19. Forward Characteristics

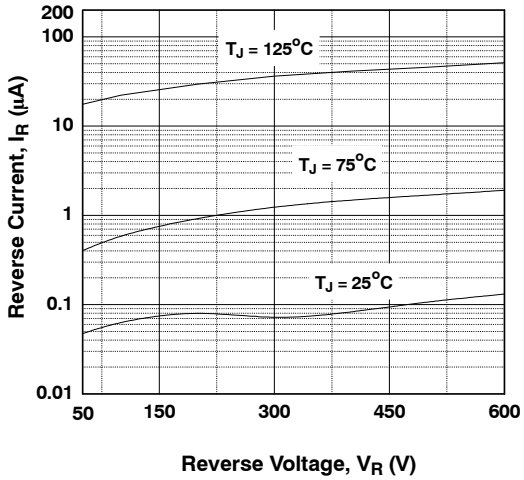


Figure 20. Reverse Current

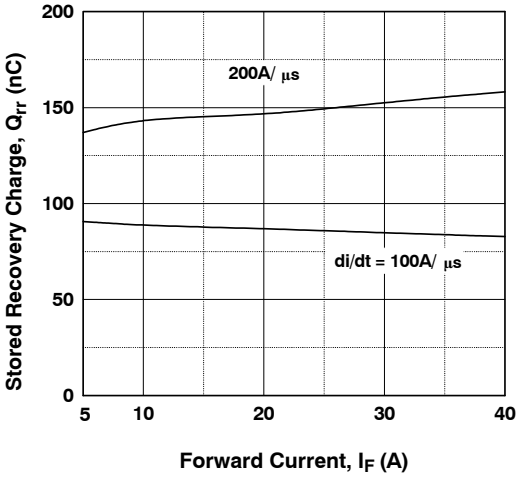


Figure 21. Stored Charge

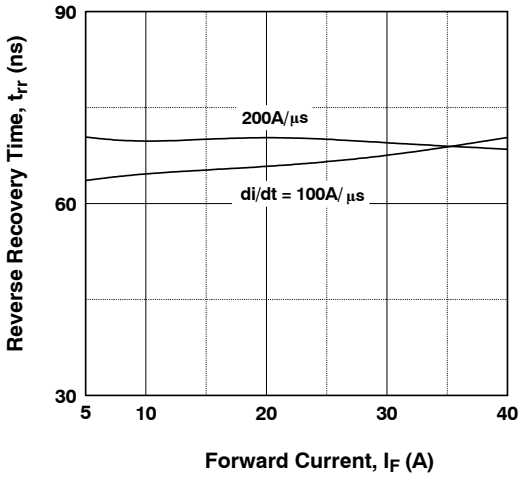


Figure 22. Reverse Recovery Time

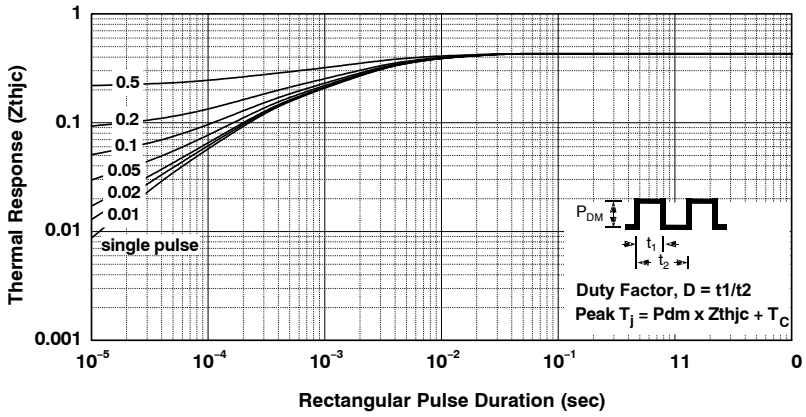


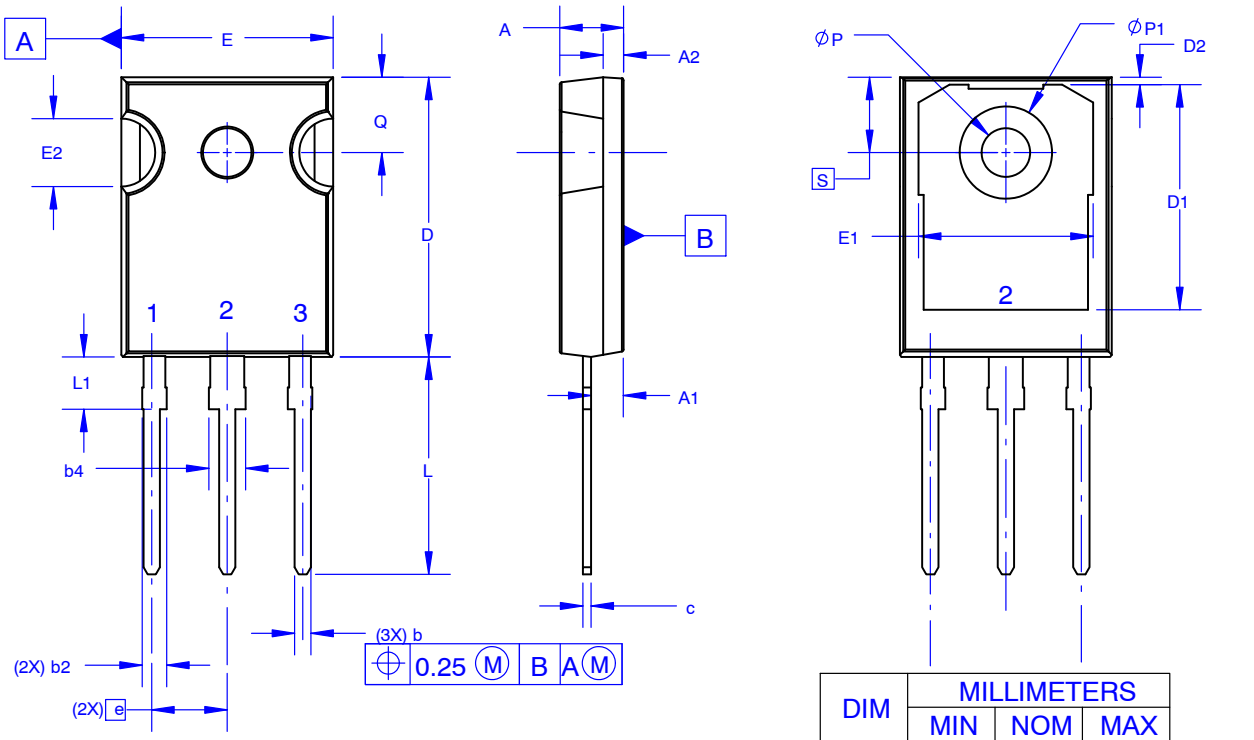
Figure 23. Transient Thermal Impedance of IGBT

MECHANICAL CASE OUTLINE
PACKAGE DIMENSIONS

ON Semiconductor®

TO−247−3LD SHORT LEAD
CASE 340CK
ISSUE A

DATE 31 JAN 2019

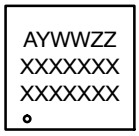


DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.58	4.70	4.82
A1	2.20	2.40	2.60
A2	1.40	1.50	1.60
b	1.17	1.26	1.35
b2	1.53	1.65	1.77
b4	2.42	2.54	2.66
c	0.51	0.61	0.71
D	20.32	20.57	20.82
D1	13.08	~	~
D2	0.51	0.93	1.35
E	15.37	15.62	15.87
E1	12.81	~	~
E2	4.96	5.08	5.20
e	~	5.56	~
L	15.75	16.00	16.25
L1	3.69	3.81	3.93
ØP	3.51	3.58	3.65
ØP1	6.60	6.80	7.00
Q	5.34	5.46	5.58
S	5.34	5.46	5.58

NOTES: UNLESS OTHERWISE SPECIFIED.

A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
B. ALL DIMENSIONS ARE IN MILLIMETERS.
C. DRAWING CONFORMS TO ASME Y14.5 - 2009.
D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC
MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb−Free indicator, “G” or microdot “▪”, may or may not be present. Some products may not follow the Generic Marking.

DOCUMENT NUMBER:	**98AON13851G**	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped “CONTROLLED COPY” in red.
DESCRIPTION:	**TO−247−3LD SHORT LEAD**	**PAGE 1 OF 1**

PUBLICATION ORDERING INFORMATION

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