# IGBT - Field Stop 600 V, 60 A

## FGH60N60SMD-F085

## Description

Using Novel Field Stop IGBT Technology, ON Semiconductor's new series of Field Stop Trench IGBTs offer the optimum performance for Automotive chargers, Solar Inverter, UPS and Digital Power Generator where low conduction and switching losses are essential.

#### **Features**

- Maximum Junction Temperature:  $T_J = 175$ °C
- Positive Temperature Co-efficient for easy Parallel Operating
- High Current Capability
- Low Saturation Voltage:  $V_{CE(sat)} = 1.8 \text{ V (Typ.)} @ I_C = 60 \text{ A}$
- High Input Impedance
- Tightened Parameter Distribution
- This Device is Pb-Free and is RoHS Compliant
- Qualified to Automotive Requirements of AEC-Q101

## **Applications**

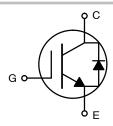
- Automotive Chargers, Converters, High Voltage Auxiliaries
- Solar Inverters, UPS, SMPS, PFC

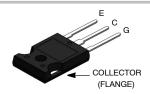


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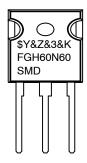
V <sub>CES</sub>	I <sub>C</sub>
600 V	60 A





TO-247-3LD CASE 340CK

### **MARKING DIAGRAM**



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code

&3 = Numeric Date Code &K = Lot Code

FGH60N60SMD = Specific Device Code

### **ORDERING INFORMATION**

See detailed ordering and shipping information on page 2 of this data sheet.

#### **ABSOLUTE MAXIMUM RATINGS**

Symbol	Description		Ratings	Unit
V <sub>CES</sub>	Collector to Emitter Voltage		600	V
V <sub>GES</sub>	Gate to Emitter Voltage		±20	V
I <sub>C</sub>	Collector Current $T_C = 25^{\circ}C$		120	Α
		T <sub>C</sub> = 100°C	60	Α
I <sub>CM</sub> (Note 1)	Pulsed Collector Current		180	Α
I <sub>F</sub>	Diode Forward Current $T_C = 25^{\circ}C$		60	Α
		T <sub>C</sub> = 100°C	30	Α
I <sub>FM</sub> (Note 1)	Pulsed Diode Maximum Forward Current		180	Α
$P_{D}$			600	W
			300	W
$T_J$	Operating Junction Temperature		-55 to +175	°C
T <sub>STG</sub>	Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temp. for Soldering Purposes, 1/8" from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

## THERMAL CHARACTERISTICS

Symbol	Parameter	Max.	Unit
R <sub>0JC</sub> (IGBT) (Note 2)	Thermal Resistance, Junction to Case	0.25	°C/W
R <sub>θJC</sub> (Diode)	Thermal Resistance, Junction to Case	1.1	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (PCB Mount) (Note 2)	45	°C/W

<sup>2.</sup> Rthjc for TO-247: according to Mil standard 883-1012 test method. Rthja for TO-247: according to JESD51-2, test method environmental condition and JESD51-10, test boards for through hole perimeter leaded package thermal measurements. JESD51-3: Low Effective Thermal Conductivity Test Board for Leaded Surface Mount Package.

## PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Package	Packing Method	Qty per Tube
FGH60N60SMD	FGH60N60SMD-F085	TO-247	Tube	30ea

<sup>1.</sup> Repetitive rating: Pulse width limited by max. junction temperature.

## **ELECTRICAL CHARACTERISTICS OF THE IGBT**

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
OFF CHARACT	FERISTICS				1	
BV <sub>CES</sub>	Collector to Emitter Breakdown Voltage	$V_{GE} = 0 \text{ V}, I_{C} = 250 \mu\text{A}$	600	-	_	٧
$\Delta BV_{CES} / \Delta T_{J}$	Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0 \text{ V}, I_{C} = 250 \mu\text{A}$	-	0.22	_	V/°C
I <sub>CES</sub>	Collector Cut-Off Current	V <sub>CE</sub> = V <sub>CES</sub> , V <sub>GE</sub> = 0 V	-	-	250	μΑ
		I <sub>CES</sub> at 80 % *BVCES, 175 °C	_	-	1100	
I <sub>GES</sub>	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0 V$	-	-	±400	nA
N CHARACTI	ERISTICS			•	•	•
V <sub>GE(th)</sub>	G-E Threshold Voltage	$I_C = 250 \mu A, V_{CE} = V_{GE}$	3.5	4.7	6.0	V
V <sub>CE(sat)</sub>	Collector to Emitter Saturation Voltage	I <sub>C</sub> = 60 A, V <sub>GE</sub> = 15 V,	-	1.8	2.5	٧
		I <sub>C</sub> = 60 A, V <sub>GE</sub> = 15 V, T <sub>C</sub> = 175°C	-	2.14	-	٧
YNAMIC CHA	RACTERISTICS			•		
C <sub>ies</sub>	Input Capacitance	V <sub>CE</sub> = 30 V, V <sub>GE</sub> = 0 V,	_	2780	3700	pF
C <sub>oes</sub>	Output Capacitance	<b>–</b> f = 1 MHz	-	260	345	pF
C <sub>res</sub>	Reverse Transfer Capacitance	1	-	80	110	pF
WITCHING CI	HARACTERISTICS			•	•	-
T <sub>d(on)</sub>	Turn-On Delay Time	$V_{CC}$ = 400 V, $I_{C}$ = 60 A, $R_{G}$ = 3 $\Omega$ , $V_{GE}$ = 15 V, Inductive Load, $T_{C}$ = 25°C	-	22	29	ns
T <sub>r</sub>	Rise Time		-	46	60	ns
T <sub>d(off)</sub>	Turn-Off Delay Time		-	116	151	ns
T <sub>f</sub>	Fall Time		-	14	18	ns
E <sub>on</sub>	Turn-On Switching Loss	7	-	1.59	2.23	mJ
E <sub>off</sub>	Turn-Off Switching Loss	7	-	0.39	0.55	mJ
E <sub>ts</sub>	Total Switching Loss	7	-	1.98	2.78	mJ
T <sub>d(on)</sub>	Turn-On Delay Time	$V_{CC}$ = 400 V, $I_{C}$ = 60 A, $R_{G}$ = 3 $\Omega$ , $V_{GE}$ = 15 V,	-	22	28	ns
T <sub>r</sub>	Rise Time	Inductive Load, $T_C = 175^{\circ}C$	-	44	58	ns
T <sub>d(off)</sub>	Turn-Off Delay Time	1	-	124	161	ns
T <sub>f</sub>	Fall Time	1	-	15	20	ns
E <sub>on</sub>	Turn-On Switching Loss	1	-	2.41	3.13	mJ
E <sub>off</sub>	Turn-Off Switching Loss	7	-	1.08	1.42	mJ
E <sub>ts</sub>	Total Switching Loss	7	-	3.49	4.55	mJ
Qg	Total Gate Charge	V <sub>CE</sub> = 400 V, I <sub>C</sub> = 60 A,	-	187	280	nC
Q <sub>ge</sub>	Gate to Emitter Charge	V <sub>GE</sub> = 15 V	-	20	29	nC
Q <sub>gc</sub>	Gate to Collector Charge	1	-	92	138	nC
	•					

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## **ELECTRICAL CHARACTERISTICS OF THE DIODE** (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions		Min	Тур	Max	Unit
$V_{FM}$	Diode Forward Voltage	I <sub>F</sub> = 30 A	T <sub>C</sub> = 25°C	-	2.1	2.7	V
			T <sub>C</sub> = 175°C	-	1.48	-	
T <sub>rr</sub>	Diode Reverse Recovery Time	I <sub>F</sub> = 30 A, dI <sub>F</sub> /dt = 200 A/μs	T <sub>C</sub> = 25°C	_	33	42	ns
		αι <sub>Ε</sub> /αι – 200 Αγμ3	T <sub>C</sub> = 175°C	-	115	-	
Q <sub>rr</sub>	Diode Reverse Recovery Charge	1	T <sub>C</sub> = 25°C	_	53	69	nC
			T <sub>C</sub> = 175°C	-	606	-	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

## TYPICAL PERFORMANCE CHARACTERISTICS

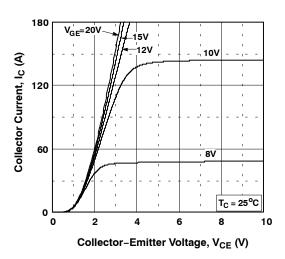


Figure 1. Typical Output Characteristics

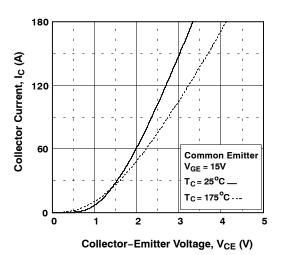
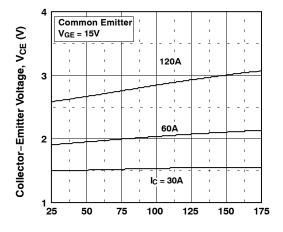


Figure 3. Typical Saturation Voltage Characteristics



Collector–Emitter Case Temperature,  $T_C$  (°C)

Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

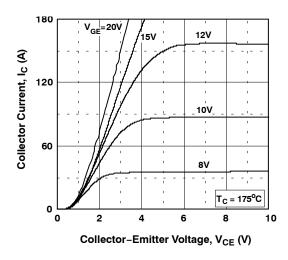


Figure 2. Typical Output Characteristics

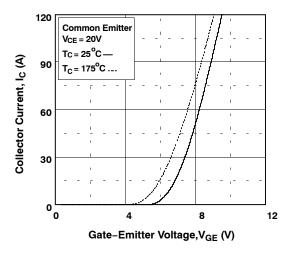


Figure 4. Transfer Characteristics

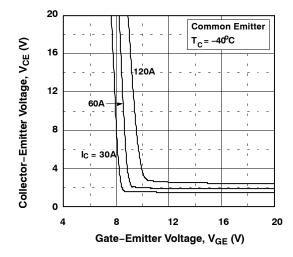


Figure 6. Saturation Voltage vs. V<sub>GE</sub>

## TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

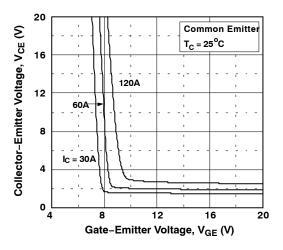


Figure 7. Saturation Voltage vs. V<sub>GE</sub>

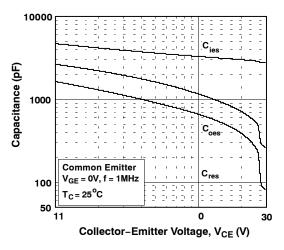


Figure 9. Capacitance Characteristics

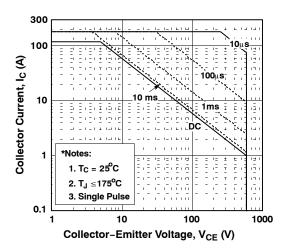


Figure 11. SOA Characteristics

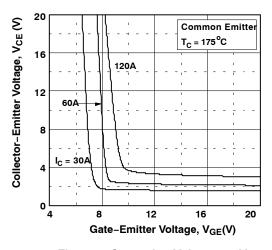


Figure 8. Saturation Voltage vs. V<sub>GE</sub>

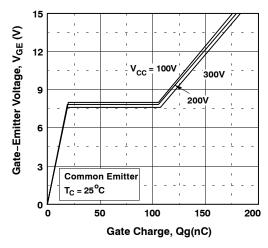


Figure 10. Gate Charge Characteristics

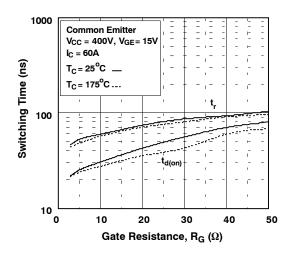


Figure 12. Turn-on Characteristics vs. Gate Resistance

## TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

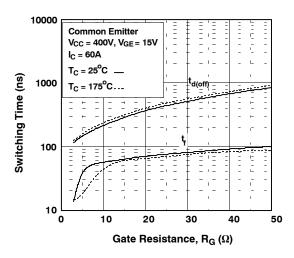


Figure 13. Turn-off Characteristics vs. Gate Resistance

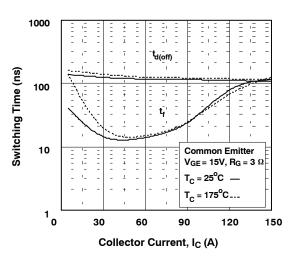


Figure 15. Turn-off Characteristics vs. Collector Current

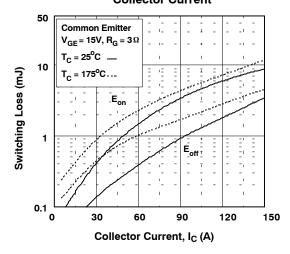


Figure 17. Switching Loss vs. Collector Current

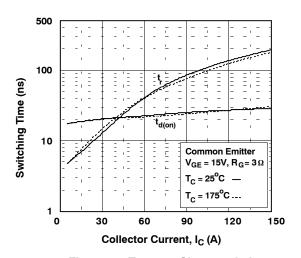


Figure 14. Turn-on Characteristics vs. Collector Current

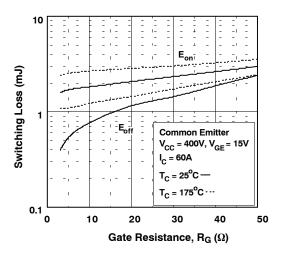


Figure 16. Switching Loss vs.
Gate Resistance

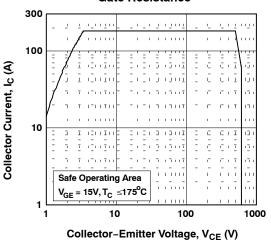


Figure 18. Turn Off Switching SOA Characteristics

## TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

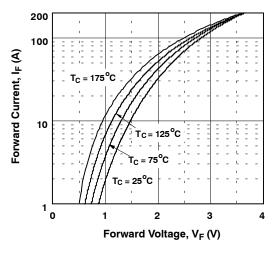


Figure 19. Forward Characteristics

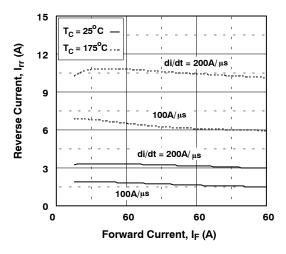


Figure 20. Reverse Recovery Current

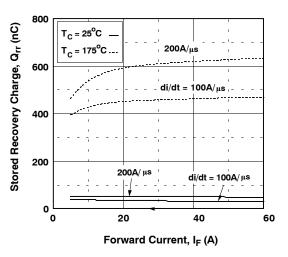


Figure 21. Stored Charge

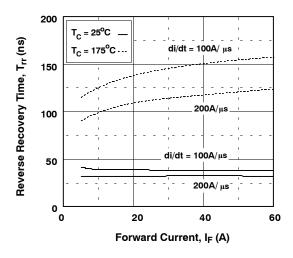


Figure 22. Reverse Recovery Time

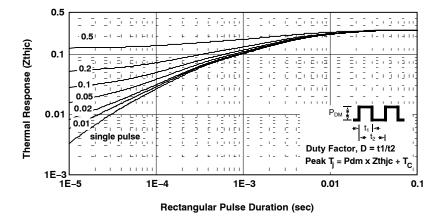
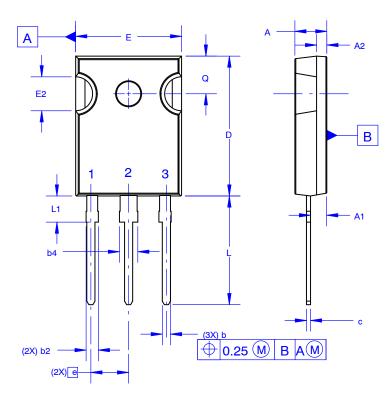


Figure 23. Transient Thermal Impedance of IGBT

## TO-247-3LD SHORT LEAD

CASE 340CK ISSUE A



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

# GENERIC MARKING DIAGRAM\*



XXXX = Specific Device Code

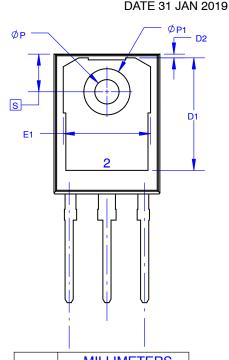
A = Assembly Location

Y = Year

WW = Work Week

ZZ = Assembly Lot Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



DIM	MILLIMETERS				
DIIVI	MIN	NOM	MAX		
Α	4.58	4.70	4.82		
A1	2.20	2.40	2.60		
A2	1.40	1.50	1.60		
b	1.17	1.26	1.35		
b2	1.53	1.65	1.77		
b4	2.42	2.54	2.66		
С	0.51	0.61	0.71		
D	20.32	20.57	20.82		
D1	13.08	~	~		
D2	0.51	0.93	1.35		
E	15.37	15.62	15.87		
E1	12.81	~	~		
E2	4.96	5.08	5.20		
е	~	5.56	~		
L	15.75	16.00	16.25		
L1	3.69	3.81	3.93		
ØΡ	3.51	3.58	3.65		
ØP1	6.60	6.80	7.00		
Q	5.34	5.46	5.58		
S	5.34	5.46	5.58		

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DESCRIPTION:	TO-247-3LD SHORT LEAD		PAGE 1 OF 1	

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 GT50JR22(STA1ES)
 TIG058E8-TL-H
 VS-CPV364M4KPBF
 NGTB25N120FL2WAG
 NGTG40N120FL2WG
 RJH60F3DPQ-A0#T0

 APT40GR120B2SCD10
 APT15GT120BRG
 APT20GT60BRG
 NGTB75N65FL2WAG
 NGTG15N120FL2WG
 IXA30RG1200DHGLB

 IXA40RG1200DHGLB
 APT70GR65B2DU40
 NTE3320
 IHFW40N65R5SXKSA1
 APT70GR120J
 APT35GP120JDQ2

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 IKFW40N65ES5XKSA1

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 XD15H120CX1
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 FGH60N60SMD\_F085

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 RJH60D2DPP-M0#T2
 IKP20N60TXKSA1

 IHW20N65R5XKSA1
 IDW40E65D2FKSA1