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October 2008

# **FJA4310 NPN Epitaxial Silicon Transistor**

- · Audio Power Amplifier
- High Current Capability : I<sub>C</sub>=10A
- High Power Dissipation
- Wide S.O.A
- Complement to FJA4210



# Absolute Maximum Ratings\* T<sub>a</sub> = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units
V <sub>CBO</sub>	Collector-Base Voltage	200	V
V <sub>CEO</sub>	Collector-Emitter Voltage	140	V
V <sub>EBO</sub>	Emitter-Base Voltage	6	V
I <sub>C</sub>	Collector Current (DC)	10	Α
I <sub>B</sub>	Base Current (DC)	1.5	Α
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	100	W
T <sub>J</sub>	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 55 ~ 150	°C

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

## Electrical Characteristics\* Ta=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C$ =5mA, $I_E$ =0	200			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> =50mA, R <sub>BE</sub> =∞	140			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	$I_E$ =5mA, $I_C$ =0	6			V
I <sub>CBO</sub>	Collector Cut-off Current	V <sub>CB</sub> =200V, I <sub>E</sub> =0			10	μА
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB}$ =6 $V$ , $I_{C}$ =0			10	μА
h <sub>FE</sub>	* DC Current Gain	$V_{CE}$ =4V, $I_{C}$ =3A	50		180	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> =5A, I <sub>B</sub> =0.5A			0.5	V
C <sub>ob</sub>	Output Capacitance	V <sub>CB</sub> =10V, f=1MHz		250		pF
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> =5V, I <sub>C</sub> =1A		30		MHz

<sup>\*</sup> Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

# **h**<sub>FE</sub> Classification

Classification	R	0	Y
h <sub>FE</sub>	50 ~ 100	70 ~ 140	90 ~ 180

# **Typical Characteristics**

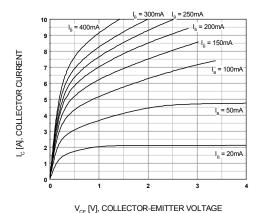


Figure 1. Static Characterstic

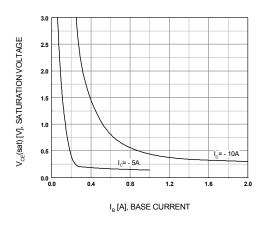


Figure 3.  $V_{CE}(sat)$  vs.  $I_B$  Characteristics

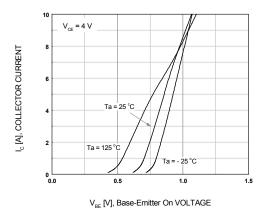


Figure 5. Base-Emitter On Voltage

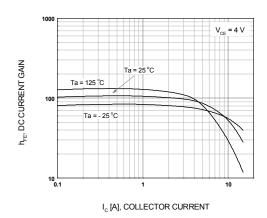


Figure 2. DC current Gain

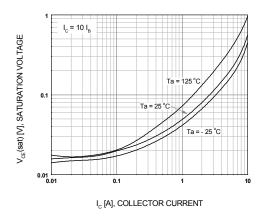
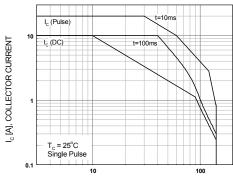


Figure 4. Collector-Emitter Saturation Voltage



 $V_{\text{CE}}$  [V], COLLECTOR-EMITTER VOLTAGE

Figure 6. Forward Bias Safe Operating Area

# Typical Characteristics (Continued)

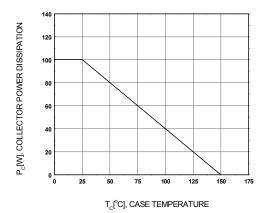
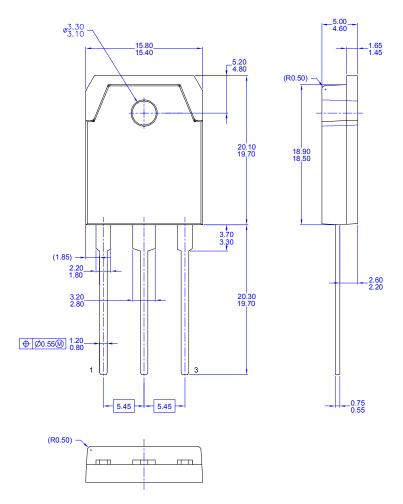


Figure 7. Power Derating

# Package Dimension (TO-3P)



- NOTES:
  A) THIS PACKAGE CONFORMS TO EIAJ
  SC-65 PACKAGING STANDARD.
  B) ALL DIMENSIONS ARE IN MILLIMETERS.
  C) DIMENSIONING AND TOLERANCING PER
- ASME14.5 1973.

  D) DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

  E) DRAWING FILE NAME: TO3P03AREV2.





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