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June 2013

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DPAK

1. Base 2. Collector 3. Emitter

Marking : J5553



FJD5553 NPN Silicon Transistor

Features

- Fast Speed Switching
- Wide Safe Operating Area
- High Voltage Capability

Application

- Electronic Ballast
- Switch Mode Power Supplies

Ordering Information

Part Number	Marking	Package	Packing Method		
FJD5553TM	J5553	D-PAK	Tape and Reel		

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Units	
BV _{CBO}	Collector-Base Voltage	1050	V	
BV _{CEO}	Collector-Emitter Voltage	400	V	
BV _{EBO}	Emitter-Base Voltage	14	V	
I _C	Collector Current (DC)	3	A	
I _{CP}	Collector Current (Pulse)	6	A	
I _B	Base Current (DC)	1	Α	
I _{BP}	Base Current (Pulse)	2	Α	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Junction Temperature Range	- 55 to +150	°C	

Thermal Characteristics

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter		Value	Units
PD	Total Device Dissipation	$T_A = 25^{\circ}C$	1.25	W
$R_{\theta ja}^{(1)}$	Thermal Resistance, Junction to Ambient		100	°C/W

Note:

1. $R_{\theta ja}$ test board and fixture under natural convection; JESD51-3 recommended thermal test board.

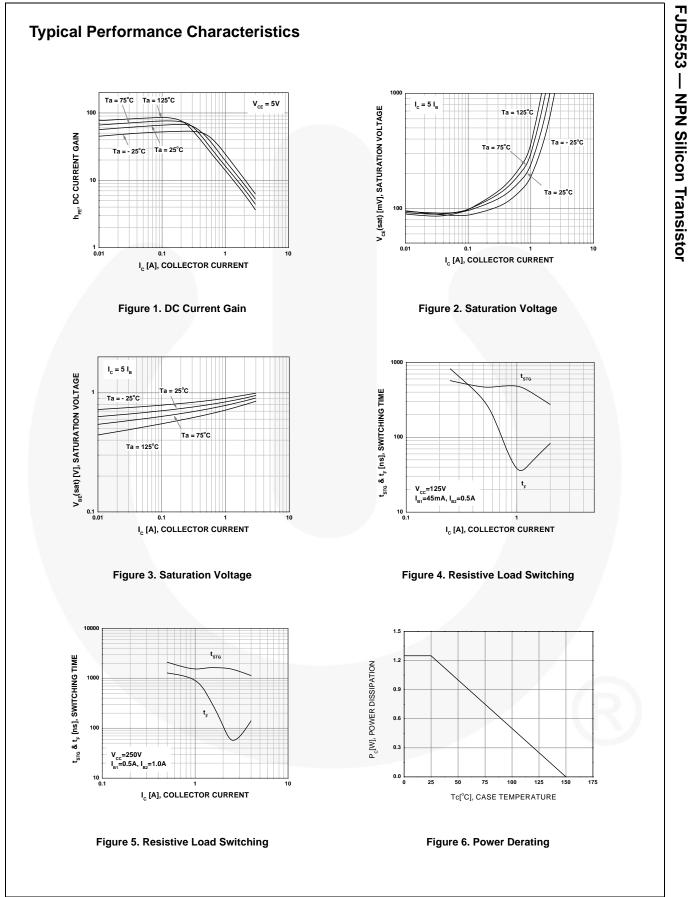
Electrical Characteristics⁽²⁾

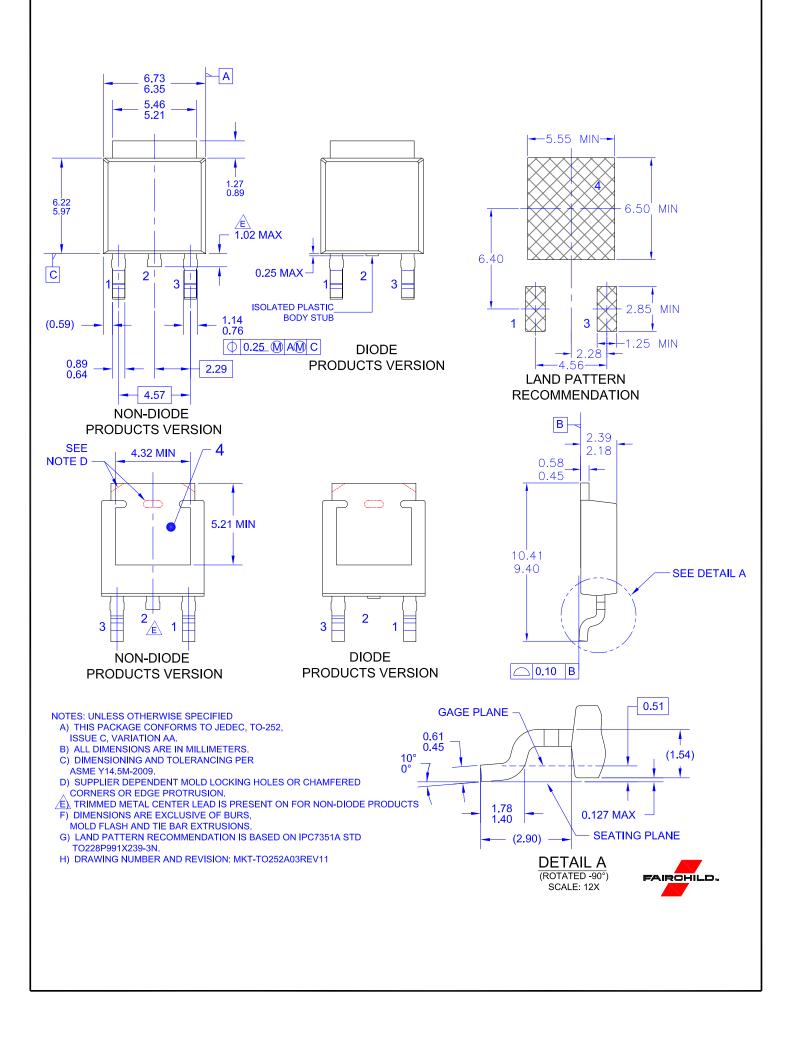
Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Тур.	Max	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_{C} = 500 \ \mu A, \ I_{E} = 0$	1050			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_{C} = 5 \text{ mA}, I_{B} = 0$	400			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_{E} = 500 \ \mu A, \ I_{C} = 0$	14			V
h _{FE}	DC Current Gain	$V_{CE} = 5 \text{ V}, \text{ I}_{C} = 10 \text{ mA}$	10			
		$V_{CE} = 3 \text{ V}, \text{ I}_{C} = 0.4 \text{ A}$	30		60	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = 1 A, I _B = 0.2 A		0.23	0.50	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = 1 A, I _B = 0.2 A			1.2	V
C _{ob}	Output Capacitance	V _{CB} = 10 V, f = 1 MHz		45		pF
t _{ON}	Turn-On Time	V_{CC} = 125 V, I _C = 0.5 A, I _{B1} = 45 mA, I _{B2} = 0.5 A, R _L = 250 Ω			1.0	μs
t _{STG}	Storage Time				1.2	μs
t _F	Fall Time			0.3		μs
t _{ON}	Turn-On Time	V_{CC} = 250 V, I _C = 2.5 A, I _{B1} = 0.5 A, I _{B2} = 1.0 A, R _L = 100 Ω			2.0	μs
t _{STG}	Storage Time				2.5	μs
t _F	Fall Time				0.3	μs
EAS	Avalanche Energy	L = 2 mH	3.5			mJ

Note:

2. Pulse test: pulse width \leq 300 µs, duty cycle \leq 2%.





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