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July 2008

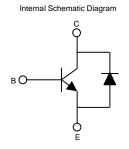
FJP3307D

High Voltage Fast Switching NPN Power Transistor

Features

- Built-in Diode between Collector and Emitter
- Suitable for Electronic Ballast and Switch Mode Power Supplies





Absolute Maximum Ratings

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	700	V
V _{CEO}	Collector-Emitter Voltage	400	V
V _{EBO}	Emitter-Base Voltage	9	V
I _C	Collector Current (DC)	8	А
I _{CP}	* Collector Current (Pulse)	16	А
I _B	Base Current (DC)	4	A
P _C	Collector Dissipation (T _C = 25°C)	80	W
T _J	Junction Temperature	150	°C
T _{STG}	Storage Temperature	-55 ~ 150	°C

^{*} Pulse Test: PW = 300ms, Duty Cycle = 2% Pulsed

Electrical Characteristics $T_C = 25$ °C unless otherwise noted

Symbol	Parameter	Conditions	Min.	Тур.	Max	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = 500\mu A, I_E = 0$	700			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 5mA, I_B = 0$	400			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 500 \mu A, I_C = 0$	9			V
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 9V, I_{C} = 0$			1	mA
h _{FE1} h _{FE2}	DC Current Gain	V _{CE} = 5V, I _C = 2A V _{CE} = 5V, I _C = 5A	8 5		40 30	
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$I_C = 2A, I_B = 0.4A$			1	V
		I _C = 5A, I _B = 1A			2	V
		$I_C = 8A, I_B = 2A$			3	V

1

Symbol	Parameter	Conditions	Min.	Тур.	Max	Units
V _{BE(sat)}	Base-Emitter Saturation Voltage	$I_C = 2A, I_B = 0.4A$			1.2	V
		I _C = 5A, I _B = 1A			1.6	V
V _F	Diode Forward Voltage	I _C = 3A			2.5	V
C _{ob}	Output Capatitance	$V_{CB} = 10V, I_{E} = 0, f = 1MHz$		60		pF
t _{STG}	Storage Time	$V_{CC} = 125V, I_C = 5A$ $I_{B1} = -I_{B2} = 1A, R_L = 50\Omega$			3	μS
t _F	Fall Time				0.7	μS
t _{STG}	Storage Time	$V_{CC} = 30V$, $I_C = 5A$, $L=200\mu H$ $I_{B1}=1A$, $R_{BB} = 0\Omega$, $V_{BE(OFF)}=-5V$ $V_{CLAMP} = 250V$			2.3	μS
t _F	Fall Time				150	ns

^{*} Pulse test: PW = 300 $\mu s,\ Duty\ cycl\ e=2\%$

hFE Classification

Classification	H1	H2
h _{FE1}	15 ~ 28	26 ~ 39

Typical Characteristics

Figure 1. Static Characterstic

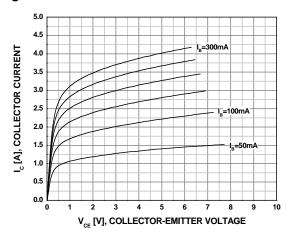


Figure 2. DC Current Gain (H1 Grade)

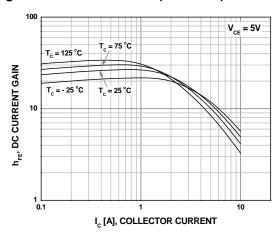


Figure 3. DC Current Gain (H2 Grade)

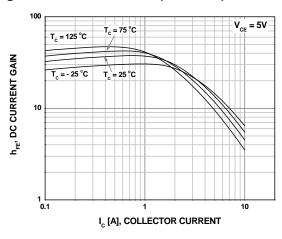


Figure 4. Collector-Emitter Saturation Voltage

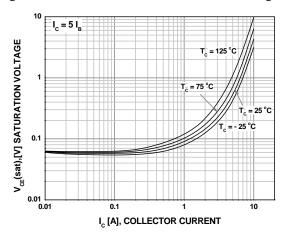


Figure 5. Base-Emitter Saturation Voltage

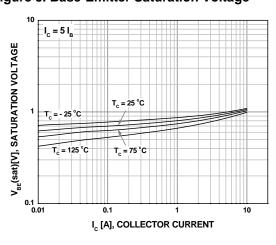
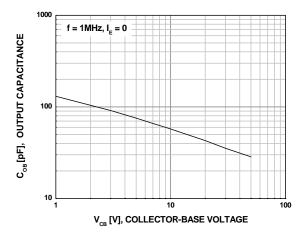


Figure 6. Output Capacitance



Typical Characteristics (Continued)

Figure 7. Power Derating

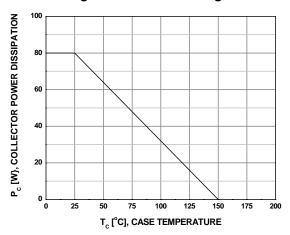


Figure 8. Reverse Biased Safe Operating Area

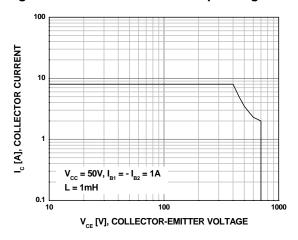
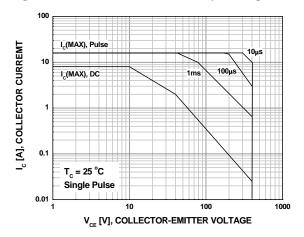
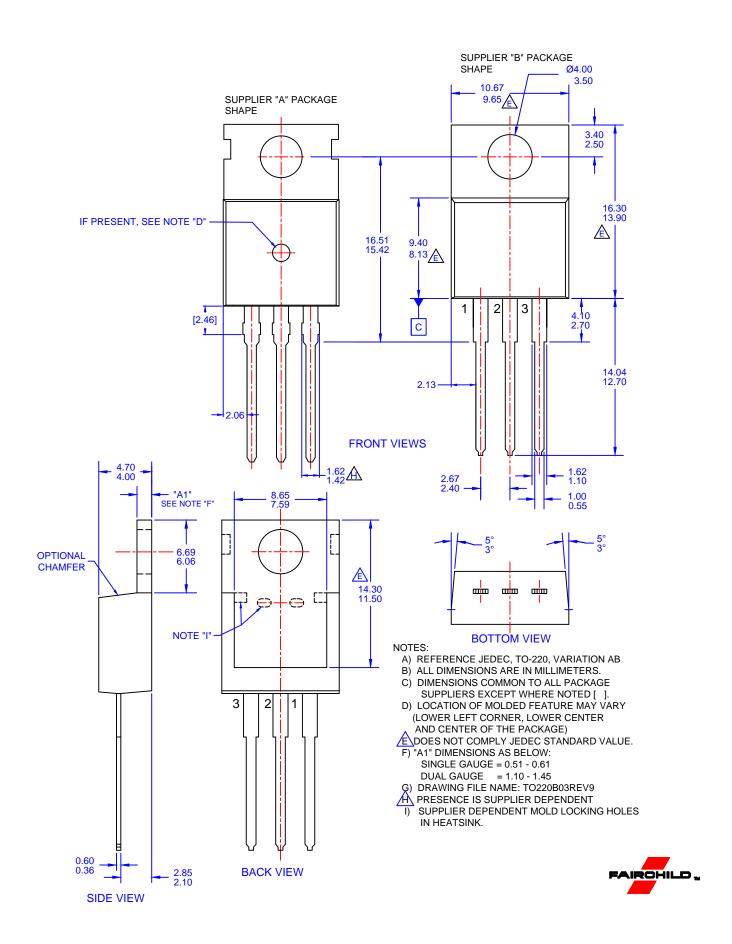


Figure 9. Forward Biased Safe Operating Area





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