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# FFB2227A / FMB2227A **NPN & PNP General-Purpose Amplifier**

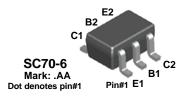
## **Description**

This complementary device is a medium-power amplifier and switch, requiring collector currents up to 500 mA. Sourced from Process 19 and 63. See FFB2222A (NPN) and FFB2907A (PNP) for characteristics.

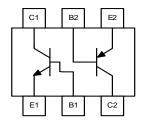
#### **Ordering Information**

Part Number	Top Mark	Package	Packing Method
FFB2227A	AA	SC70 6L	Tape and Reel
FMB2227A	001	SSOT 6L	Tape and Reel

## **Block Diagram**



FFB2227A Device Package



FFR2227∆	Internal C	onnoction

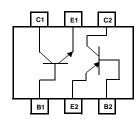
TRANSISTOR TYPE C1 B1 E1 C2 B2 E2

FMB2227A Device Package

SuperSOT™-6

Mark: .001

Dot denotes pin#1



**FMB2227A Internal Connection** 

1

#### **Absolute Maximum Ratings**(1)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Units
V <sub>CEO</sub>	Collector-Emitter Voltage	30	V
V <sub>CBO</sub>	Collector-Base Voltage	60	V
V <sub>EBO</sub>	Emitter-Base Voltage	5	V
I <sub>C</sub>	Collector Current - Continuous	500	mA
T <sub>J,</sub> T <sub>STG</sub>	Operating and Storage Junction Temperature Range	- 55 to +150	°C

#### Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. ON Semiconductor should be consulted on applications involving pulsed or lowduty cycle operations.
- 3. All voltages (V) and currents (A) are negative polarity for PNP transistors.
- 4. These Ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

#### Thermal Characteristics(2)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Maxi	Units	
		FFB2227A	FMB2227A	Office
P <sub>D</sub>	Total Device Dissipation	300	700	mV
	Derate Above 25°C	2.4	5.6	mV/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	415	180	°C/W

#### Note:

2. PCB board size: FR-4 76 x 114 x 0.6T mm<sup>3</sup>(3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

## Electrical Characteristics(3)

Values are at  $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Units
OFF CHAF	RACTERISTICS					
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage <sup>(4)</sup>	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	30			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = 10 μA, I <sub>E</sub> = 0	60			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	$I_E = 10 \mu A, I_C = 0$	5			V
I <sub>CBO</sub>	Collector Cut-Off Current	V <sub>CB</sub> = 50 V, I <sub>E</sub> = 0			30	nA
I <sub>EBO</sub>	Emitter Cut-Off Current	V <sub>EB</sub> = 3.0 V, I <sub>C</sub> = 0			30	nA
ON CHAR	ACTERISTICS					
	DC Current Gain	I <sub>C</sub> = 1.0 mA, V <sub>CE</sub> = 10 V	50			
h		I <sub>C</sub> = 10 mA, V <sub>CE</sub> = 10 V	75			
h <sub>FE</sub>		I <sub>C</sub> = 150 mA, V <sub>CE</sub> = 10 V <sup>(4)</sup>	100			
		$I_C$ = 300 mA, $V_{CE}$ = 10 $V^{(4)}$	30			
\/ (aat)	Collector-Emitter Saturation Voltage <sup>(4)</sup>	I <sub>C</sub> = 150 mA, I <sub>B</sub> = 15 mA			0.4	V
V <sub>CE</sub> (sat)		I <sub>C</sub> = 300 mA, I <sub>B</sub> = 30 mA			1.4	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage <sup>(4)</sup>	I <sub>C</sub> = 150 mA, I <sub>B</sub> = 15 mA			1.3	V
SMALL SIG	SNAL CHARACTERISTICS					
f <sub>T</sub>	Current Gain - Bandwidth Product	I <sub>C</sub> = 50 mA, V <sub>CE</sub> = 20 V, f = 100 MHz		250		MHz
C <sub>obo</sub>	Output Capacitance	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 100 kHz		4.0		pF
C <sub>ibo</sub>	Input Capacitance	$V_{EB} = 2.0 \text{ V}, I_C = 0, f = 100 \text{ kHz}$		12		pF
NF	Noise Figure	$I_C$ = 100 μA, $V_{CE}$ = 10 V, $R_S$ = 1.0 kΩ, f = 1.0 kHz		2.0		dB
SWITCHIN	G CHARACTERISTICS			•		
t <sub>on</sub>	Turn-on Time			30		ns
t <sub>d</sub>	Delay Time	$V_{CC} = 30 \text{ V, I}_{C} = 150 \text{ mA,}$ $I_{B1} = 15 \text{ mA}$		8.0		ns
t <sub>r</sub>	Rise Time			20		ns
t <sub>off</sub>	Turn-off Time	$V_{CC} = 6.0 \text{ V}, I_{C} = 150 \text{ mA},$ $I_{B1} = I_{B2} = 15 \text{ mA}$		80		ns
t <sub>s</sub>	Storage Time			60		ns
t <sub>f</sub>	Fall Time	ום: ושל וסיויי.		20		ns

- 3. All voltages (V) and currents (A) are negative polarity for PNP transistors.
- 4. Pulse test: pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2.0%.

## **Physical Dimensions**

# SC70 6L

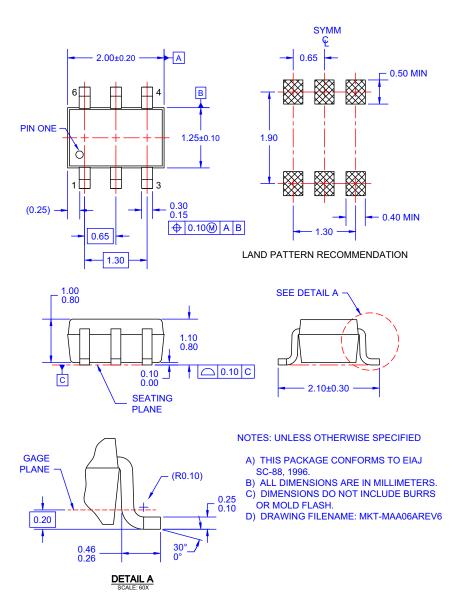


Figure 2. 6-LEAD, SC70, EIAJ SC-88, 1.25 MM WIDE (ACTIVE)

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## Physical Dimensions (Continued)

# SSOT 6L

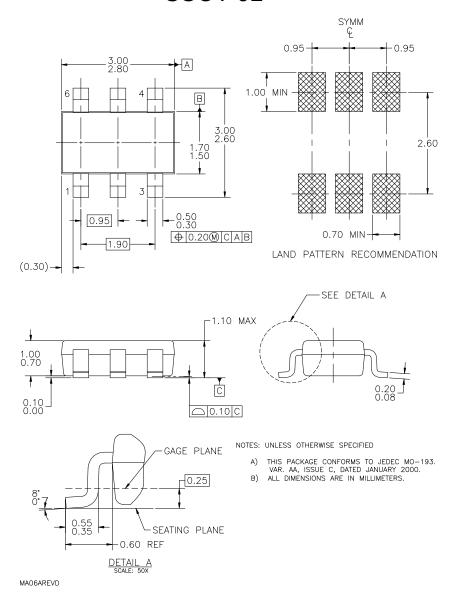


Figure 3. 6-LEAD, SUPER SOT-6, JEDEC MO-193, 1.6 MM WIDE (ACTIVE)

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