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April 2011

FMBS2383 NPN Epitaxial Silicon Transistor

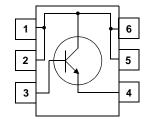
Features

• Power Amplifier

• Collector-Emitter Voltage : V_{CEO}=160V

• Current Gain Bandwidth Product : f_T=120MHz





Absolute Maximum Ratings $T_a = 25$ °C unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CBO}	Collector-Base Voltage	160	V	
V _{CEO}	Collector-Emitter Voltage	160	60 V	
V _{EBO}	Emitter-Base Voltage	5 V		
I _C	Collector Current	800	mA	
I _B	Base Current	160	mA	
P _D	Power Dissipation	630	mW	
R _{θJA} *	Thermal Resistance, Junction to Ambient	200	°C/W	
T _J	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	-55 to +150	°C	

^{*} note1): Minimum land pattern size

Electrical Characteristics $T_a = 25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV _{CBO}	Collector-Base Breakdown Voltage	$I_C = 10\mu A, I_B = 0$	160			V
BV _{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 10 \text{mA}, I_B = 0$	160			V
BV _{EBO}	Emitter-Base Breakdown Voltage	$I_E = 1 \text{mA}, I_C = 0$	5			V
I _{CBO}	Collector Cut-off Current	$V_{CB} = 120V, I_{E} = 0$			100	nA
I _{EBO}	Emitter Cut-off Current	$V_{BE} = 5V, I_{C} = 0$			100	nA
h _{FE}	DC Current Gain	$V_{CE} = 5V, I_{C} = 100mA$	80		160	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 500 \text{mA}, I_B = 50 \text{mA}$			1.0	V
V _{BE} (on)	Base-Emitter On Voltage	$V_{CE} = 5V, I_{C} = 500 \text{mA}$			1.0	V
f _T	Current Gain Bandwidth Product	$V_{CE} = 5V, I_{C} = 100mA$		120		MHz
C _{ob}	Output Capacitance	$V_{CB} = 10V, I_{E} = 0, f = 1MHz$			30	pF

Typical Performance Characteristics

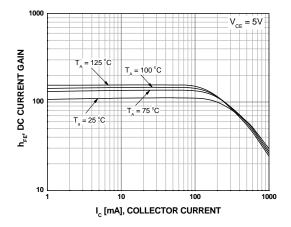


Figure 1. DC Current Gain

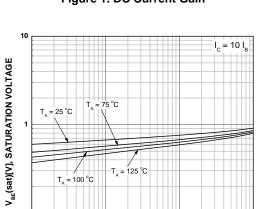


Figure 3. Base-Emitter Saturation Voltage

 $I_{_{\rm C}}$ [mA], COLLECTOR CURRENT

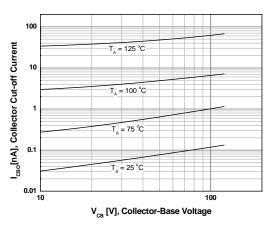


Figure 5. Collector-Base Cutoff Current

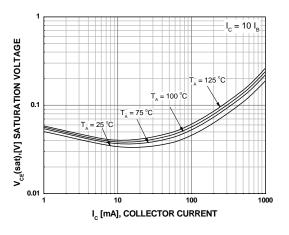


Figure 2. Collector-Emitter Saturation Voltage

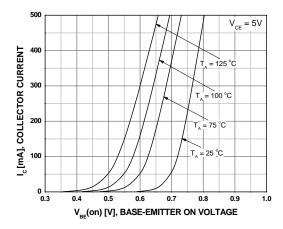


Figure 4. Base-Emitter On Voltage

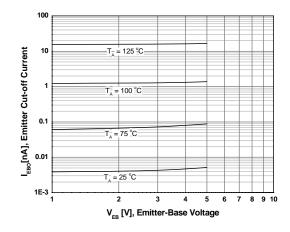


Figure 6. Emitter-Base Cutoff Current

Typical Performance Characteristics (Continued)

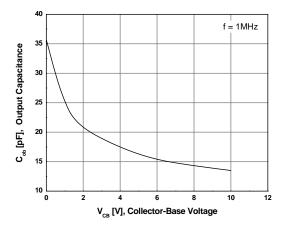


Figure 7. Output Capacitance

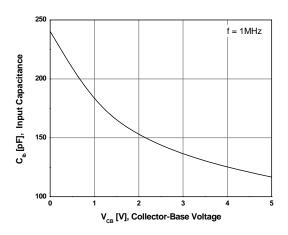


Figure 8. Input Capacitance

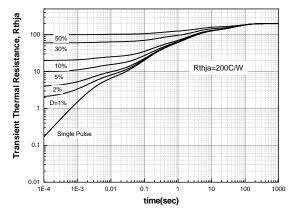
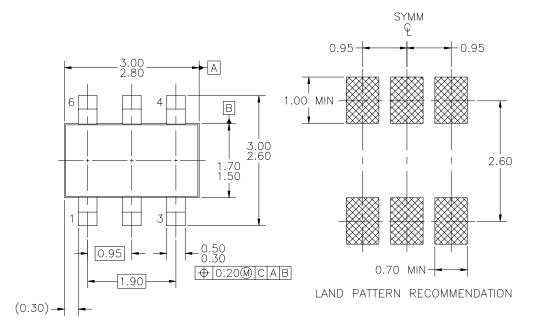
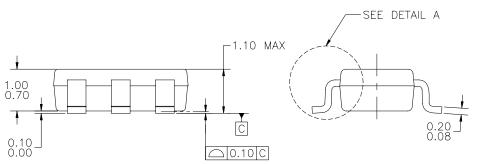


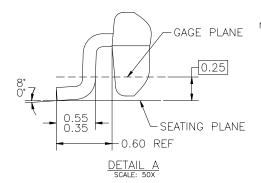
Figure 9. Transient Thermal Resistance

Physical Dimensions

SuperSOTTM-6







NOTES: UNLESS OTHERWISE SPECIFIED

- THIS PACKAGE CONFORMS TO JEDEC MO-193. VAR. AA, ISSUE C, DATED JANUARY 2000. ALL DIMENSIONS ARE IN MILLIMETERS.

Dimensions in Millimeters





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