

December 2008

# FOD814 Series, FOD617 Series, FOD817 Series 4-Pin High Operating Temperature Phototransistor Optocouplers

#### **Features**

- AC input response (FOD814 only)
- Applicable to Pb-free IR reflow soldering
- Compact 4-pin package
- Current transfer ratio in selected groups:

FOD617A: 40–80% FOD817: 50–600% FOD617B: 63–125% FOD817A: 80–160% FOD617C: 100–200% FOD817B: 130–260% FOD814: 20–300% FOD817D: 300–600% FOD817D: 300–600%

FOD814A: 50-150%

■ C-UL, UL and VDE approved

- High input-output isolation voltage of 5000Vrms
- Minimum BV<sub>CFO</sub> of 70V guaranteed
- Higher operating temperatures (versus H11AXXX counterparts)

### **Applications**

FOD814 Series

- AC line monitor
- Unknown polarity DC sensor
- Telephone line interface

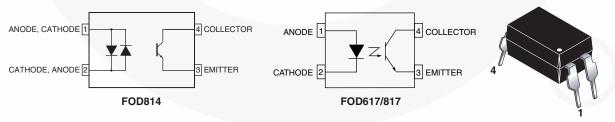
FOD617 and FOD817 Series

- Power supply regulators
- Digital logic inputs
- Microprocessor inputs

### Description

The FOD814 consists of two gallium arsenide infrared emitting diodes, connected in inverse parallel, driving a silicon phototransistor output in a 4-pin dual in-line package. The FOD617/817 Series consists of a gallium arsenide infrared emitting diode driving a silicon phototransistor in a 4-pin dual in-line package.

## **Functional Block Diagram**



## **Absolute Maximum Ratings** (T<sub>A</sub> = 25°C Unless otherwise specified.)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

		Va	lue	Units
Symbol	Parameter	FOD814	FOD617/817	
TOTAL DEVIC	DE .			
T <sub>STG</sub>	Storage Temperature	-55 to	+150	°C
T <sub>OPR</sub>	Operating Temperature	-55 to +105	-55 to +110	°C
T <sub>SOL</sub>	Lead Solder Temperature	260 for	10 sec	°C
P <sub>TOT</sub>	Total Power Dissipation	20	00	mW
EMITTER				
I <sub>F</sub>	Continuous Forward Current	±50	50	mA
V <sub>R</sub>	Reverse Voltage		6	
P <sub>D</sub>	Power Dissipation	7	0	mW
	Derate above 100°C	1.	.7	mW/°C
DETECTOR				
V <sub>CEO</sub>	Collector-Emitter Voltage	7	0	V
V <sub>ECO</sub>	Emitter-Collector Voltage	6	6 (FOD817)	V
			7 (FOD617)	
I <sub>C</sub>	Continuous Collector Current	ous Collector Current 50		mA
P <sub>C</sub>	Collector Power Dissipation	15	150	
		.9	mW/°C	

## **Electrical Characteristics** (T<sub>A</sub> = 25°C Unless otherwise specified.)

### **Individual Component Characteristics**

Symbol	Parameter	Device	Test Conditions	Min.	Тур.*	Max.	Unit
EMITTER		•					
V <sub>F</sub>	Forward Voltage	FOD814	I <sub>F</sub> = ±20mA		1.2	1.4	V
		FOD617	I <sub>F</sub> = 60mA		1.35	1.65	
		FOD817	I <sub>F</sub> = 20mA		1.2	1.4	
I <sub>R</sub>	Reverse Leakage Current	FOD617	V <sub>R</sub> = 6.0V		0.001	10	μΑ
		FOD817	V <sub>R</sub> = 4.0V			10	
Ct	Terminal Capacitance	FOD814	V = 0, f = 1kHz		50	250	pF
		FOD617	V = 0, f = 1kHz		30	250	
		FOD817	V = 0, f = 1kHz		30	250	
DETECTO	3	•					
I <sub>CEO</sub>	Collector Dark Current	FOD814	$V_{CE} = 20V, I_F = 0$			100	nA
		FOD617C/D	$V_{CE} = 10V, I_F = 0$		1	100	
		FOD617A/B	$V_{CE} = 10V, I_F = 0$		1	50	
		FOD817	$V_{CE} = 20V, I_F = 0$			100	
BV <sub>CEO</sub>	Collector-Emitter Breakdown	FOD814	$I_C = 0.1 \text{mA}, I_F = 0$	70			V
	Voltage	FOD617	$I_C = 100 \mu A, I_F = 0$	70			
		FOD817	$I_C = 0.1 \text{mA}, I_F = 0$	70			
BV <sub>ECO</sub>	Emitter-Collector Breakdown	FOD814	$I_E = 10\mu A, I_F = 0$	6			V
	Voltage	FOD617	$I_E = 10\mu A, I_F = 0$	7			
		FOD817	$I_E = 10\mu A, I_F = 0$	6			

### **Transfer Characteristics**

Symbol	DC Characteristic	Device	Test Conditions	Min.	Тур.*	Max.	Unit
CTR Current Transfer	FOD814	$I_F = \pm 1 \text{mA}, V_{CE} = 5V^{(1)}$	20		300	%	
	Ratio	FOD814A		50		150	
		FOD617A	$I_F = 10 \text{mA}, V_{CE} = 5V^{(1)}$	40		80	ĺ
		FOD617B		63		125	ĺ
		FOD617C		100		200	ĺ
	FOD617D		160		320	ĺ	
		FOD617A	$I_F = 1 \text{mA}, V_{CE} = 5V^{(1)}$	13			ĺ
	FOD617B		22				
		FOD617C		34			
		FOD617D		56			
		FOD817	$I_F = 5mA, V_{CE} = 5V^{(1)}$	50		600	
		FOD817A		80		160	
		FOD817B		130		260	ĺ
		FOD817C		200		400	ĺ
		FOD817D		300		600	ĺ
OL (3at)	Collector-Emitter	FOD814	$I_F = \pm 20$ mA, $I_C = 1$ mA		0.1	0.2	V
	Saturation Voltage	FOD617	I <sub>F</sub> = 10mA, I <sub>C</sub> = 2.5mA			0.4	
		FOD817	I <sub>F</sub> = 20mA, I <sub>C</sub> = 1mA		0.1	0.2	

<sup>\*</sup>Typical values at T<sub>A</sub> = 25°C

## **Electrical Characteristics** (T<sub>A</sub> = 25°C Unless otherwise specified.) (Continued)

### Transfer Characteristics (Continued)

Symbol	AC Characteristic	Device	Test Conditions	Min.	Тур.*	Max.	Unit
f <sub>C</sub>	Cut-Off Frequency	FOD814	$V_{CE} = 5V$ , $I_{C} = 2mA$ , $R_{L} = 100\Omega$ , $-3dB$	15	80		kHz
t <sub>r</sub>	Response Time (Rise)	FOD814	$V_{CE} = 2 \text{ V}, I_{C} = 2\text{mA}, R_{L} = 100\Omega^{(2)}$		4	18	μs
		FOD617					
		FOD817					
t <sub>f</sub>	Response Time (Fall)	FOD814			3	18	μs
		FOD617					
		FOD817					

### **Isolation Characteristics**

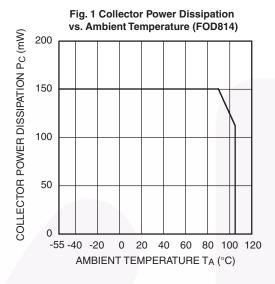
Symbol	Characteristic	Device	Test Conditions	Min.	Тур.*	Max.	Units
V <sub>ISO</sub>	Input-Output Isolation	FOD814	f = 60Hz, t = 1 min,	5000			Vac(rms)
	Voltage <sup>(3)</sup>	FOD617	I <sub>I-O</sub> ≤ 2μΑ				
		FOD817					
R <sub>ISO</sub>	Isolation Resistance	FOD814	V <sub>I-O</sub> = 500VDC	5x10 <sup>10</sup>	1x10 <sup>11</sup>		Ω
		FOD617					
		FOD817					
C <sub>ISO</sub>	Isolation Capacitance	FOD814	V <sub>I-O</sub> = 0, f = 1 MHz		0.6	1.0	pf
		FOD617					
		FOD817					

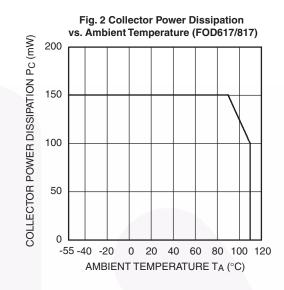
<sup>\*</sup>Typical values at T<sub>A</sub> = 25°C

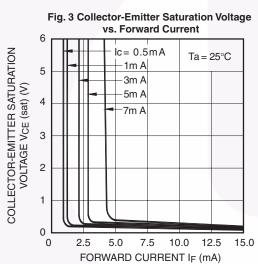
#### Notes:

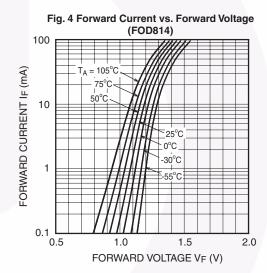
- 1. Current Transfer Ratio (CTR) =  $I_C/I_F \times 100\%$ .
- 2. For test circuit setup and waveforms, refer to page 4.
- 3. For this test, Pins 1 and 2 are common, and Pins 3 and 4 are common.

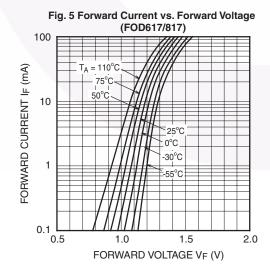
# Typical Electrical/Optical Characteristics ( $T_A = 25$ °C Unless otherwise specified.)

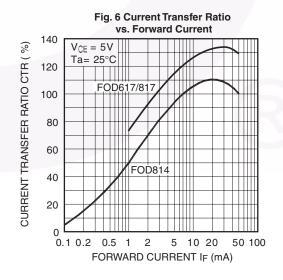












## Typical Electrical/Optical Characteristics (Continued) (T<sub>A</sub> = 25°C Unless otherwise specified.)

Fig. 7 Collector Current vs. Collector-Emitter Voltage (FOD814)

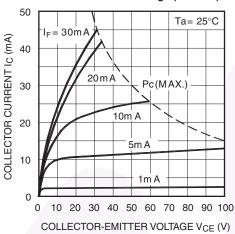


Fig. 9 Relative Current Transfer Ratio

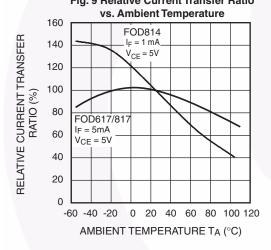


Fig. 11 LED Power Dissipation vs. Ambient Temperature (FOD814)

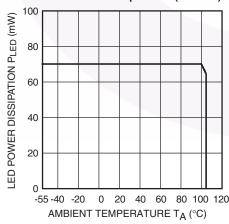


Fig. 8 Collector Current vs.
Collector-Emitter Voltage (FOD617/817)

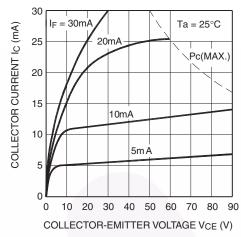


Fig. 10 Collector-Emitter Saturation Voltage vs. Ambient Temperature

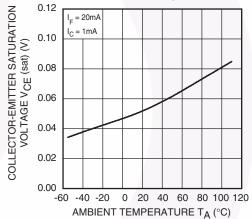
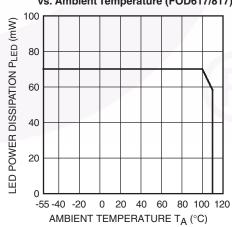


Fig. 12 LED Power Dissipation vs. Ambient Temperature (FOD617/817)



## Typical Electrical/Optical Characteristics (Continued) (T<sub>A</sub> = 25°C Unless otherwise specified.)

Fig. 13 Response Time vs. Load Resistance 100  $V_{CE} = 2V$ 50 Ic= 2mA Ta= 25°C 20 RESPONSE TIME (us) 10 0.5 0.2 0.1 0.1 0.2 0.5 1 LOAD RESISTANCE R<sub>L</sub> ( $k\Omega$ )

Fig. 15 Collector Dark Current
vs. Ambient Temperature

Voice = 20V

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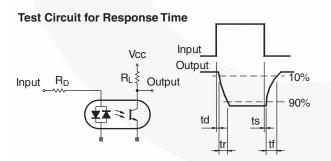
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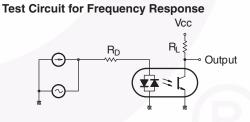
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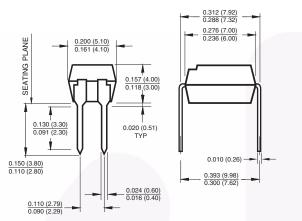
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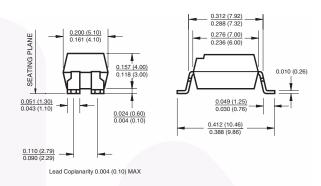


## **Package Dimensions**

### **Through Hole**

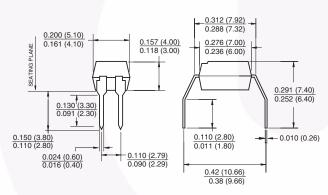


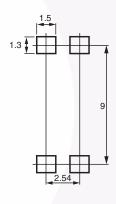
#### **Surface Mount**



### **Surface Mount (Footprint Dimensions)**

### 0.4" Lead Spacing





#### Note:

All dimensions are in inches (millimeters)

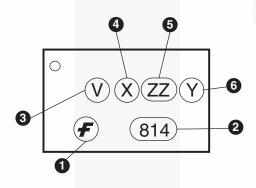
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## **Ordering Information**

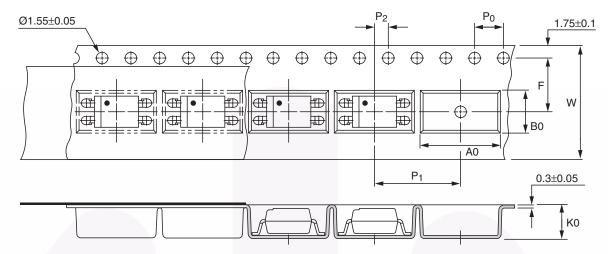
Option	Part Number Example	Description
S	FOD814S	Surface Mount Lead Bend
SD	FOD814SD	Surface Mount; Tape and reel
W	FOD814W	0.4" Lead Spacing
300	FOD814300	VDE Approved
300W	FOD814300W	VDE Approved, 0.4" Lead Spacing
3S	FOD8143S	VDE Approved, Surface Mount
3SD	FOD8143SD	VDE Approved, Surface Mount, Tape & Reel

## **Marking Information**



Definiti	Definitions					
1	Fairchild logo					
2	Device number					
3	VDE mark (Note: Only appears on parts ordered with VDE option – See order entry table)					
4	One digit year code					
5	Two digit work week ranging from '01' to '53'					
6	Assembly package code					

## **Carrier Tape Specifications**

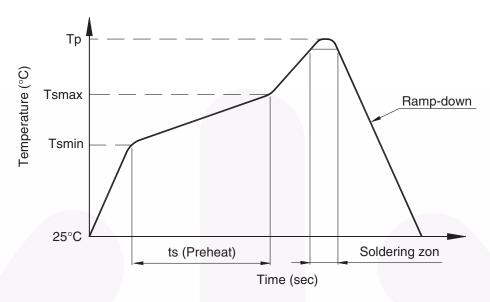


#### Note:

All dimensions are in millimeters.

Symbol	Description	Dimensions in mm (inches)
W	Tape wide	16 ± 0.3 (.63)
P <sub>0</sub>	Pitch of sprocket holes	4 ± 0.1 (.15)
F	Distance of compartment	$7.5 \pm 0.1  (.295)$
P <sub>2</sub>		2 ± 0.1 (.079)
P <sub>1</sub>	Distance of compartment to compartment	12 ± 0.1 (.472)
A0	Compartment	10.45 ± 0.1 (.411)
В0		5.30 ± 0.1 (.209)
K0		4.25 ± 0.1 (.167)

## **Lead Free Recommended IR Reflow Condition**



Profile Feature	Pb-Sn solder assembly	Lead Free assembly
Preheat condition (Tsmin-Tsmax / ts)	100°C ~ 150°C 60 ~ 120 sec	150°C ~ 200°C 60 ~120 sec
Melt soldering zone	183°C 60 ~ 120 sec	217°C 30 ~ 90 sec
Peak temperature (Tp)	240 +0/-5°C	260 +0/-5°C
Ramp-down rate	6°C/sec max.	6°C/sec max.

## **Recommended Wave Soldering condition**

Profile Feature	For all solder assembly
Peak temperature (Tp)	Max 260°C for 10 sec





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#### PRODUCT STATUS DEFINITIONS

#### **Definition of Terms**

Delinition of Terms		
Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor.  The datasheet is for reference information only.

Rev. 137

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