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# FQA10N80C-F109

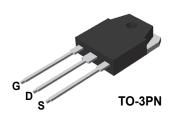
# N-Channel QFET $^{(\!\! R)}$ MOSFET 800 V, 10 A, 1.1 $\Omega$

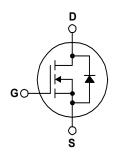
### **Features**

- 10 A, 800 V,  $R_{DS(on)} = 1.1 \Omega$  (Max.) @  $V_{GS} = 10 \text{ V}$ ,  $I_D = 5 \text{ A}$
- Low Gate Charge (Typ. 44 nC)
- Low Crss (Typ. 15 pF)
- · 100% Avalanche Tested
- · RoHS compliant

## **Description**

This N-Channel enhancement mode power MOSFET is produced using ON Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.





## **MOSFET Maximum Ratings** T<sub>C</sub> = 25°C unless otherwise noted.

Symbol	Drain to Source Voltage			FQA10N80C-F109	Unit
V <sub>DSS</sub>				800	V
I <sub>D</sub>	Drain Current	-Continuous (T <sub>C</sub> = 25°C)		10	Α
	Drain Current	-Continuous (T <sub>C</sub> = 100°C)		6.32	Α
I <sub>DM</sub>	Drain Current	- Pulsed	(Note 1)	40	Α
V <sub>GSS</sub>	Gate to Source Voltage			± 30	V
E <sub>AS</sub>	Single Pulsed Avalanche Energy		(Note 2)	920	mJ
I <sub>AR</sub>	Avalanche Current		(Note 1)	10	Α
E <sub>AR</sub>	Repetitive Avalanche Energy		(Note 1)	24	mJ
dv/dt	Peak Diode Recovery dv/dt		(Note 3)	4.0	V/ns
$P_{D}$	Davier Dissipation	(T <sub>C</sub> = 25°C)		240	W
	Power Dissipation	- Derate above 25°C		1.92	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range			-55 to +150	°C
T <sub>L</sub>	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds			300	°C

### **Thermal Characteristics**

Symbol	Parameter	FQA10N80C-F109	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max	0.52	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max	40	°C/W

## **Package Marking and Ordering Information**

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQA10N80C-F109	FQA10N80C	TO-3PN	Tube	N/A	N/A	30 units

## **Electrical Characteristics** $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter Test Conditions		Min	Тур	Max	Unit
Off Cha	racteristics					
$BV_{DSS}$	Orain-Source Breakdown Voltage $V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		800			V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 μA, Referenced to 25°C		0.98		V/°C
	Zara Oata Valtaria Daria Orienta	V <sub>DS</sub> = 800 V, V <sub>GS</sub> = 0 V			10	μΑ
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 640 V, T <sub>C</sub> = 125°C			100	μΑ
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 30 V, V <sub>DS</sub> = 0 V	-		100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -30 V, V <sub>DS</sub> = 0 V	1		-100	nA
On Cha	racteristics					
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	3.0		5.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5.0 A		0.93	1.1	Ω
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 50 V, I <sub>D</sub> = 5.0 A	-	5.8		S
Dynami C <sub>iss</sub>	c Characteristics Input Capacitance			2150	2800	pF
C <sub>oss</sub>	Output Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$		180	230	рF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1.0 MHz		15	20	рF
riss	Treverse Transier Supusitance			10	20	Pi.
Switchi	ng Characteristics					
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 400 V, I <sub>D</sub> = 10.0 A,	-	50	110	ns
t <sub>r</sub>	Turn-On Rise Time	$R_G = 25 \Omega$		130	270	ns
$t_{d(off)}$	Turn-Off Delay Time			90	190	ns
t <sub>f</sub>	Turn-Off Fall Time	(Note4)		80	170	ns
$Q_g$	Total Gate Charge	V <sub>DS</sub> = 640 V, I <sub>D</sub> = 10.0 A,	-	45	58	nC
$Q_{gs}$	Gate-Source Charge	V <sub>GS</sub> = 10 V		13.5		nC
$Q_{gd}$	Gate-Drain Charge	(Note 4)		17		nC
Drain-S	ource Diode Characteristics ar	nd Maximum Ratings				
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current				10.0	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current				40.0	Α
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 10.0 A			1.4	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 10.0 A,		730		ns
Q <sub>rr</sub>	Reverse Recovery Charge	dI <sub>F</sub> / dt = 100 A/μs		10.9		μС

#### Notes :

- 1. Repetitive Rating : Pulse width limited by maximum junction temperature.
- 2. L = 17.3 mH, I  $_{AS}$  = 10 A, V  $_{DD}$  = 50 V, R  $_{G}$  = 25  $\Omega,$  starting  $\,$  T  $_{J}$  = 25  $^{\circ}C.$
- $3.~I_{SD} \leq 8.4~A,~di/dt \leq 200~A/\mu s,~V_{DD} \leq BV_{DSS,}~starting~~T_J = 25^{\circ}C.$
- 4. Essentially independent of operating temperature.

## **Typical Characteristics**

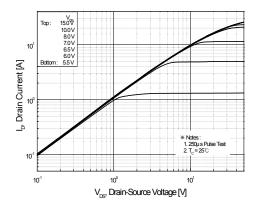


Figure 1. On-Region Characteristics

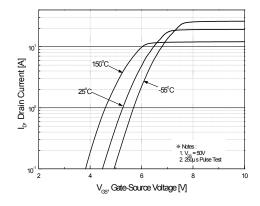


Figure 2. Transfer Characteristics

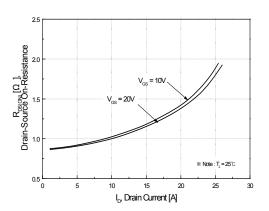


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

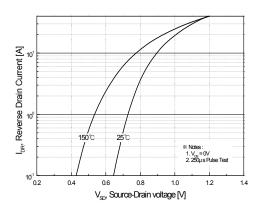


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

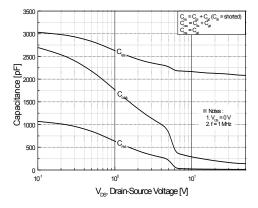


Figure 5. Capacitance Characteristics

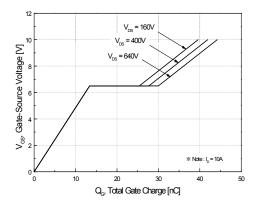


Figure 6. Gate Charge Characteristics

## Typical Characteristics (Continued)

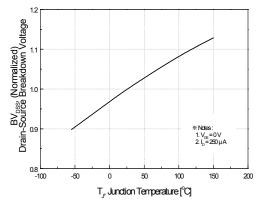


Figure 7. Breakdown Voltage Variation vs Temperature

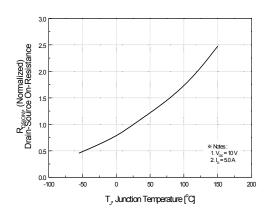


Figure 8. On-Resistance Variation vs Temperature

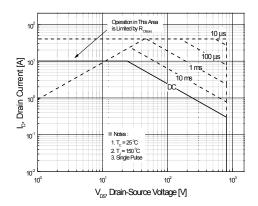


Figure 9. Maximum Safe Operating Area

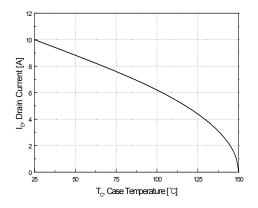


Figure 10. Maximum Drain Current vs Case Temperature

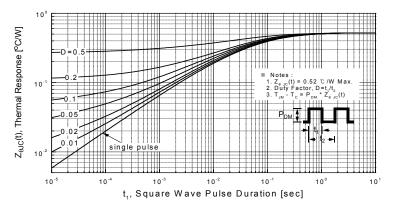


Figure 11. Transient Thermal Response Curve

Figure 12. Gate Charge Test Circuit & Waveform

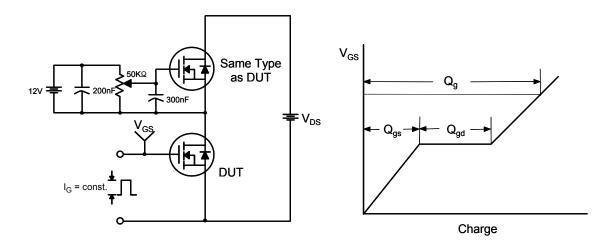


Figure 13. Resistive Switching Test Circuit & Waveforms

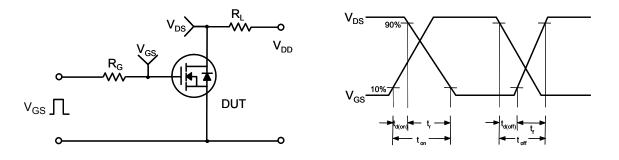


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms

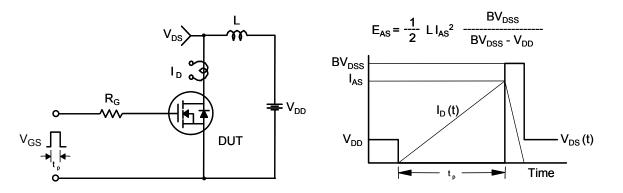
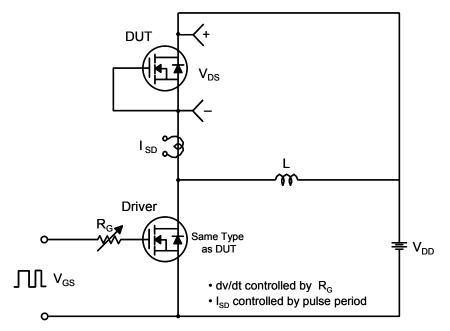
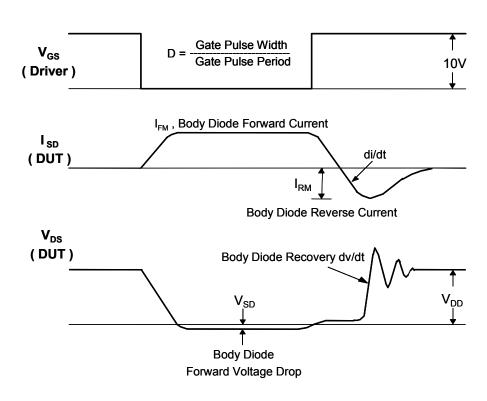


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms





### **Mechanical Dimensions**

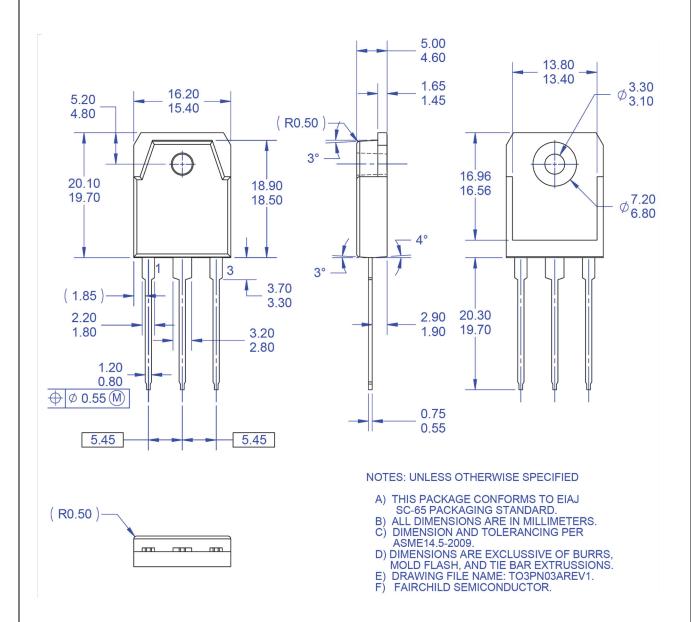


Figure 16. TO3PN, 3-Lead, Plastic, EIAJ SC-65

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