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June 2014

FQA160N08

N-Channel QFET[®] MOSFET

80 V, 160 A, 7 mΩ

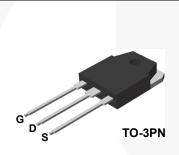
Description

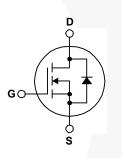
This N-Channel enhancement mode power MOSFET is • 160 A, 80 V, $R_{DS(on)}$ = 7 m Ω (Max.) @ V_{GS} = 10 V, produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state $I_D = 80 \text{ A}$ Low Gate Charge (Typ. 220 nC) resistance, and to provide superior switching performance and • Low Crss (Typ. 530 pF) high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor • 100% Avalanche Tested control, and variable switching power applications.

Features

- I_D = 80 A

- 175°C Maximum Junction Temperature Rating





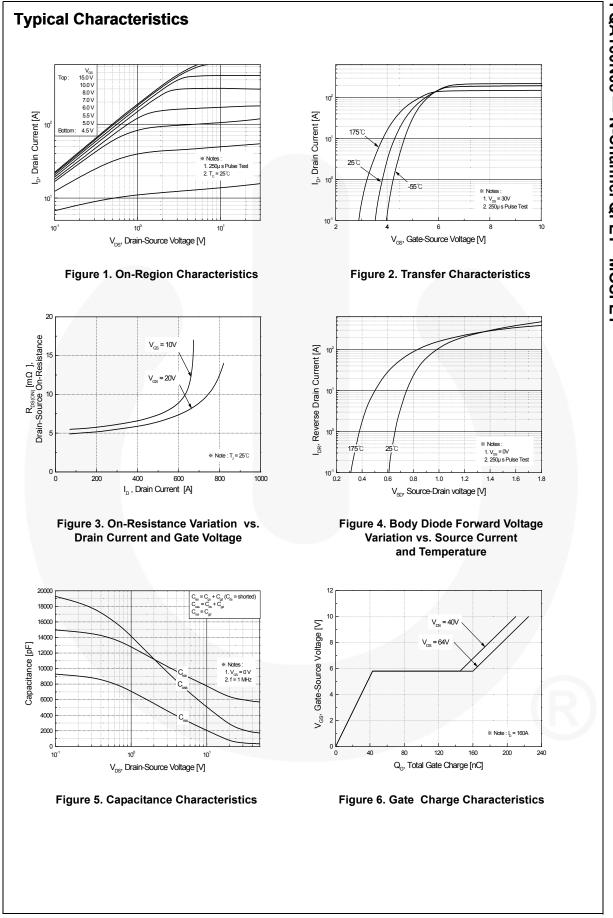
Absolute Maximum Ratings T_c = 25°C unless otherwise noted.

Symbol	Parameter		FQA160N08	Unit
V _{DSS}	Drain-Source Voltage		80	V
I _D	Drain Current - Continuous ($T_C = 25^{\circ}C$)		160	A
	- Continuous (T _C = 100°C)		113	A
I _{DM}	Drain Current - Pulsed	(Note 1)	640	A
V _{GSS}	Gate-Source Voltage		± 25	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	1600	mJ
I _{AR}	Avalanche Current	(Note 1)	160	A
E _{AR}	Repetitive Avalanche Energy	(Note 1)	37.5	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	6.5	V/ns
PD	Power Dissipation ($T_C = 25^{\circ}C$)		375	W
	- Derate above 25°C		2.5	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +175	°C
TL	Maximum lead temperature for soldering, 1/8" from case for 5 seconds.		300	°C

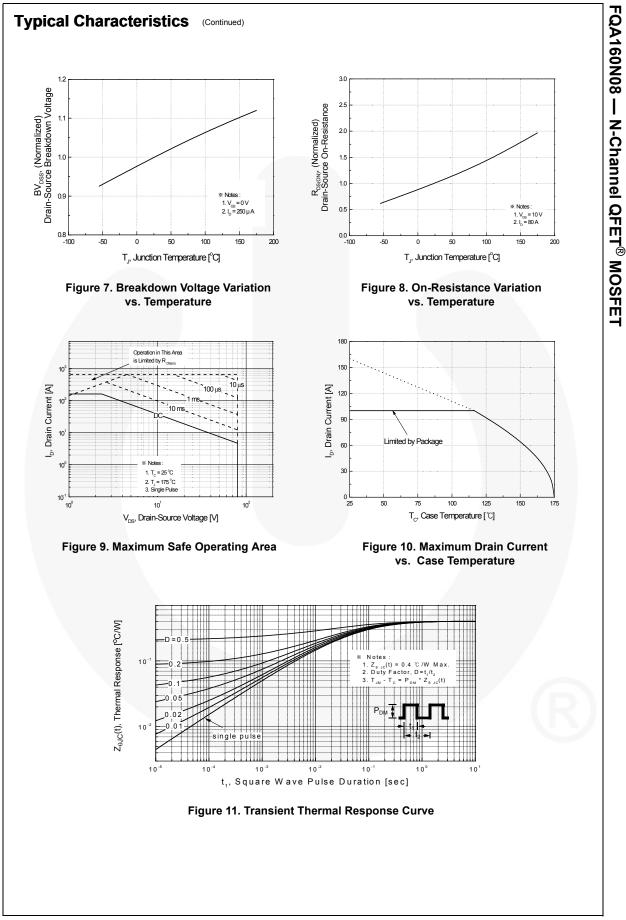
Thermal Characteristics

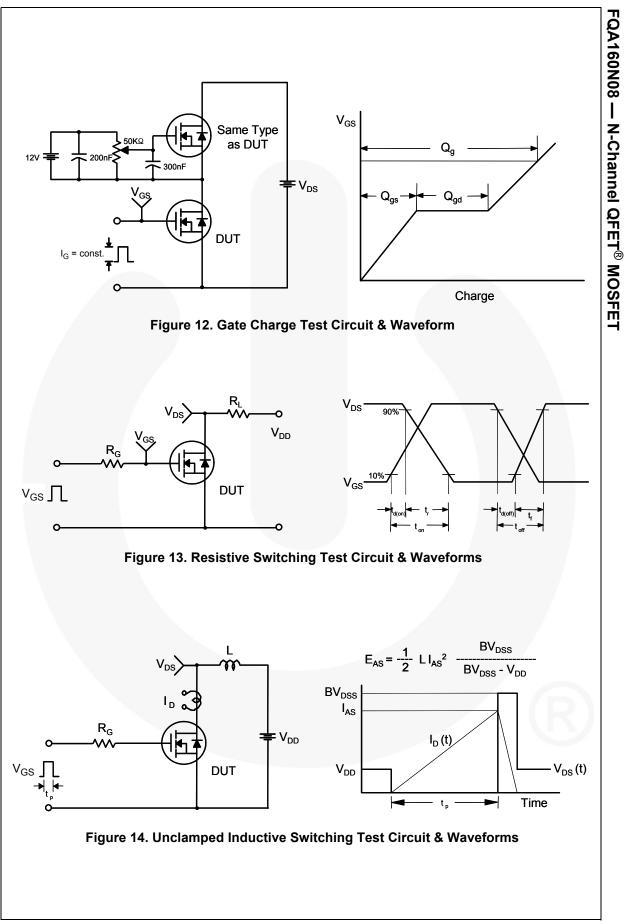
Symbol	Parameter	FQA160N08	Unit
R _{θJC}	Thermal Resistance, Junction-to-Case, Max.	0.4	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction-to-Ambient, Max.	40	°C/W

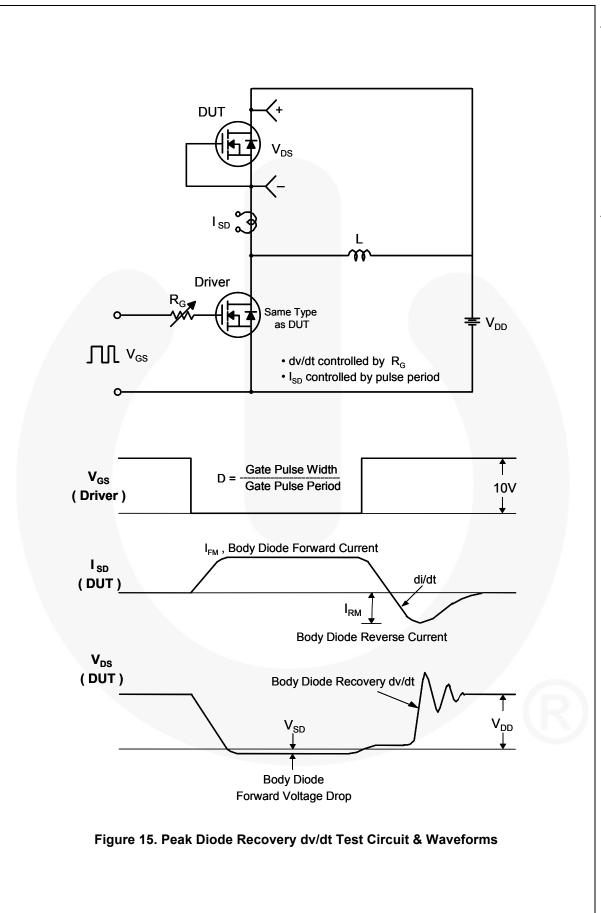
FQA1	lumber	Top Mark	Pack	kage	Packing Method	I Ree	Size	Tape Wi	dth	Quantity
	60N08	FQA160N08	TO-	-	Tube		/A	N/A		30 units
1							1		1	
		Parameter	T _C = 25°0	C unless oth	Test Conditions		Min.	Тур.	Мах	. Unit
Symbol		Farameter			Test Conditions		WIIII.	тур.	Wax	. Onit
	aracterist			N -	0 V, I _D = 250 μA				1	
BV _{DSS} ABV _{DSS}		Irce Breakdown Voltag	•	v _{GS} -	0 ν, I _D – 250 μΑ		80			V
$/ \Delta T_J$	Coefficien	n Voltage Temperatui t	re	_	i0 μA, Referenced t	o 25°C		0.08		V/°C
DSS	Zero Cate	Voltage Drain Currer	ot		80 V, V _{GS} = 0 V				1	μΑ
	Zero Gale	voltage Drain Currer	n.		64 V, T _C = 150°C				10	μA
GSSF	Gate-Bod	y Leakage Current, F	orward		25 V, V _{DS} = 0 V				100	nA
GSSR	Gate-Bod	y Leakage Current, R	everse	V _{GS} =	-25 V, V _{DS} = 0 V				-100) nA
On Cha	racterist	ics								
V _{GS(th)}		eshold Voltage		V _{DS} =	V _{GS} , I _D = 250 μA		2.0		4.0	V
R _{DS(on)}	Static Dra On-Resist			V _{GS} =	10 V, I _D = 80 A			0.0056	0.007	7 Ω
JFS		ransconductance	_	V _{DS} =	30 V, I _D = 80 A	-		92		S
Dynam	ic Chara	cteristics								
	ic Charac	lensucs								
	1	acitance	-	<u>ار -</u>				6100	7900) pF
C _{iss}	Input Cap				25 V, V _{GS} = 0 V,			6100 2400	7900	
C _{iss} C _{oss}	Input Cap Output Ca	apacitance		V _{DS} = f = 1.0				6100 2400 530	7900 3100 690) pF
C _{iss}	Input Cap Output Ca		_					2400	3100) pF
C _{iss} C _{oss} C _{rss}	Input Cap Output Ca Reverse T	apacitance Fransfer Capacitance						2400 530	3100) pF
C _{iss} C _{oss} C _{rss} Switchi	Input Cap Output Ca Reverse T ing Chara Turn-On E	apacitance Transfer Capacitance acteristics Delay Time		f = 1.0				2400	3100) pF pF
C _{iss} C _{oss} C _{rss} Switchi	Input Cap Output Ca Reverse T Ing Chara Turn-On E Turn-On F	apacitance Transfer Capacitance acteristics Delay Time Rise Time		f = 1.0	MHz 40 V, I _D = 160 A,			2400 530	3100 690) pF pF ns
C _{iss} C _{oss} C _{rss} Switchi d(on) r d(off)	Input Cap Output Ca Reverse 1 ing Chara Turn-On E Turn-On F Turn-Off E	apacitance Transfer Capacitance acteristics Delay Time Rise Time Delay Time		f = 1.0	MHz 40 V, I _D = 160 A,	(Nate 4)		2400 530 85	3100 690 180) pF pF ns) ns
Ciss Coss Crss Switchi d(on) r d(off) f	Input Cap Output Ca Reverse 1 ing Chara Turn-On E Turn-On F Turn-Off F Turn-Off F	apacitance Fransfer Capacitance acteristics Delay Time Rise Time Delay Time Fall Time		f = 1.0	MHz 40 V, I _D = 160 A,	(Note4)	 	2400 530 85 970	3100 690 180 2000) pF pF ns) ns ns
Ciss Coss Crss Switchi (d(on) (f (d(off)) (f Q _g	Input Cap Output Ca Reverse T ing Chara Turn-On E Turn-On F Turn-Off E Turn-Off F Total Gate	apacitance Transfer Capacitance acteristics Delay Time Rise Time Delay Time Fall Time a Charge		f = 1.0	MHz 40 V, I _D = 160 A,	(Note4)	 	2400 530 85 970 260 410 225	3100 690 180 2000 530	 pF pF ns ns ns ns nc
C _{iss} C _{oss} C _{rss} Switchi d(on) r d(off) f Q _g Q _{gs}	Input Cap Output Ca Reverse 1 ing Chara Turn-On E Turn-On F Turn-Off E Turn-Off F Total Gate Gate-Sou	apacitance Transfer Capacitance Acteristics Delay Time Rise Time Delay Time Fall Time Charge rce Charge		f = 1.0	MHz 40 V, I _D = 160 A, 5 Ω 64 V, I _D = 160 A,	(Note4)	 	2400 530 85 970 260 410 225 43	3100 690 180 2000 530 830) pF pF ns) ns ns ns
Ciss Coss Crss Switchi (d(on) (f (d(off)) (f Q _g	Input Cap Output Ca Reverse T ing Chara Turn-On E Turn-On F Turn-Off E Turn-Off F Total Gate	apacitance Transfer Capacitance Acteristics Delay Time Rise Time Delay Time Fall Time Charge rce Charge		f = 1.0	MHz 40 V, I _D = 160 A, 5 Ω 64 V, I _D = 160 A,	(Note4) (Note4)	 	2400 530 85 970 260 410 225	3100 690 180 2000 530 830 290	 pF pF ns ns ns ns nc
Ciss Coss Crss Switchi d(on) r d(off) f Qg Qgs Qgd	Input Cap Output Ca Reverse 1 ing Chara Turn-On E Turn-Off E Turn-Off F Turn-Off F Total Gate Gate-Soul Gate-Drai	apacitance Transfer Capacitance acteristics Delay Time Rise Time Delay Time Fall Time a Charge rce Charge n Charge		f = 1.0 V _{DD} = R _G = 2 V _{DS} = V _{GS} =	MHz 40 V, $I_D = 160 A$, 5 Ω 64 V, $I_D = 160 A$, 10 V	(Note4)	 	2400 530 85 970 260 410 225 43	3100 690 180 2000 530 830 290 	 pF pF ns ns ns nc nC
C_{iss} C_{oss} C_{rss} Switchi d(on) r d(off) f Q_g Q_{gs} Q_{gd} Drain-S	Input Cap Output Ca Reverse 1 ing Chara Turn-On E Turn-Off E Turn-Off F Total Gate Gate-Drai Gate-Drai	apacitance Transfer Capacitance acteristics Delay Time Rise Time Delay Time Fall Time Charge rce Charge n Charge ode Characteris		$f = 1.0$ $V_{DD} =$ $R_{G} = 2$ $V_{DS} =$ $V_{GS} =$ Max	MHz 40 V, $I_D = 160 A$, 5 Ω 64 V, $I_D = 160 A$, 10 V kimum Ratings	(Note4)	 	2400 530 85 970 260 410 225 43	3100 690 180 2000 530 830 290) pF pF ns ns ns nC nC nC
Ciss Coss Crss Switchi d(on) r d(off) f Qg Qgs Qgs Qgd Drain-S S	Input Cap Output Ca Reverse T ing Chara Turn-On E Turn-On F Turn-Off E Turn-Off F Total Gate Gate-Soui Gate-Drai	apacitance Transfer Capacitance acteristics Delay Time Rise Time Delay Time Charge roce Charge n Charge ode Characteris Continuous Drain-So	ource Dic	$f = 1.0$ $V_{DD} =$ $R_{G} = 2$ $V_{DS} =$ $V_{GS} =$ Max Max Max Max	MHz 40 V, $I_D = 160$ A, 5 Ω 64 V, $I_D = 160$ A, 10 V kimum Ratings rard Current	(Note4)	 	2400 530 85 970 260 410 225 43 120	3100 690 2000 530 830 290 160	 pF pF ns ns ns nc nC nC A
C_{iss} C_{oss} C_{rss} Switchi d(on) r d(off) f Q_{gs} Q_{gs} Q_{gd} Drain-S s SM	Input Cap Output Ca Reverse 1 ing Chara Turn-On E Turn-On F Turn-Off E Turn-Off F Total Gate Gate-Sour Gate-Drai	apacitance Transfer Capacitance acteristics Delay Time Rise Time Delay Time Fall Time Charge n Charge n Charge Ode Characteris Continuous Drain-So Pulsed Drain-Source	ource Dio Diode F	$f = 1.0$ $V_{DD} =$ $R_{G} = 2$ $V_{DS} =$ $V_{GS} =$ $M_{GS} =$ $M_{GS} =$ $M_{GS} =$	MHz 40 V, $I_D = 160$ A, 5 Ω 64 V, $I_D = 160$ A, 10 V kimum Ratings rard Current Current	(Note4)	 	2400 530 85 970 260 410 225 43 120	3100 690 2000 530 830 290 160 640	 pF pF ns ns ns nc nC nC A
Ciss Coss Crss Switchi d(on) r d(off) f Qg Qgs Qgs Qgd Drain-S S	Input Cap Output Ca Reverse 1 ing Chara Turn-On E Turn-Off E Turn-Off F Total Gate Gate-Sou Gate-Drai Source Di Maximum Maximum Drain-Sou	apacitance Transfer Capacitance acteristics Delay Time Rise Time Delay Time Charge roce Charge n Charge ode Characteris Continuous Drain-So	ource Dio Diode F	$f = 1.0$ $V_{DD} =$ $R_{G} = 2$ $V_{DS} =$ $V_{GS} =$ $M = \frac{M}{2}$ $R_{G} = 2$	MHz 40 V, $I_D = 160$ A, 5 Ω 64 V, $I_D = 160$ A, 10 V kimum Ratings rard Current	(Note4)	 	2400 530 85 970 260 410 225 43 120	3100 690 2000 530 830 290 160	 pF pF ns ns ns nc nC nC A A

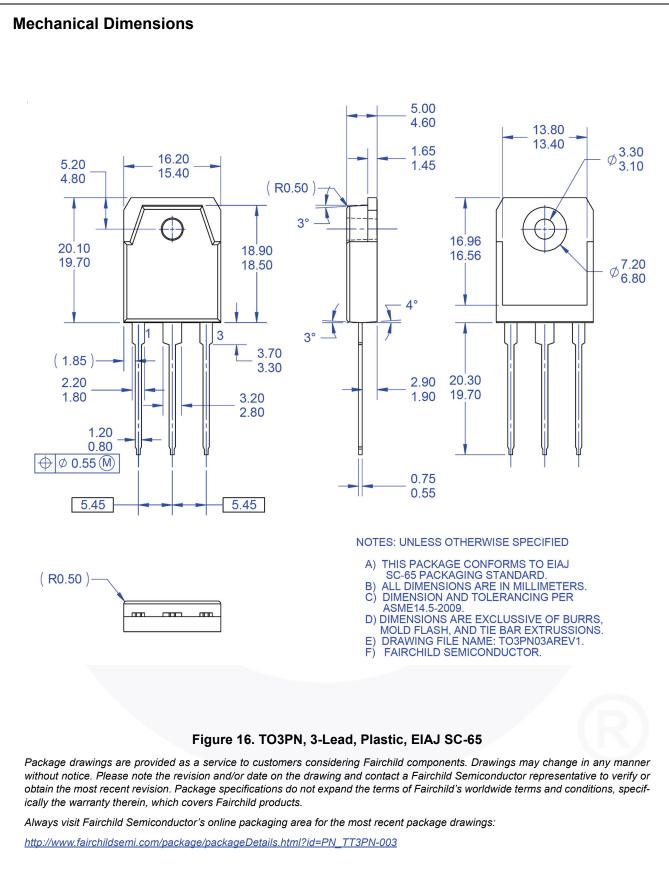


FQA160N08 — N-Channel QFET[®] MOSFET









FQA160N08

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