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# FQA6N90C-F109

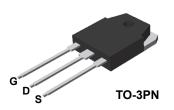
# N-Channel QFET $^{\circledR}$ MOSFET 900 V, 6 A, 2.3 $\Omega$

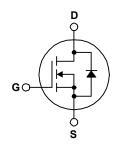
#### **Features**

- 6 A, 900 V,  $R_{DS(on)}$  = 2.3  $\Omega$  (Max.) @  $V_{GS}$  = 10 V,  $I_D$  = 3 A
- Low Gate Charge (Typ. 30 nC)
- Low Crss (Typ. 11 pF)
- 100% Avalanche Tested
- · RoHS Compliant

#### **Description**

This N-Channel enhancement mode power MOSFET is produced using ON Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.





# Absolute Maximum Ratings $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter		FQA6N90C-F109	Unit	
V <sub>DSS</sub>	Drain-Source Voltage		900	V	
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> = 25°C)		6.0	Α	
	- Continuous (T <sub>C</sub> = 100°C)		3.87	А	
I <sub>DM</sub>	Drain Current - Pulsed	(Note 1)	24.0	А	
V <sub>GSS</sub>	Gate-Source Voltage		± 30	V	
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	650	mJ	
I <sub>AR</sub>	Avalanche Current (Note		6.0	А	
E <sub>AR</sub>	Repetitive Avalanche Energy (Note		19.8	mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.0	V/ns	
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> = 25°C)		198	W	
	- Derate above 25°C		1.59	W/°C	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C	
TL	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	°C	

#### **Thermal Characteristics**

Symbol	Parameter	FQA6N90C-F109	Unit
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	0.63	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink, Typ.	0.24	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max	40	°C/W

# Package Marking and Ordering Information

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQA6N90C-F109	FQA6N90C	TO-3PN	Tube	N/A	N/A	30 units

# **Electrical Characteristics** $T_C = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	teristics	1				
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	900			V
$\Delta BV_{DSS}/$ $\Delta T_J$	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 μA, Referenced to 25°C		1.07		V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 900 V, V <sub>GS</sub> = 0 V	-		10	μΑ
		V <sub>DS</sub> = 720 V, T <sub>C</sub> = 125°C	-		100	μА
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 30 V, V <sub>DS</sub> = 0 V			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -30 V, V <sub>DS</sub> = 0 V			-100	nA
On Charact	eristics					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	3.0		5.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 3.0 A		1.93	2.3	Ω
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 50 V, I <sub>D</sub> = 3.0 A	-	5.5		S
Dynamic Cl	haracteristics					
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V,		1360	1770	pF
C <sub>oss</sub>	Output Capacitance	f = 1.0 MHz		110	145	pF
C <sub>rss</sub>	Reverse Transfer Capacitance	-	-	11	15	pF
Switching C	Characteristics					I.
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 450 V, I <sub>D</sub> = 6.0A,		35	80	ns
t <sub>r</sub>	Turn-On Rise Time	$R_G = 25 \Omega$		90	190	ns
t <sub>d(off)</sub>	Turn-Off Delay Time	-		55	120	ns
t <sub>f</sub>	Turn-Off Fall Time	(Note 4)		60	130	ns
Qg	Total Gate Charge	V <sub>DS</sub> = 720 V, I <sub>D</sub> = 6.0A,		30	40	nC
Q <sub>gs</sub>	Gate-Source Charge	V <sub>GS</sub> = 10 V		9.0		nC
Q <sub>gd</sub>	Gate-Drain Charge	(Note 4)		12		nC
Drain-Source	ce Diode Characteristics and Maximum Ratings					
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current				6.0	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current		-		24	Α
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 6.0 A	-		1.4	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 6.0 A,		630		ns
Q <sub>rr</sub>	Reverse Recovery Charge	dI <sub>F</sub> / dt = 100 A/μs		6.9		μС

<sup>1.</sup> Repetitive rating: pulse-width limited by maximum junction temperature.

<sup>2.</sup> L = 34 mH, I  $_{AS}$  = 6 A, V  $_{DD}$  = 50 V, R  $_{G}$  = 25  $\Omega,$  starting T  $_{J}$  = 25  $^{\circ}C.$ 

 $<sup>3.</sup>I_{SD} \le 6$  A, di/dt  $\le 200$  A/ $\mu$ s,  $V_{DD} \le BV_{DSS}$ , starting  $T_J$  = 25°C.

<sup>4.</sup> Essentially independent of operating temperature typical characteristics.

### **Typical Performance Characteristics**

Figure 1. On-Region Characteristics

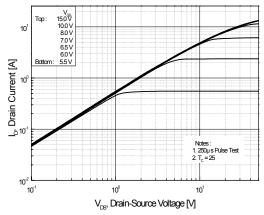


Figure 3. On-Resistance Variation vs.
Drain Current and Gate Voltage

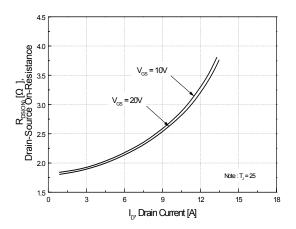


Figure 5. Capacitance Characteristics

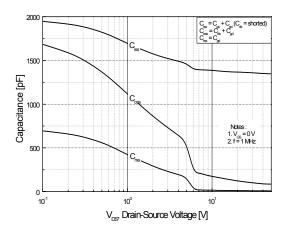


Figure 2. Transfer Characteristics

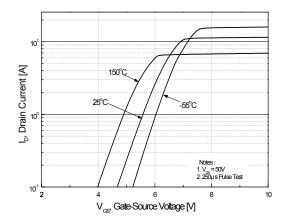
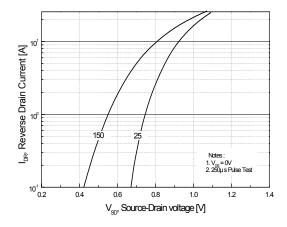
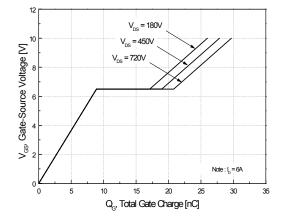


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue



**Figure 6. Gate Charge Characteristics** 



### Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

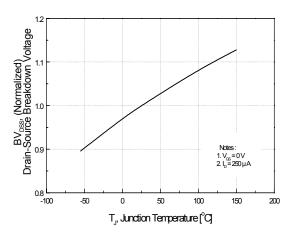


Figure 9. Maximum Safe Operating Area

Figure 8. On-Resistance Variation vs. Temperature

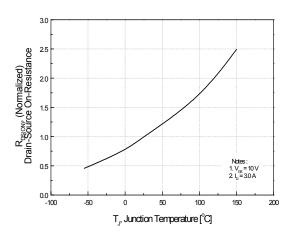


Figure 10. Maximum Drain Current vs. Case Temperature

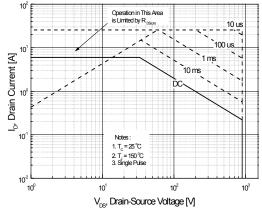
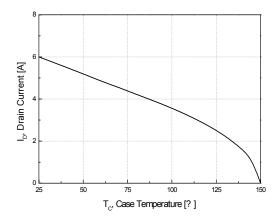
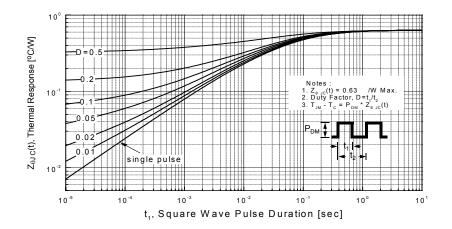


Figure 11. Transient Thermal Response Curve





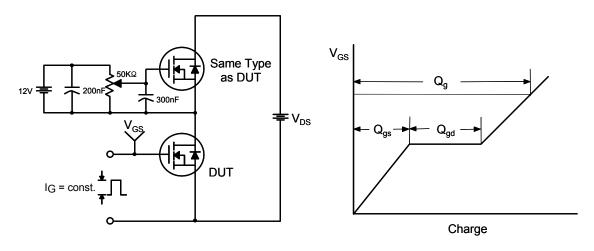


Figure 12. Gate Charge Test Circuit & Waveform

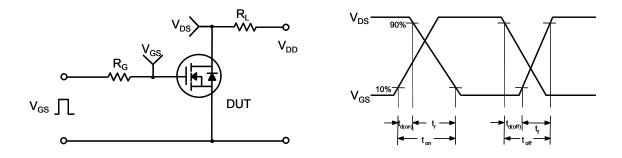


Figure 13. Resistive Switching Test Circuit & Waveforms

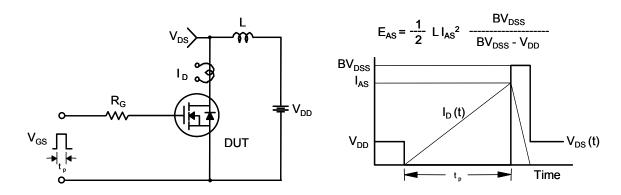
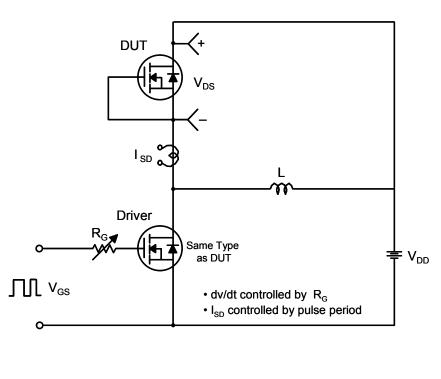


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



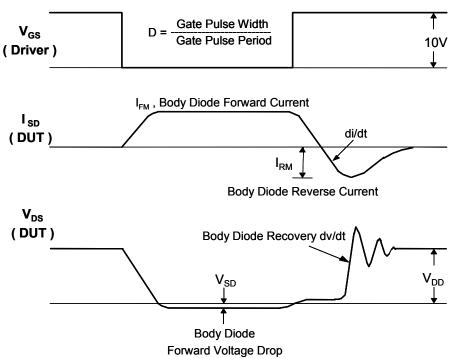
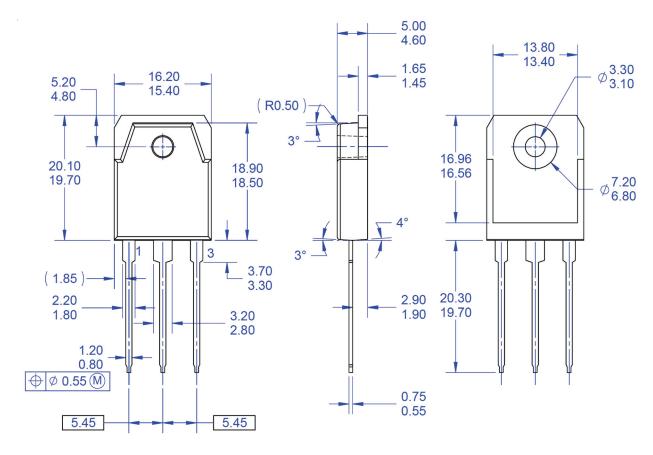
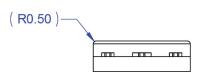


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

#### **Mechanical Dimensions**





#### NOTES: UNLESS OTHERWISE SPECIFIED

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   B) ALL DIMENSIONS ARE IN MILLIMETERS.
- DIMENSION AND TOLERANCING PER ASME14.5-2009.
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Figure 16. TO3PN, 3-Lead, Plastic, EIAJ SC-65

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