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ON Semiconductor® FQD13N06L / FQU13N06L N-Channel QFET[®] MOSFET 60 V, 11 A, 115 mΩ

Description

This N-Channel enhancement mode power MOSFET is produced using ON Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

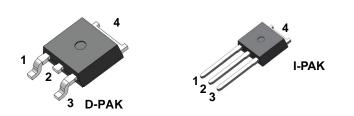
Features

- 11 A, 60 V, $R_{DS(on)}$ = 115 m Ω (Max) @ V_{GS} = 10 V, I_D = 5.5 A
- Low Gate Charge (Typ. 4.8 nC)
- Low Crss (Typ. 17 pF)
- 100% Avalanche Tested
- · Low Level Gate Drive Requirements Allowing Direct Operation form Logic Drivers

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Absolute Maximum Ratings To = 25°C unless otherwise noted.

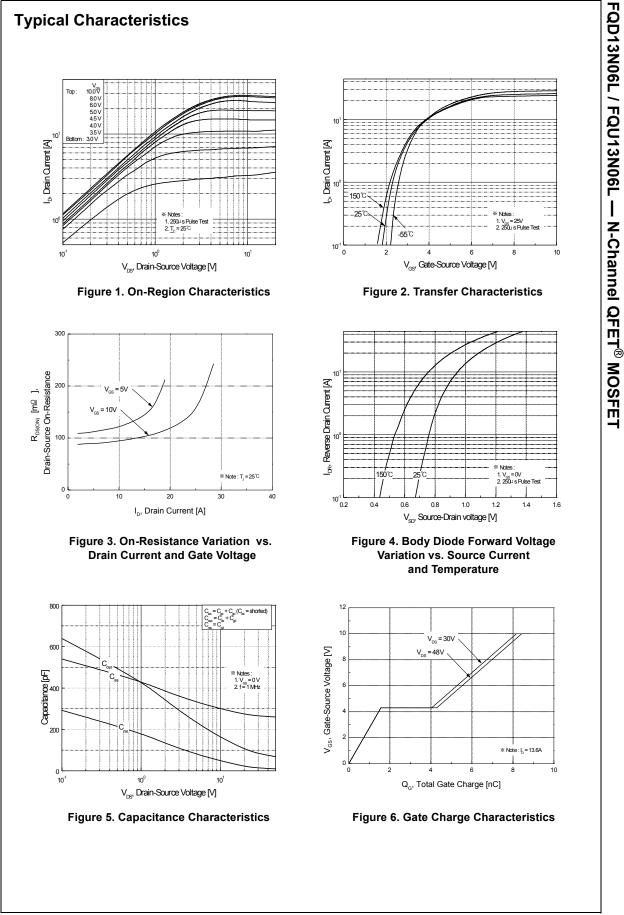
Symbol	Parameter	FQD13N06LTM / FQU13N06LTU FQU13N06LTU-WS	Unit V	
V _{DSS}	Drain-Source Voltage	60		
I _D	Drain Current - Continuous ($T_C = 25^{\circ}C$)		11	А
	- Continuous (T _C = 100°C)		7	А
I _{DM}	Drain Current - Pulsed	(Note 1)	44	А
V _{GSS}	Gate-Source Voltage		± 20	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	90	mJ
I _{AR}	Avalanche Current	(Note 1)	11	А
E _{AR}	Repetitive Avalanche Energy	(Note 1)	2.8	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	7.0	V/ns
P _D	Power Dissipation (T _A = 25°C) *	2.5	W	
	Power Dissipation (T _C = 25°C)	28	W	
	- Derate above 25°C	0.22	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C	
TL	Maximum Lead Temperature for Soldering, 1/8" from Case 5 or Seconds	300	°C	

Thermal Characteristics

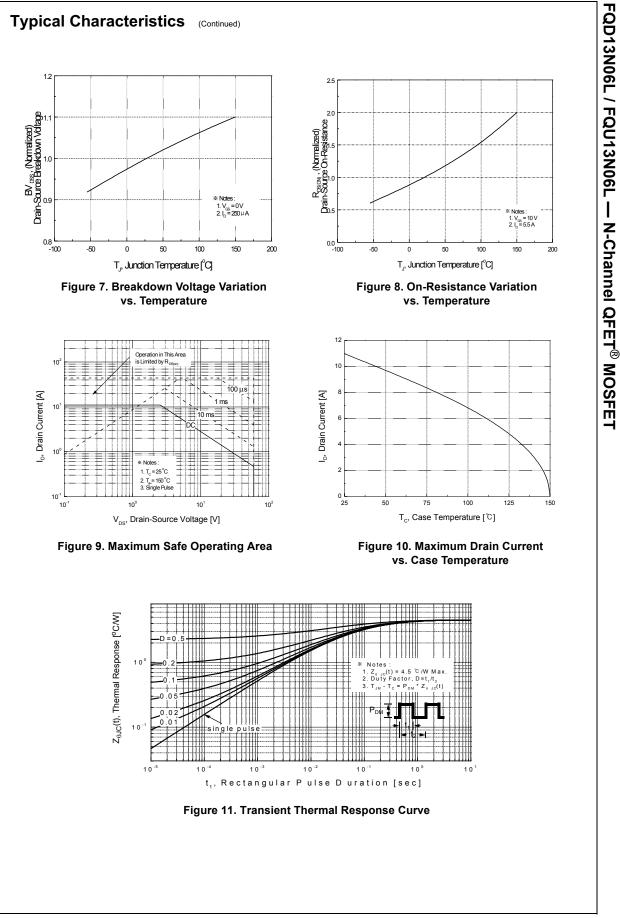
Symbol	Thermal Resistance, Junction to Case, Max. Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max	FQD13N06LTM FQU13N06LTU FQU13N06LTU-WS	Unit
$R_{ ext{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	2.5	
Р	Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.	110	°C/W
R_{\thetaJA}	Thermal Resistance, Junction to Ambient (*1 in ² Pad of 2-oz Copper), Max.	50	L

Part Number		Top Mark Pack		age Packing Method Reel		Size	Tape Width		Quantity	
		FQD13N06L	D-PAK		-		mm	16 mm		2500 units
		AK Tube N/A		A	N/A		70 units			
FQU13N06LTU-WS FQU13N06LS I-P		AK Tube N/		A N/A		75 units				
lectri	cal Chai	racteristics	T _C = 25°C un	less otherwis	e noted.					
Symbol		Parameter			Test Conditions		Min	Тур	Max	Unit
Off Cha	aracteristi	cs								
BV _{DSS}	Drain-Source Breakdown Voltage		V _{GS} = 0 V, I _D = 250 μA			60			V	
∆BV _{DSS} ∆TJ	Breakdowr Coefficient	Breakdown Voltage Temperature		$I_D = 250 \ \mu$ A, Referenced to 25°C				0.05		V/°C
DSS	Zero Gate Voltage Drain Current			V_{DS} = 60 V, V_{GS} = 0 V V_{DS} = 48 V, T_{C} = 150°C					1	μA
722									10	μΑ
GSSF	Gate-Body Leakage Current, Forward		$V_{GS} = 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$					100	nA	
GSSR		Leakage Current,			20 V, V _{DS} = 0 V				-100	nA
)n Cha	racteristi	CE					L		I	
GS(th)	1	shold Voltage		V _{DS} = V	′ _{GS} , I _D = 250 μA		1.0		2.5	V
R _{DS(on)}	Static Drain	n-Source		V _{GS} = 1	0 V, I _D = 5.5 A			0.092	0.115	0
. ,	On-Resista	ance			V, I _D = 5.5 A			0.115	0.145	Ω
IFS	Forward Tr	ansconductance		V _{DS} = 2	5 V, I _D = 5.5 A			6		S
Jvnam	ic Charac	toristics								
	Input Capa			V - 2	5 V, V _{GS} = 0 V,			270	350	pF
Soss	Output Cap			f = 1.0 N				95	125	pF
Prss		ansfer Capacitanc	е					17	23	pF
Switchi	ng Chara	otoriation					1			
d(on)	Turn-On D	cteristics						8	25	ns
r	Turn-On Ri	,			0 V, I _D = 6.8 A,			90	190	ns
d(off)	Turn-Off D			R _G = 25	0.02			20	50	ns
	Turn-Off Fa	,		-		(Note 4)		40	90	ns
λ ^g	Total Gate			$V_{} = 4$	8 V, I _D = 13.6 A,			4.8	6.4	nC
λ _{gs}	Gate-Source	•		$V_{\rm GS} = 5$	-			1.6		nC
λ ^{gg}	Gate-Drain	•		•65 0		(Note 4)		2.7		nC
	D.						1	1	1	1
Srain-S		ode Character							11	A
S SM		Pulsed Drain-Source							44	A
/ _{SD}		ce Diode Forward			V, I _S = 11 A				1.5	V
rr		ecovery Time	voltago		V, I _S = 13.6 A,			45		ns
יי 2 _m		ecovery Charge			= 100 A/μs			45		nC
L = 870 µH I _{SD} ≤ 13.6 A	, I _{AS} = 11 A, V _{DD} A, di/dt ≤ 300 A/μ	th limited by maximum ju = 25 V, $R_G = 25 \Omega$, starti s, $V_{DD} \le BV_{DSS}$, starting T operating temperature.	ng T _J = 25°C							

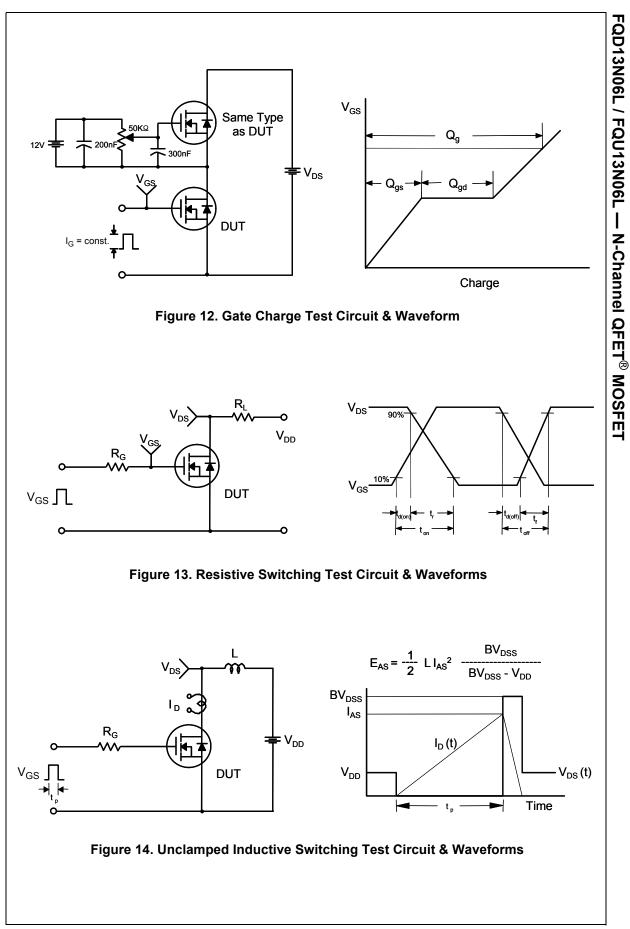
FQD13N06L / FQU13N06L — N-Channel QFET[®] MOSFET

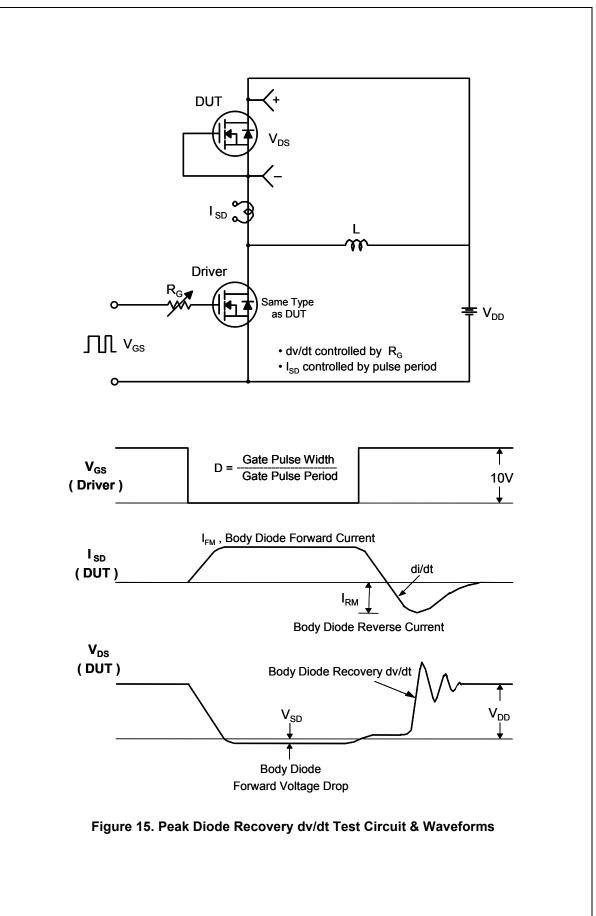


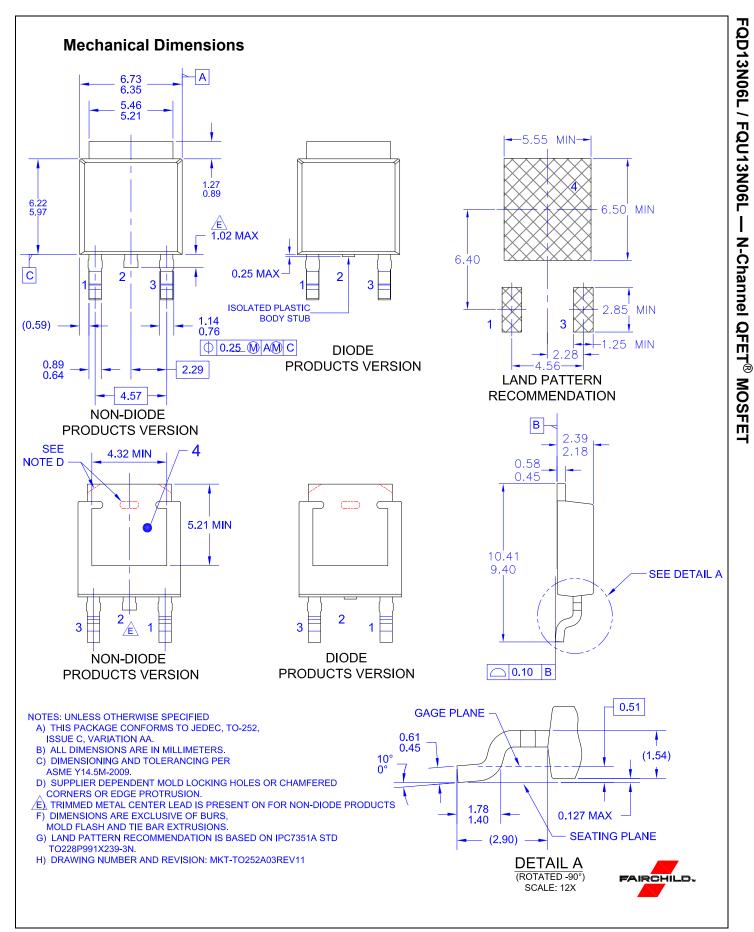
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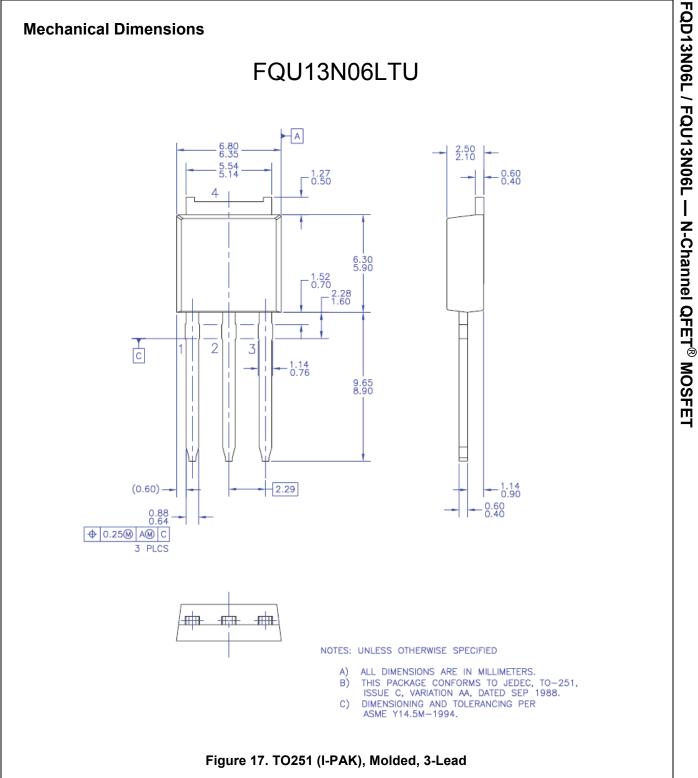


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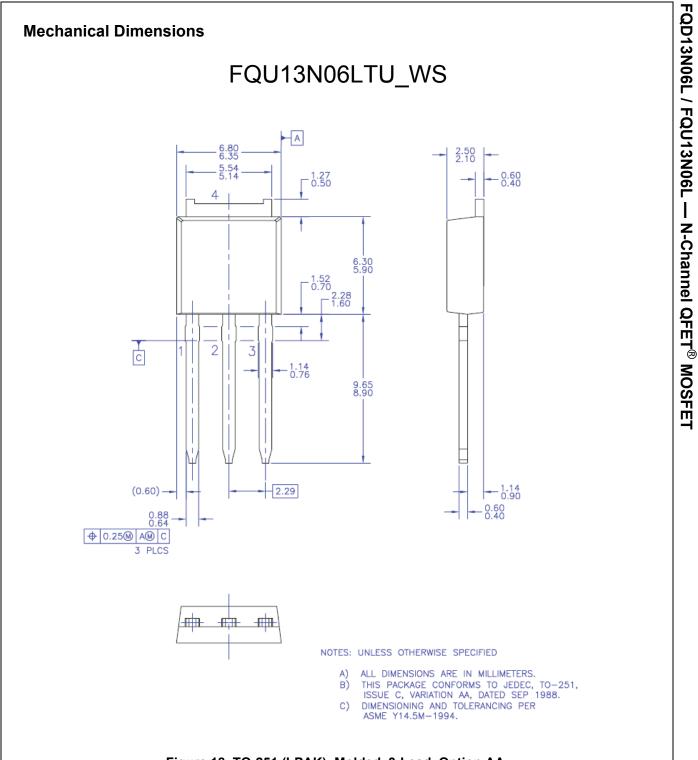


Figure 18. TO-251 (I-PAK), Molded, 3-Lead, Option AA

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