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### FQU5N50CTU-WS

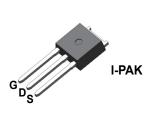
# N-Channel QFET<sup>®</sup> MOSFET 500 V, 4.0 A, 1.4 $\Omega$

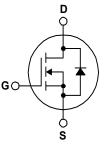
#### Features

- 4.0 A, 500 V, R<sub>DS(on)</sub> = 1.4 Ω @V<sub>GS</sub> = 10 V
- Low Gate Charge (Typ. 18 nC)
- Low Crss (Typ. 15 pF)
- · Fast Switching
- 100% Avalanche Tested
- · Improved dv/dt Capability

#### Description

This N-Channel enhancement mode power MOSFET is produced using ON Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.





#### Absolute Maximum Ratings T<sub>c</sub> = 25°C unless otherwise noted.

Symbol	Parameter	FQU5N50CTU-WS	Units	
V <sub>DSS</sub>	Drain-Source Voltage		500	V
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> = 25°C	4.0	A	
	- Continuous (T <sub>C</sub> = 100°C	2.4	A	
I <sub>DM</sub>	Drain Current - Pulsed	(Note 1)	16	Α
V <sub>GSS</sub>	Gate-Source Voltage	± 30	V	
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	300	mJ
I <sub>AR</sub>	Avalanche Current	(Note 1)	4	A
E <sub>AR</sub>	Repetitive Avalanche Energy	(Note 1)	4.8	mJ
dv/dt	Peak Diode Recovery dv/dt (Note		4.5	V/ns
PD	Power Dissipation (T <sub>C</sub> = 25°C)		48	W
	- Derate above 25°C	0.38	W/°C	
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

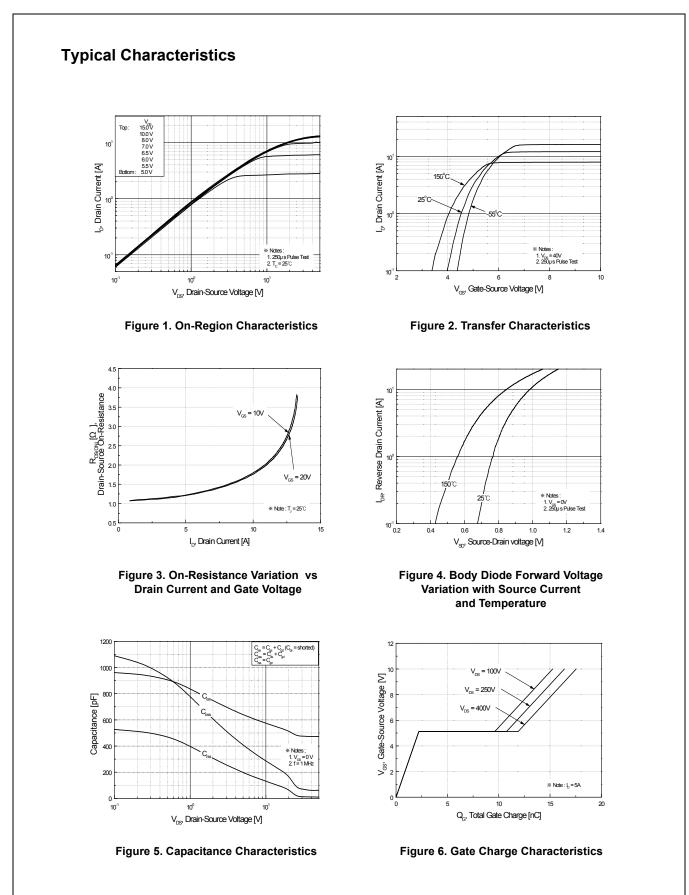
#### **Thermal Characteristics**

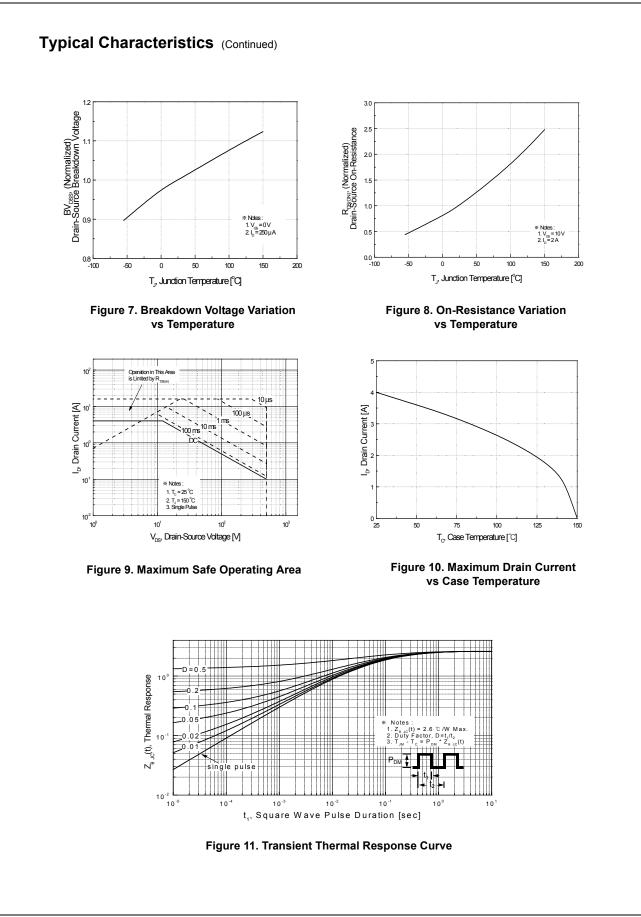
Symbol	Parameter	FQU5N50CTU_WS	Units	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	2.6	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	110	°C/W	

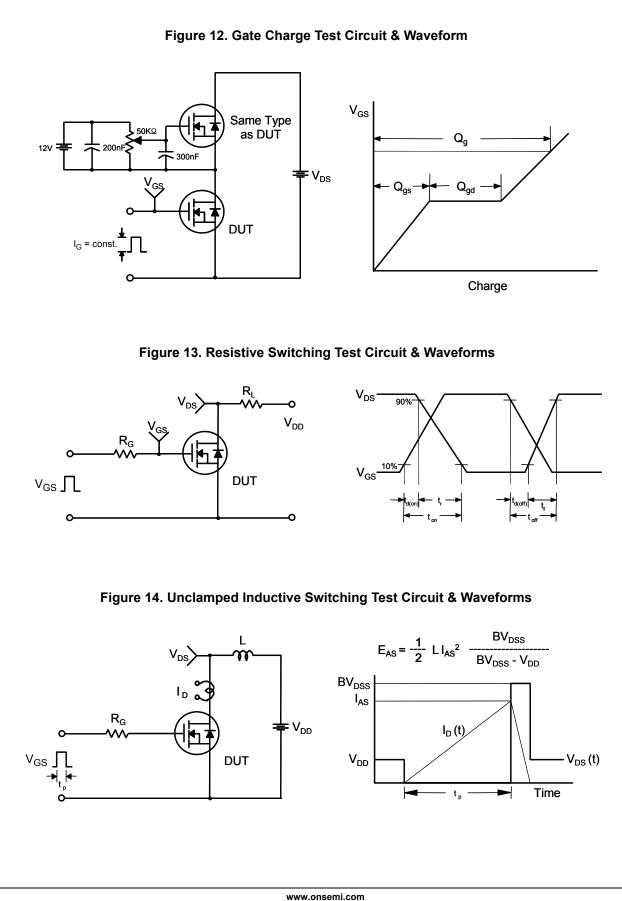
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		Top Mark	Package	ge Packing Method Reel Size		Tape Width		Quantity	
		I-PAK	K Tube N/A		N/A		75 units		
lectric	al Chara	acteristics $\tau_c$	= 25°C unles	s otherwise noted.					
Symbol	Parameter			Test Conditions		Min	Тур	Max	Units
Off Cha	racteristic	cs							
3V <sub>DSS</sub>	Drain-Source Breakdown Voltage		e ۱	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		500			V
ABV <sub>DSS</sub> /	Breakdown Voltage Temperature Coeffi- cient		o Cooffi	$I_D = 250 \ \mu\text{A}$ , Referenced to 25°C			0.5		V/°C
DSS			•	V <sub>DS</sub> = 500 V, V <sub>GS</sub> = 0 V				1	μA
	Zero Gate	Zero Gate Voltage Drain Current		$V_{DS} = 400 \text{ V}, \text{ T}_{C} = 125^{\circ}\text{C}$				10	μA
GSSF	Gate-Body	Gate-Body Leakage Current, Forward $V_{GS}$ = 30 V, $V_{DS}$ =		$V_{\rm GS}$ = 30 V, $V_{\rm DS}$ = 0 V				100	nA
GSSR	Gate-Body	Gate-Body Leakage Current, Reverse $V_{GS} = -30 \text{ V}, V_{DS} = 0 \text{ V}$						-100	nA
On Chai	racteristic	°6							
V <sub>GS(th)</sub>	Gate Threshold Voltage			V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA				4.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance			$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 2.0 \text{ A}$			1.14	1.4	Ω
FS	Forward Transconductance		\	$V_{DS} = 40 \text{ V}, I_{D} = 2.0 \text{ A}$			5.2		S
Dynami	c Charact	eristics	L.						
C <sub>iss</sub>	Input Capa	Input Capacitance		V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V,			480	625	pF
C <sub>oss</sub>	Output Capacitance			f = 1.0 MHz			80	105	pF
C <sub>rss</sub>	Reverse T	ransfer Capacitance					15	20	pF
Switchiı	ng Charao	cteristics							
d(on)	Turn-On D		1				12	35	ns
r	Turn-On R	ise Time		$V_{DD}$ = 250 V, I <sub>D</sub> = 5 A, V <sub>GS</sub> = 10 V, R <sub>G</sub> = 25 Ω			46	100	ns
d(off)	Turn-Off D	elay Time					50	110	ns
f	Turn-Off Fa	all Time			(Note 4)		48	105	ns
ל <sup>מ</sup>	Total Gate	Charge	١	/ <sub>DS</sub> = 400 V, I <sub>D</sub> = 5 A,			18	24	nC
ସୁ <sub>gs</sub>	Gate-Sour	ce Charge	١	$V_{GS} = 10 V$			2.2		nC
ຊ <sub>gd</sub>	Gate-Drain	n Charge			(Note 4)		9.7		nC
Drain S		de Characteriat	ioo and Ma	winnum Datinga					
s s s s	rain-Source Diode Characteristics and Maximum Ratings Maximum Continuous Drain-Source Diode Forward Current							4	A
S SM	Maximum Pulsed Drain-Source Diode Forw							16	A
/ <sub>SD</sub>		rce Diode Forward Vo		/ <sub>GS</sub> = 0 V, I <sub>S</sub> = 4 A				1.4	V
-rr		ecovery Time		$V_{\rm GS} = 0$ V, I <sub>S</sub> = 5 A,			263		ns
Q <sub>rr</sub>		ecovery Charge		dI <sub>F</sub> / dt = 100 A/μs	(Note 4)		1.9		μC

1. Repetitive rating : pulse width limited by maximum junction temperature. 2. L = 21.5 mH,  $I_{AS}$  = 5 A,  $V_{DD}$  = 50 V,  $R_G$  = 25  $\Omega$ , starting  $T_J$  = 25°C. 3.  $I_{SD} \le 5$  A, di/dt  $\le 200$  A/µs,  $V_{DD} \le BV_{DSS}$ , starting  $T_J$  = 25°C. 4. Essentially independent of operating temperature.







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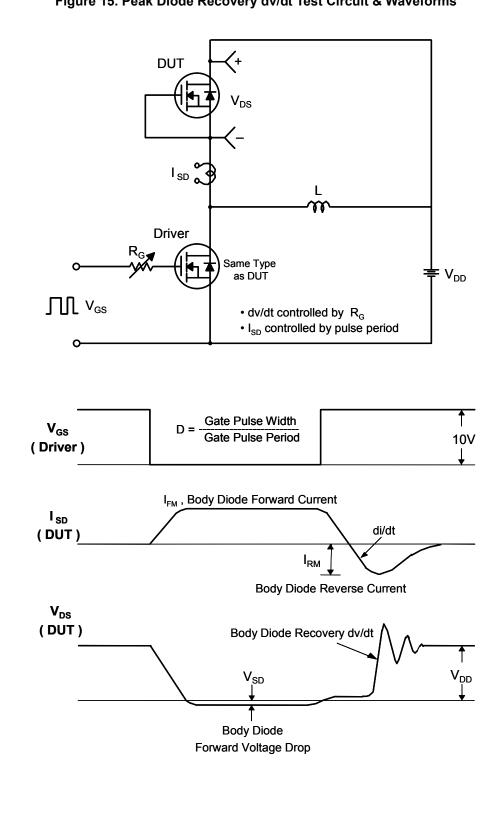


Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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