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FQP6N60C/FQPF6N60C 600V N-Channel MOSFET

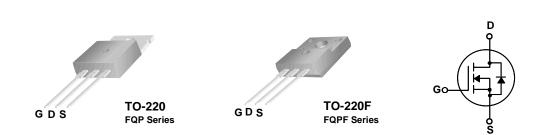
General Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switched mode power supplies, active power factor correction, electronic lamp ballasts based on half bridge topology.

Features

- + 5.5A, 600V, $R_{DS(on)}$ = 2.0 Ω @V_{GS} = 10 V + Low gate charge (typical 16 nC)
- Low Crss (typical 7 pF) •
- Fast switching
- · 100% avalanche tested
- · Improved dv/dt capability



Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter		FQP6N60C	FQPF6N60C	Units
V _{DSS}	Drain-Source Voltage		600		V
I _D	Drain Current - Continuous ($T_C = 25^{\circ}C$)		5.5	5.5 *	А
	- Continuous (T _C = 100°C)		3.3	3.3 *	А
I _{DM}	Drain Current - Pulsed	(Note 1)	22	22 *	А
V _{GSS}	Gate-Source Voltage		± 30		V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	300		mJ
I _{AR}	Avalanche Current	(Note 1)	5.5		А
E _{AR}	Repetitive Avalanche Energy (Note		12.5		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)		4.5		V/ns
P _D	Power Dissipation (T _C = 25°C) - Derate above 25°C		125	40	W
			1.0	0.31	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150		°C
TL	Maximum lead temperature for soldering purposes,		300		°C
۲L	1/8" from case for 5 seconds				
Orain current lim	nited by maximum junction temperature.			· · · · ·	

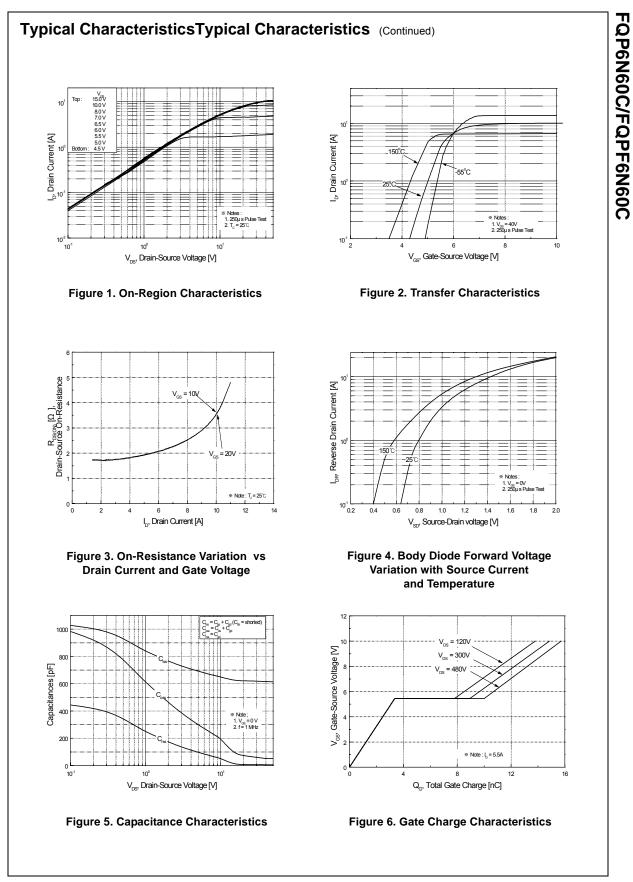
Thermal Characteristics

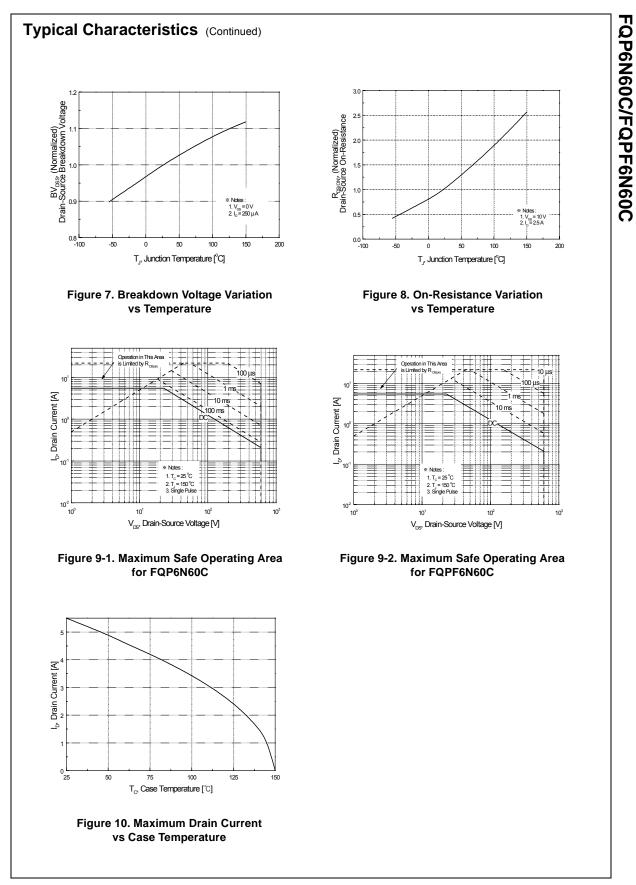
Symbol	Parameter	FQP6N60C	FQPF6N60C	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.0	3.2	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.5		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	62.5	°C/W

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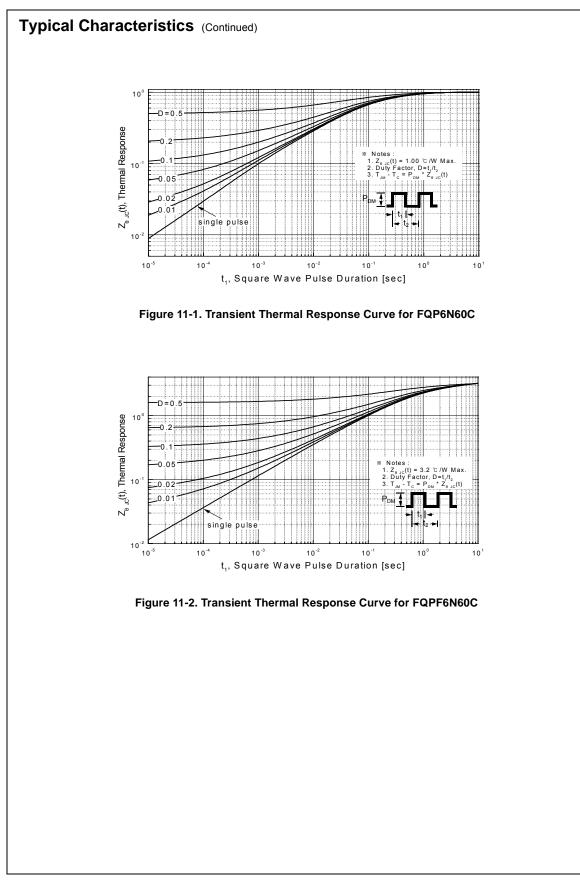
ŲFET®

racteristics Drain-Source Breakdown Voltage Breakdown Voltage Temperature Coefficient Zero Gate Voltage Drain Current	Voc = 0 V Ip = 250 µA				
Drain-Source Breakdown Voltage Breakdown Voltage Temperature Coefficient	$V_{CS} = 0 V I_{D} = 250 \mu A$				
Coefficient	V _{GS} = 0 V, I _D = 250 μA				V
Zero Gate Voltage Drain Current	I_D = 250 µA, Referenced to 25°C		0.6		V/°C
Zero Odle Vollage Drain Ourrent	$V_{DS} = 600 \text{ V}, V_{GS} = 0 \text{ V}$ $V_{DS} = 480 \text{ V}, T_{C} = 125^{\circ}\text{C}$			1	μA
Cata Rody Loakage Current Ferward	$V_{\rm DS} = 400$ V, $V_{\rm C} = 125$ C $V_{\rm GS} = 30$ V, $V_{\rm DS} = 0$ V			10 100	μA nA
Gate-Body Leakage Current, Forward Gate-Body Leakage Current, Reverse	$V_{GS} = -30 V, V_{DS} = 0 V$			-100	nA
				-100	ПА
racteristics					
	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$			4.0	V
Static Drain-Source On-Resistance	V_{GS} = 10 V, I _D = 2.75 A		1.7	2.0	Ω
Forward Transconductance	V _{DS} = 40 V, I _D = 2.75 A (Note 4)		4.8		S
				L	1
	25.11.11 0.11		620	810	pF
					pF
	f = 1.0 MHZ				pF
			15	40	ns
	V _{DD} = 300 V, I _D = 5.5A,		10		
Turn On Dian Time			45		
Turn-On Rise Time	$R_{G} = 25 \Omega$		45 45	100	ns
Turn-Off Delay Time	R _G = 25 Ω		45	100 100	ns ns
Turn-Off Delay Time Turn-Off Fall Time	$R_G = 25 \Omega$ (Note 4, 5)		45 45	100 100 100	ns ns ns
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge	R_{G} = 25 Ω (Note 4, 5) V_{DS} = 480 V, I _D = 5.5A,		45 45 16	100 100 100 20	ns ns ns nC
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge	$R_{G} = 25 \Omega$ (Note 4, 5) V _{DS} = 480 V, I _D = 5.5A, V _{GS} = 10 V	 	45 45 16 3.5	100 100 100 20 	ns ns ns nC nC
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge	$R_{G} = 25 \Omega$ (Note 4, 5) V _{DS} = 480 V, I _D = 5.5A, V _{GS} = 10 V (Note 4, 5)		45 45 16	100 100 100 20	ns ns ns nC
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge	$R_{G} = 25 \ \Omega$ $(Note 4, 5)$ $V_{DS} = 480 \ V, \ I_{D} = 5.5 A,$ $V_{GS} = 10 \ V$ $(Note 4, 5)$ $(Note 4, 5)$	 	45 45 16 3.5	100 100 100 20 	ns ns ns nC nC
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge ource Diode Characteristics ar	$R_{G} = 25 \Omega$ (Note 4, 5) $V_{DS} = 480 V, I_{D} = 5.5A,$ $V_{GS} = 10 V$ (Note 4, 5) (N	 	45 45 16 3.5 6.5	100 100 20 	ns ns nC nC nC
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge ource Diode Characteristics ar Maximum Continuous Drain-Source Dio	$R_{G} = 25 \Omega$ (Note 4, 5) $V_{DS} = 480 V, I_{D} = 5.5A,$ $V_{GS} = 10 V$ (Note 4, 5) (N		45 45 16 3.5 6.5	100 100 20 5.5	ns ns nC nC nC
Turn-Off Delay Time Turn-Off Fall Time Total Gate Charge Gate-Source Charge Gate-Drain Charge ource Diode Characteristics ar Maximum Continuous Drain-Source Diode Maximum Pulsed Drain-Source Diode F	$R_{G} = 25 \Omega$ (Note 4, 5) $V_{DS} = 480 V, I_{D} = 5.5A,$ $V_{GS} = 10 V$ (Note 4, 5) (N		45 45 16 3.5 6.5 	100 100 20 5.5 22	ns ns nC nC nC A A
	Gate Threshold Voltage Static Drain-Source On-Resistance	Gate Threshold Voltage $V_{DS} = V_{GS}$, $I_D = 250 \mu A$ Static Drain-Source $V_{GS} = 10 V$, $I_D = 2.75 A$ On-Resistance $V_{DS} = 40 V$, $I_D = 2.75 A$ Forward Transconductance $V_{DS} = 40 V$, $I_D = 2.75 A$ (Note 4) C C Characteristics Input Capacitance Input Capacitance $V_{DS} = 25 V$, $V_{GS} = 0 V$, $f = 1.0 \text{MHz}$ Reverse Transfer Capacitance f = 1.0 \text{MHz} ng Characteristics Turn-On Delay Time	Gate Threshold Voltage $V_{DS} = V_{GS}$, $I_D = 250 \mu\text{A}$ 2.0 Static Drain-Source $V_{GS} = 10 \text{V}$, $I_D = 2.75 \text{A}$ On-Resistance $V_{DS} = 40 \text{V}$, $I_D = 2.75 \text{A}$ Forward Transconductance $V_{DS} = 40 \text{V}$, $I_D = 2.75 \text{A}$ c Characteristics Input Capacitance $V_{DS} = 25 \text{V}$, $V_{GS} = 0 \text{V}$, Output Capacitance $f = 1.0 \text{MHz}$ ng Characteristics Turn-On Delay Time	Gate Threshold Voltage $V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$ 2.0Static Drain-Source On-Resistance $V_{GS} = 10 \ V$, $I_D = 2.75 \ A$ 1.7Forward Transconductance $V_{DS} = 40 \ V$, $I_D = 2.75 \ A$ 4.8C CharacteristicsInput Capacitance $V_{DS} = 25 \ V$, $V_{GS} = 0 \ V$, Output Capacitance620Output Capacitance $f = 1.0 \ MHz$ 65Reverse Transfer Capacitance7Input CapacitanceTurn-On Delay Time15	Gate Threshold Voltage $V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$ 2.0 4.0 Static Drain-Source $V_{GS} = 10 \ V$, $I_D = 2.75 \ A$ 1.7 2.0 On-Resistance $V_{DS} = 40 \ V$, $I_D = 2.75 \ A$ 1.7 2.0 Forward Transconductance $V_{DS} = 40 \ V$, $I_D = 2.75 \ A$ 4.8 c Characteristics 4.8 Input Capacitance $V_{DS} = 25 \ V$, $V_{GS} = 0 \ V$, Output Capacitance 620 \ 810 Output Capacitance f = 1.0 \ MHz 65 \ 85 Reverse Transfer Capacitance 7 \ 10 ng Characteristics 40

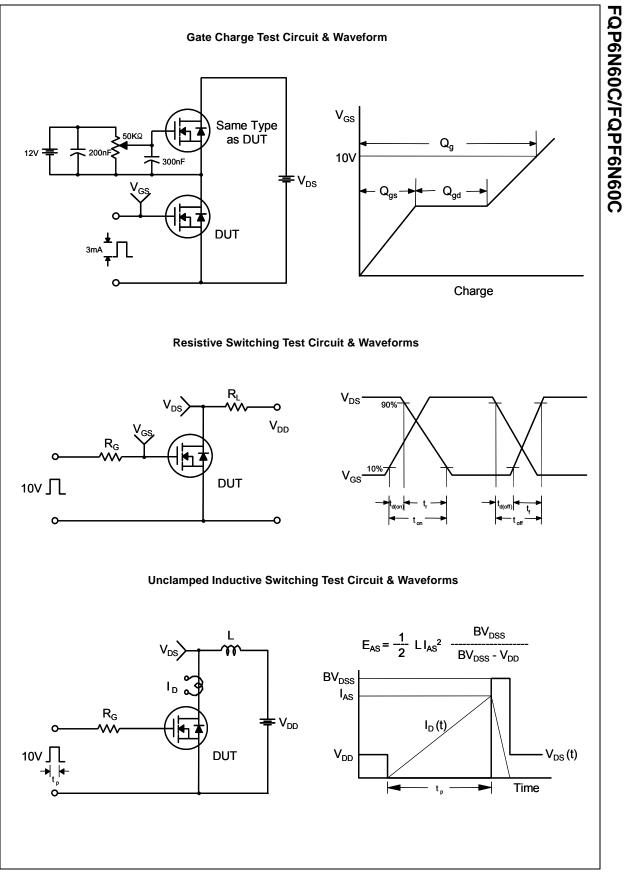


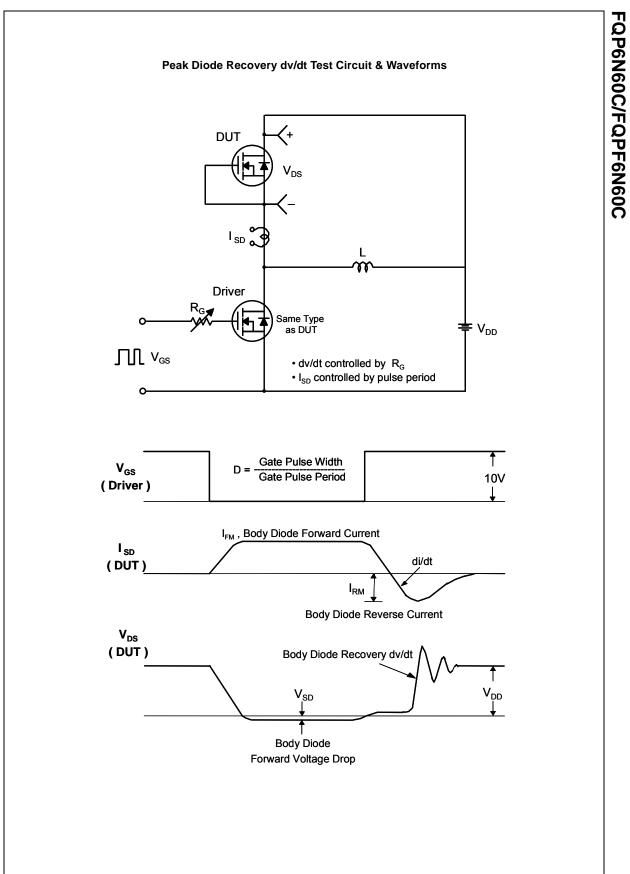


Rev. A, March 2004

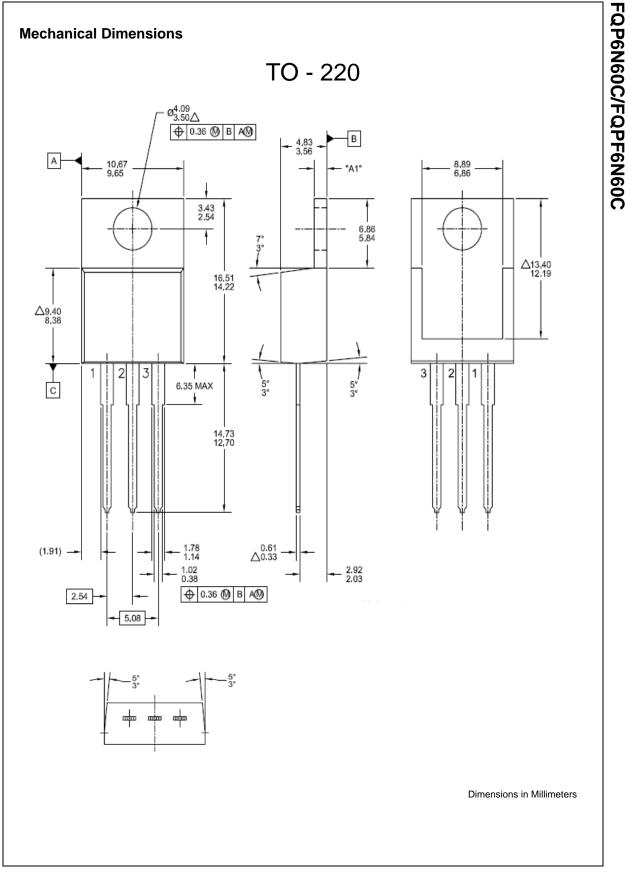


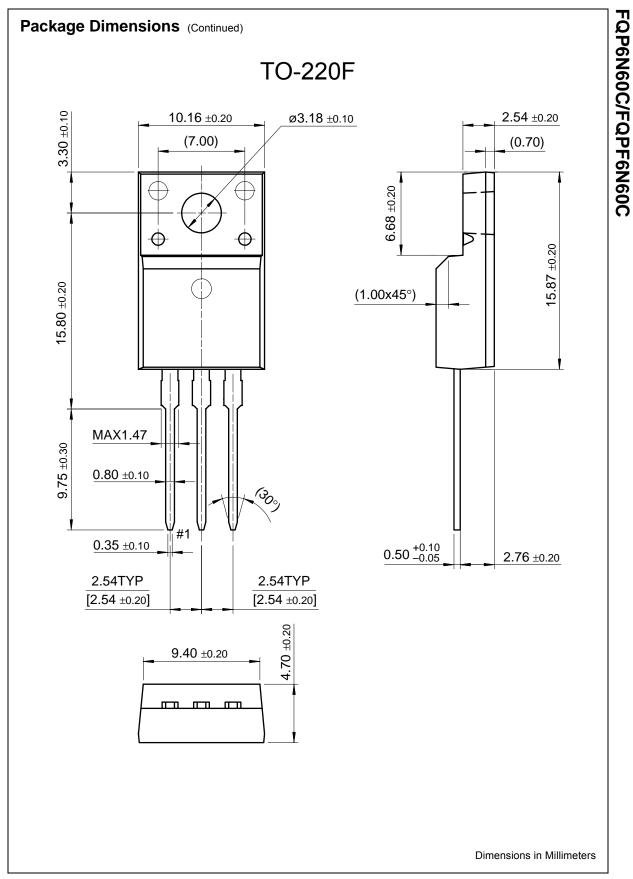
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