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November 2013

FQPF11N50CF

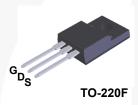
N-Channel QFET® FRFET® MOSFET 500 V, 11 A, 550 m Ω

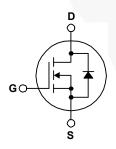
Description

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- 11 A, 500 V, $R_{DS(on)}$ = 550 m Ω (Max.) @ V_{GS} = 10 V, I_D = 5.5 A
- Low Gate Charge (Typ. 43 nC)
- Low C_{rss} (Typ. 20 pF)
- · 100% Avalanche Tested
- · Fast Recovery Body Diode





Absolute Maximum Ratings T_C = 25°C unless otherwise noted.

Symbol	Parameter	FQPF11N50CF	Unit	
V_{DSS}	Drain-Source Voltage	500	V	
I _D	Drain Current - Continuous (T _C = 25°C)	11*	Α	
	- Continuous (T _C = 100°C)	7*	Α	
I _{DM}	Drain Current - Pulsed (Note 1)	44*	Α	
V _{GSS}	Gate-Source voltage	± 30	V	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)	670	mJ	
I _{AR}	Avalanche Current (Note 1)	11	Α	
E _{AR}	Repetitive Avalanche Energy (Note 1)	19.5	mJ	
dv/dt	Peak Diode Recovery dv/dt (Note 3)	4.5	V/ns	
P_{D}	Power Dissipation (T _C = 25°C)	48	W	
	- Derate above 25°C	0.39	W/°C	
T _{J,} T _{STG}	Operating and Storage Temperature Range	-55 to +150	°C	
T _L	Maximum Lead Temperature for Soldering Purpose, 1/8" from Case for 5 Seconds	300	°C	

^{*}Drain current limited by maximum junction temperature

Thermal Characteristics

Symbol	Parameter	FQPF11N50CF	Unit
$R_{\theta JC}$	nal Resistance, Junction to Case, Max. 2.58		00.11
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FQPF11N50CF	FQPF11N50CF	TO-220F	Tube	N/A	50 units

Electrical Characteristics T_C = 25°C unless otherwise noted.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Off Charac	teristics			•		
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	500			V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C		0.5		V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 500 V, V _{GS} = 0 V			10	μА
		V _{DS} = 400 V, T _C = 125°C			100	μА
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V			-100	nA
On Charac	teristics					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	٧
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 5.5 A		0.48	0.55	Ω
9 _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 5.5 A		15		S
Dynamic C	Characteristics			I.		
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V,		1515	2055	pF
C _{oss}	Output Capacitance	f = 1.0 MHz	\	185	235	pF
C _{rss}	Reverse Transfer Capacitance			25	30	pF
Switching	Characteristics					
t _{d(on)}	Turn-On Delay Time	V _{DD} = 250 V, I _D = 11 A		24	57	ns
t _r	Turn-On Rise Time	$R_G = 25 \Omega$		70	150	ns
t _{d(off)}	Turn-Off Delay Time			120	250	ns
t _f	Turn-Off Fall Time	(Note 4)		75	160	ns
Qg	Total Gate Charge	V _{DS} = 400 V, I _D = 11 A V _{GS} = 10 V		43	55	nC
Q _{gs}	Gate-Source Charge			8		nC
Q _{gd}	Gate-Drain Charge	(Note 4)	/	19		nC
Drain-Soul	rce Diode Characteristics and Maximu	m Ratings		l		
I _S	Maximum Continuous Drain-Source Diode Forward Current				11	Α
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current				44	Α
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 11 A			1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 11 A		90	/	ns
Q _{rr}	Reverse Recovery Charge	dI _F /dt =100 A/μs		1.5		μС

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 10 mH, I $_{AS}$ = 11 A, V $_{DD}$ = 50 V, R $_{G}$ = 25 Ω , starting T $_{J}$ = 25°C
- 3. $I_{SD} \le$ 11 A, di/dt \le 200 A/ μ s, $V_{DD} \le BV_{DSS,}$ starting T_J = 25°C
- 4. Essentially independent of operating temperature.

Typical Performance Characteristics

Figure 1. On-Region Characteristics

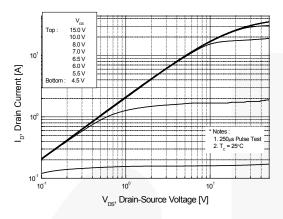


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

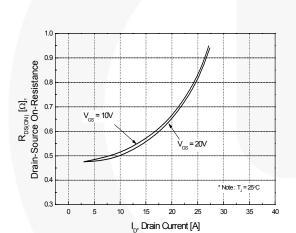


Figure 5. Capacitance Characteristics

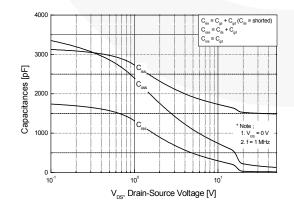


Figure 2. Transfer Characteristics

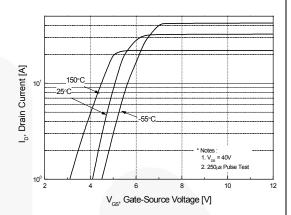


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperatue

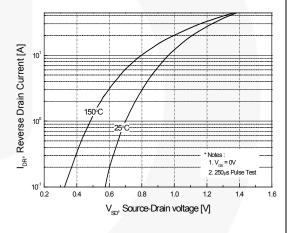
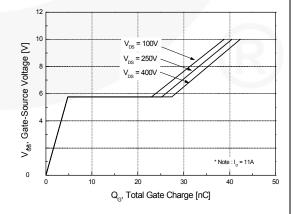


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

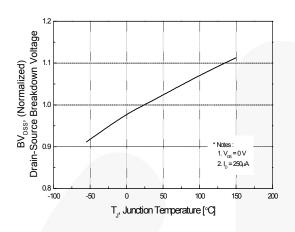


Figure 8. On-Resistance Variation vs. Temperature

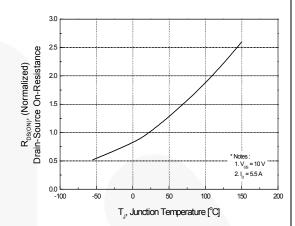
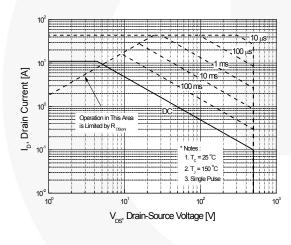


Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature



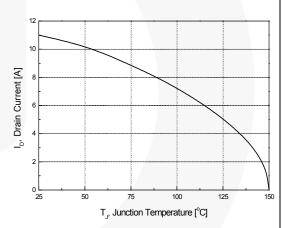
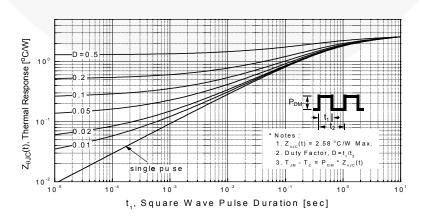


Figure 11. Transient Thermal Response Curve





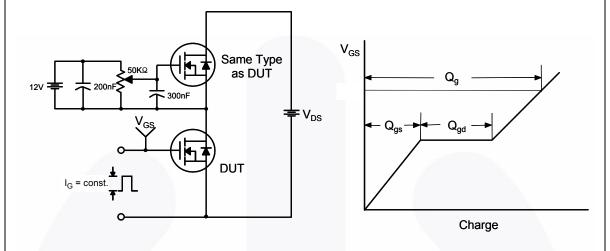


Figure 13. Resistive Switching Test Circuit & Waveforms

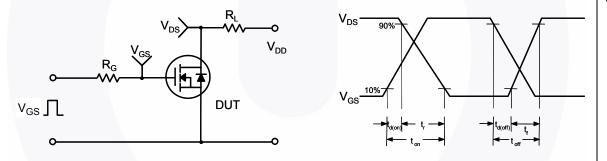
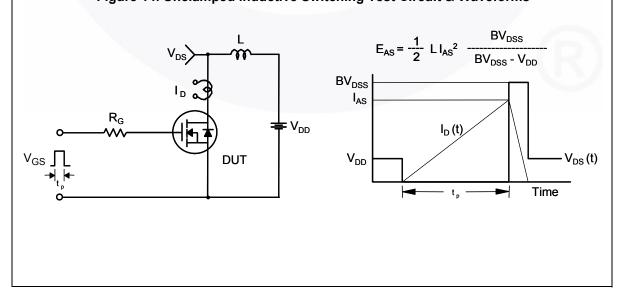
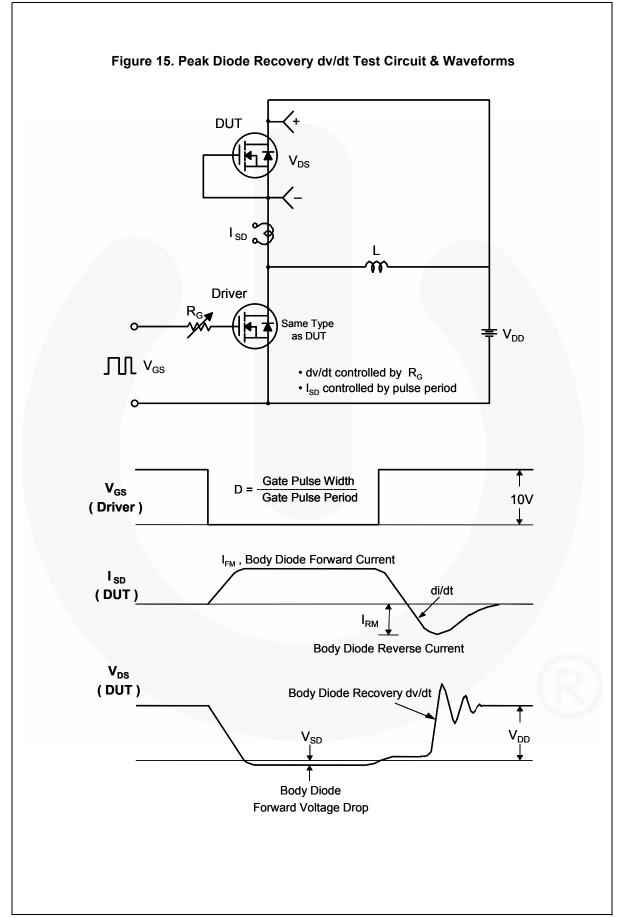


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms





Mechanical Dimensions

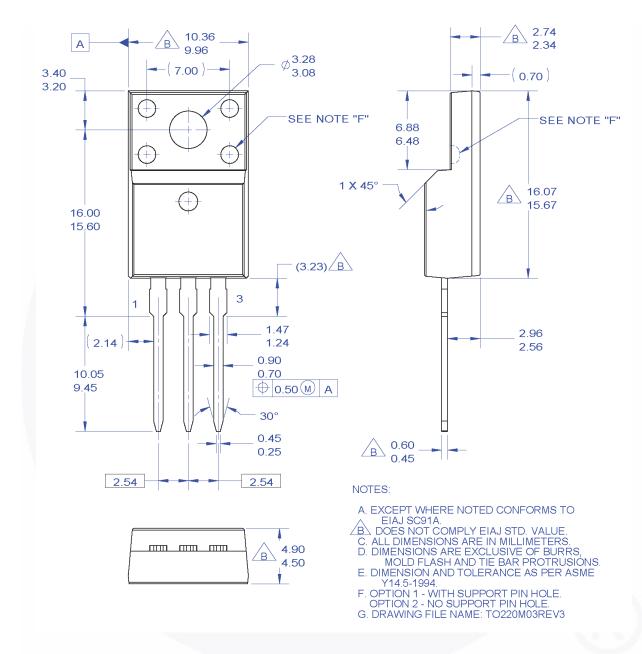


Figure 16. TO220, Molded, 3-Lead, Full Pack, EIAJ SC91, Straight Lead

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