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FQPF47P06 / FQPF47P06YDTU P-Channel QFET[®] MOSFET -60 V, -30 A, 26 mΩ

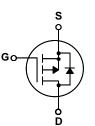
Description

This P-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor®'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

Features

- -30 A, -60 V, $R_{DS(on)}$ =26 m Ω (Max.) @V_{GS}=-10 V, I_D=-15 A
- Low Gate Charge (Typ. 84 nC)
- Low Crss (Typ. 320 pF)
- 100% Avalanche Tested
- 175°C Maximum Junction Temperature Rating





Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter		FQPF47P06 / FQPF47P06YDTU	Unit
V _{DSS}	Drain-Source Voltage		-60	V
I _D	Drain Current - Continuous ($T_C = 25^{\circ}C$))	-30	А
	- Continuous (T _C = 100°C	C)	-21.2	А
I _{DM}	Drain Current - Pulsed	(Note 1)	-120	А
V _{GSS}	Gate-Source Voltage		± 25	V
E _{AS}	Single Pulsed Avalanche Energy	(Note 2)	820	mJ
I _{AR}	Avalanche Current	(Note 1)	-30	А
E _{AR}	Repetitive Avalanche Energy	(Note 1)	6.2	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	-7.0	V/ns
P _D	Power Dissipation ($T_C = 25^{\circ}C$)		62	W
	- Derate above 25°C		0.41	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range	е	-55 to +175	°C
ΤL	Maximum lead temperature for soldering p 1/8" from case for 5 seconds	urposes,	300	°C

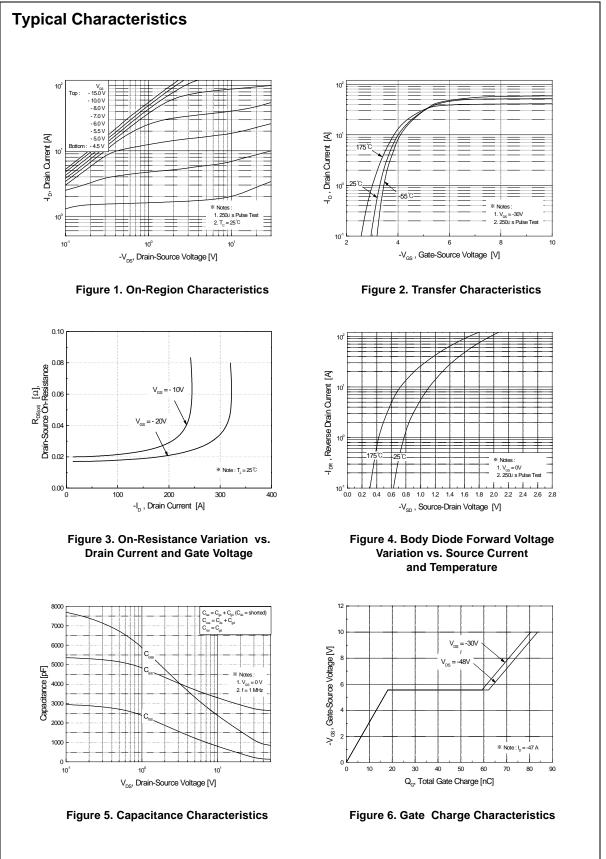
Thermal Characteristics

Symbol	Parameter	Тур	Max	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-to-Case		2.42	°C/W
R_{\thetaJA}	Thermal Resistance, Junction-to-Ambient		62.5	°C/W

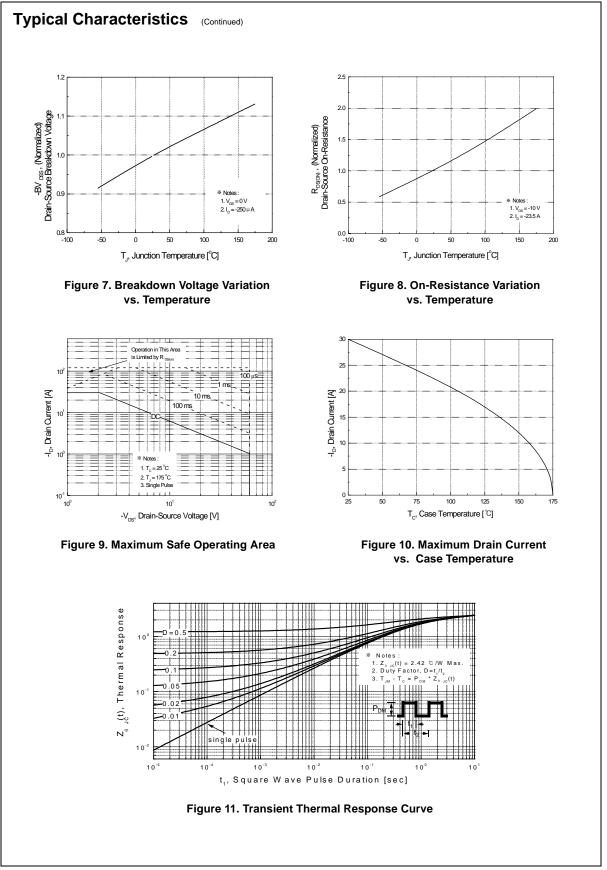
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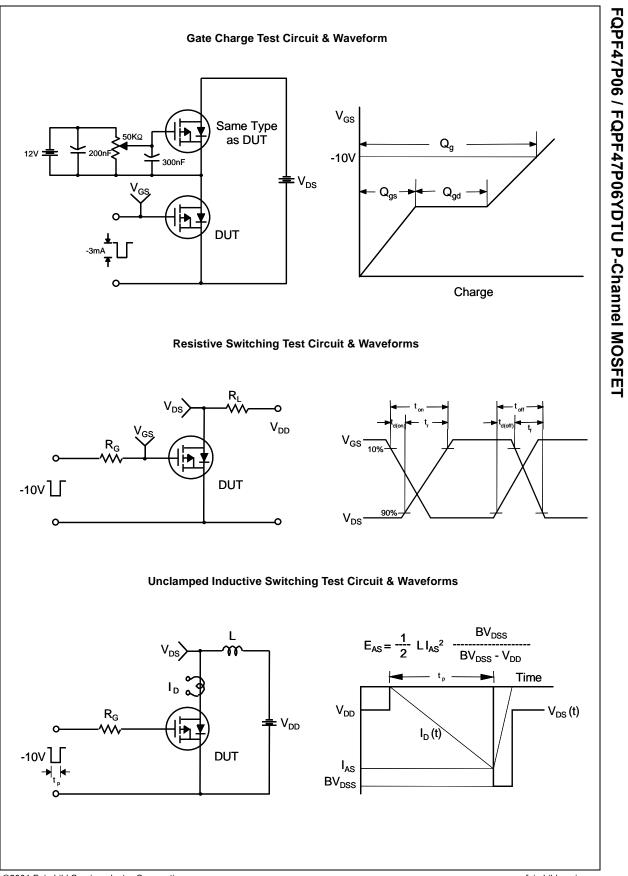
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Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Off Cha	racteristics					
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = -250 μA	-60			V
ΔBV _{DSS} ΔT _J	Breakdown Voltage Temperature Coefficient	$I_D = -250 \ \mu\text{A}$, Referenced to 25°C		-0.06		V/°C
DSS	Zero Gate Voltage Drain Current	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			-1	μA
		V _{DS} = -48 V, T _C = 150°C			-10	μA
GSSF	Gate-Body Leakage Current, Forward	$V_{GS} = -25 V, V_{DS} = 0 V$			-100	nA
GSSR	Gate-Body Leakage Current, Reverse	$V_{GS} = 25 \text{ V}, V_{DS} = 0 \text{ V}$			100	nA
On Cha	racteristics					
/ _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = -250 μA	-2.0		-4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -15 \text{ A}$		0.021	0.026	Ω
ĴFS	Forward Transconductance	$V_{DS} = -30 \text{ V}, \text{ I}_{D} = -15 \text{ A}$ (Note 4)		19		S
_			1	1		
	ic Characteristics		1			_
C _{iss}	Input Capacitance	$V_{DS} = -25 V, V_{GS} = 0 V,$		2800	3600	pF
Coss Crss	Output Capacitance Reverse Transfer Capacitance	f = 1.0 MHz		1300 320	1700 420	pF pF
d(on)	ng Characteristics Turn-On Delay Time Turn-On Rise Time	$V_{DD} = -30 \text{ V}, \text{ I}_{D} = -23.5 \text{ A},$		50 450	110 910	ns ns
r	Turn-On Rise Time	$V_{DD} = -30 \text{ V}, I_D = -23.5 \text{ A},$ $R_G = 25 \Omega$		450	910	ns
d(off)	Turn-Off Delay Time	Ŭ		100	210	ns
f	Turn-Off Fall Time	(Note 4, 5)		195	400	ns
ე ^g	Total Gate Charge	$V_{DS} = -48 \text{ V}, \text{ I}_{D} = -47 \text{ A},$		84	110	nC
2 _{gs}	Gate-Source Charge	V _{GS} = -10 V		18		nC
ე _{gd}	Gate-Drain Charge	(Note 4, 5)		44		nC
Drain S	ource Diode Characteristics a	ad Maximum Patinga				
s	Maximum Continuous Drain-Source Did	•			-30	А
S SM	Maximum Pulsed Drain-Source Diode F				-120	A
/ _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, \text{ I}_{S} = -30 \text{ A}$			-4.0	V
rr	Reverse Recovery Time	$V_{GS} = 0 \text{ V}, \text{ I}_{S} = -47 \text{ A},$		130		ns
ີ ຊ _{rr}	Reverse Recovery Charge	$dI_{\rm F} / dt = 100 \text{ A}/\mu \text{s}$ (Note 4)		0.55		μC
ptes: Repetitive R L = 1.06mH, $I_{SD} \leq -47A$, Pulse Test :	ating : Pulse width limited by maximum junction tempe $I_{AS} = -30A, V_{DD} = -25V, R_G = 25 \Omega$, Starting $T_J = 25^{\circ}C$ di/dt $\leq 300A/\mu s, V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^{\circ}C$ Pulse width $\leq 300 \mu s$, Duty cycle $\leq 2\%$ independent of operating temperature	rature	<u> </u>	0.00	<u> </u>	<u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u><u></u></u>



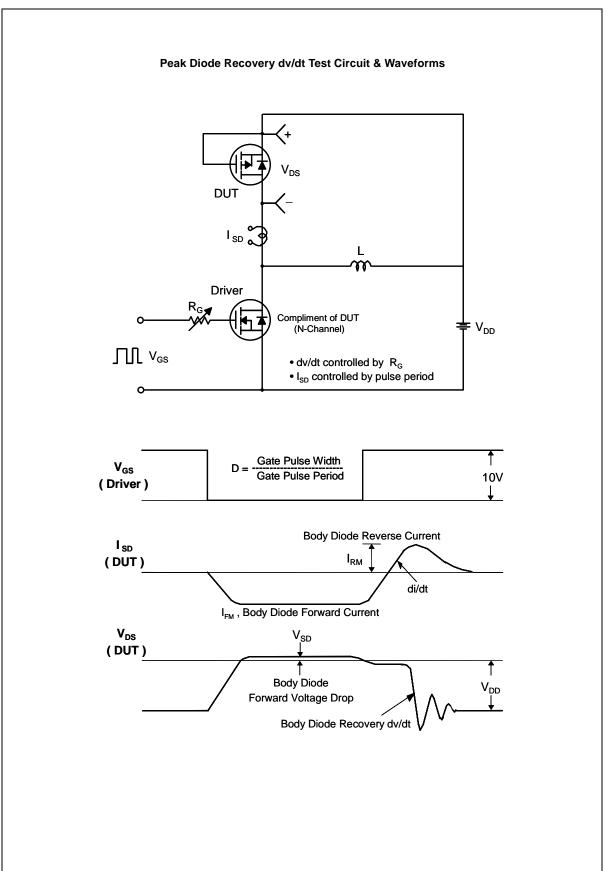
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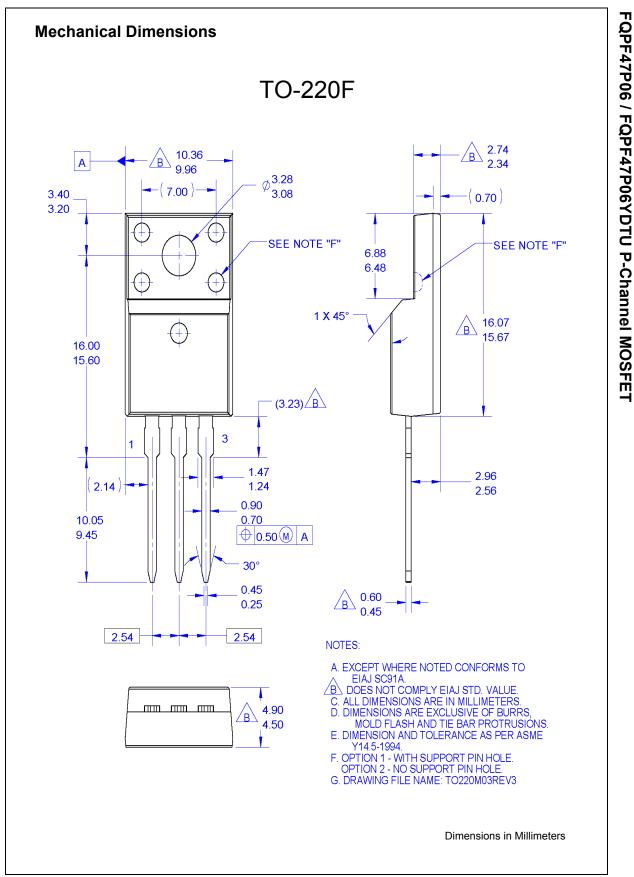




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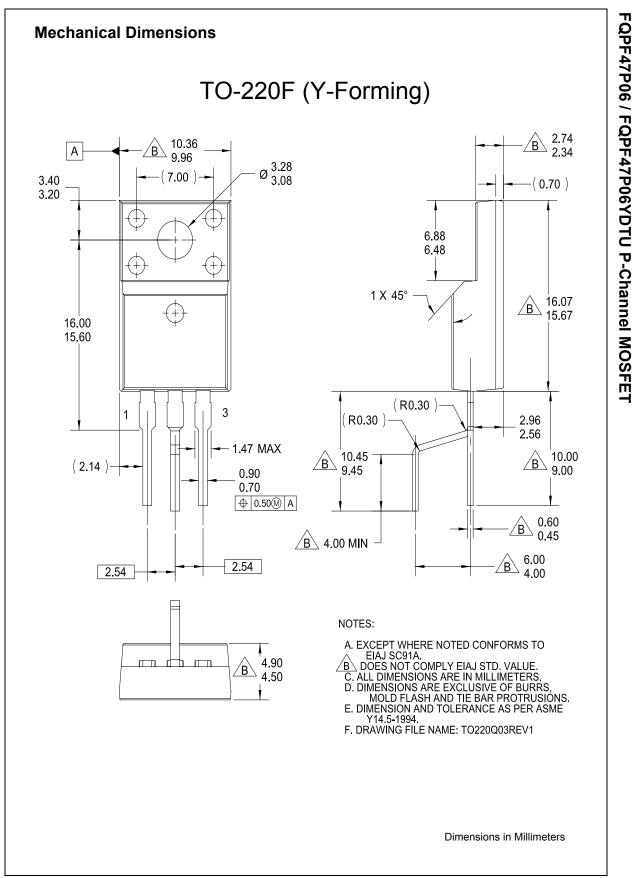
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