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November 2013

# FQPF70N10

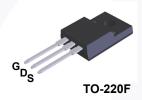
# N-Channel QFET<sup>®</sup> MOSFET 100 V, 35 A, 23 m $\Omega$

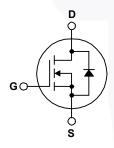
### **Description**

This N-Channel enhancement mode power MOSFET is produced using Fairchild Semiconductor's proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.

### **Features**

- 35 A, 100 V,  $R_{DS(on)}$  = 23 m $\Omega$  (Max.) @  $V_{GS}$  = 10 V,  $I_D$  = 17.5 A
- Low Gate Charge (Typ. 85 nC)
- Low Crss (Typ. 150 pF)
- · 100% Avalanche Tested
- · 175°C Maximum Junction Temperature Rating





## Absolute Maximum Ratings T<sub>C</sub> = 25°C unless otherwise noted.

Symbol	Parameter		FQPF70N10	Unit
V <sub>DSS</sub>	Drain-Source Voltage		100	V
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> = 25°C	C)	35	Α
	- Continuous (T <sub>C</sub> = 100°	°C)	24.7	Α
I <sub>DM</sub>	Drain Current - Pulsed	(Note 1)	140	Α
$V_{GSS}$	Gate-Source Voltage		± 25	V
E <sub>AS</sub>	Single Pulsed Avalanche Energy	(Note 2)	1300	mJ
I <sub>AR</sub>	Avalanche Current	(Note 1)	35	Α
E <sub>AR</sub>	Repetitive Avalanche Energy	(Note 1)	6.2	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	6.0	V/ns
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> = 25°C)		62	W
	- Derate above 25°C		0.41	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range		-55 to +175	°C
TL	Maximum Lead Temperature for Soldering	ng, 300		°C
٠.٢	1/8" from Case for 5 seconds		300	

### **Thermal Characteristics**

Symbol	Parameter	FQPF70N10	Unit	
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case, Max.	2.42	°C/W	
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient, Max.	62.5	°C/W	

# **Package Marking and Ordering Information**

Part Number	Top Mark	Package	Packing Method	Reel Size	Tape Width	Quantity
FQPF70N10	FQPF70N10	TO-220F	Tube	N/A	N/A	50 units

### **Electrical Characteristics**

T<sub>C</sub> = 25°C unless otherwise noted.

Symbol	Parameter	Test Conditions	Min	Тур	Max	Uni
Off Cha	racteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100			V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 μA, Referenced to 25°C		0.1		V/°(
I <sub>DSS</sub> Zero Gate Voltage Drain Current	Zara Cata Valtaga Drain Current	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1	μΑ
	V <sub>DS</sub> = 80 V, T <sub>C</sub> = 150°C			10	μΑ	
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 25 V, V <sub>DS</sub> = 0 V			100	nΑ
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = - 25 V, V <sub>DS</sub> = 0 V			-100	nA
On Cha	racteristics					
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.0		4.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 17.5 A		0.019	0.023	Ω
9 <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 40 V, I <sub>D</sub> = 17.5 A	\	38		S
Dynam	ic Characteristics					
	ic Ollaracteristics					
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V,		2500	3300	pF
C <sub>iss</sub>		$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0 MHz		2500 720	3300 940	_ '
C <sub>iss</sub> C <sub>oss</sub> C <sub>rss</sub>	Input Capacitance					pF pF
C <sub>iss</sub> C <sub>oss</sub> C <sub>rss</sub>	Input Capacitance Output Capacitance			720	940	pF
C <sub>iss</sub> C <sub>oss</sub> C <sub>rss</sub>	Input Capacitance Output Capacitance Reverse Transfer Capacitance	f = 1.0 MHz		720	940	pF
C <sub>iss</sub> C <sub>oss</sub> C <sub>rss</sub>	Input Capacitance Output Capacitance Reverse Transfer Capacitance  ng Characteristics	f = 1.0 MHz V <sub>DD</sub> = 50 V, I <sub>D</sub> = 70 A,		720 150	940	pF pF
$C_{iss}$ $C_{oss}$ $C_{rss}$ Switchi $t_{d(on)}$ $t_r$	Input Capacitance Output Capacitance Reverse Transfer Capacitance  Ing Characteristics Turn-On Delay Time	f = 1.0  MHz $V_{DD} = 50 \text{ V}, I_{D} = 70 \text{ A},$ $R_{G} = 25 \Omega$		720 150	940 200 70	pF pF
$C_{iss}$ $C_{oss}$ $C_{rss}$ Switchi $t_{d(on)}$ $t_r$ $t_{d(off)}$	Input Capacitance Output Capacitance Reverse Transfer Capacitance Ing Characteristics Turn-On Delay Time Turn-On Rise Time	f = 1.0 MHz V <sub>DD</sub> = 50 V, I <sub>D</sub> = 70 A,		720 150 30 470	940 200 70 950	pF pF ns
$\begin{aligned} & C_{iss} \\ & C_{oss} \\ & C_{rss} \end{aligned}$ $& Switchi \\ & t_{d(on)} \\ & t_r \\ & t_{d(off)} \end{aligned}$	Input Capacitance Output Capacitance Reverse Transfer Capacitance  Ing Characteristics Turn-On Delay Time Turn-On Rise Time Turn-Off Delay Time	f = 1.0  MHz $V_{DD} = 50 \text{ V}, I_{D} = 70 \text{ A},$ $R_{G} = 25 \Omega$	  	720 150 30 470 130	940 200 70 950 270	pF pF
C <sub>iss</sub> C <sub>oss</sub> C <sub>rss</sub> Switchi	Input Capacitance Output Capacitance Reverse Transfer Capacitance  Ing Characteristics Turn-On Delay Time Turn-On Rise Time Turn-Off Delay Time Turn-Off Fall Time	$f$ = 1.0 MHz $V_{DD}$ = 50 V, $I_{D}$ = 70 A, $R_{G}$ = 25 $\Omega$ (Note 4)	   	720 150 30 470 130 160	940 200 70 950 270 330	pF pF ns ns

### **Drain-Source Diode Characteristics and Maximum Ratings**

I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current		 	35	Α
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current		 	140	Α
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 35 A	 	1.5	V
t <sub>rr</sub>	Reverse Recovery Time	$V_{GS} = 0 V, I_{S} = 70 A,$	 110		ns
Q <sub>rr</sub>	Reverse Recovery Charge	dI <sub>F</sub> / dt = 100 A/μs	 430	/	nC

- **Notes:** 1. Repetitive Rating : Pulse width limited by maximum junction temperature. 2. L = 1.59 mH,  $I_{AS}$  = 35 A,  $V_{DD}$  = 25 V,  $R_{G}$  = 25  $\Omega$ , starting  $T_{J}$  = 25°C. 3.  $I_{SD}$   $\leq$  70 A, di/dt  $\leq$  300 A/µs,  $V_{DD}$   $\leq$  BV $_{DSS}$ , starting  $T_{J}$  = 25°C. 4. Essentially independent of operating temperature.

# **Typical Characteristics**

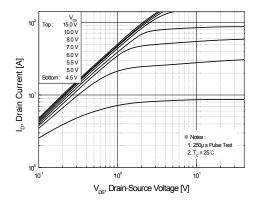


Figure 1. On-Region Characteristics

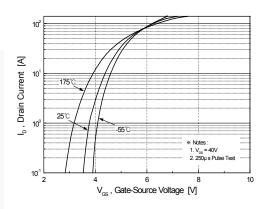


Figure 2. Transfer Characteristics

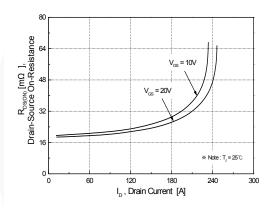


Figure 3. On-Resistance Variation vs.
Drain Current and Gate Voltage

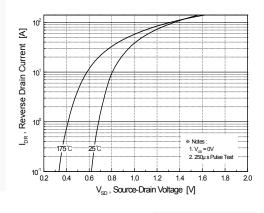


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

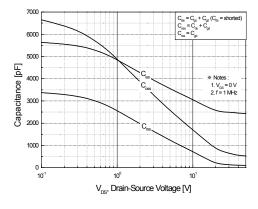


Figure 5. Capacitance Characteristics

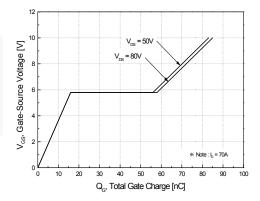
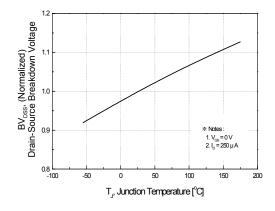


Figure 6. Gate Charge Characteristics

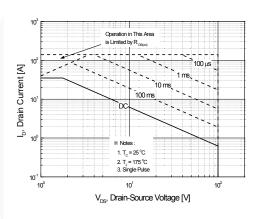
# Typical Characteristics (continued)



3.0 (paging 2.5 (paging 2.0 

Figure 7. Breakdown Voltage Variation vs. Temperature

Figure 8. On-Resistance Variation vs. Temperature



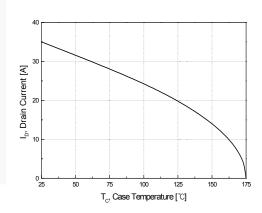


Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature

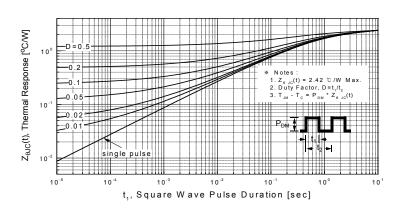


Figure 11. Transient Thermal Response Curve

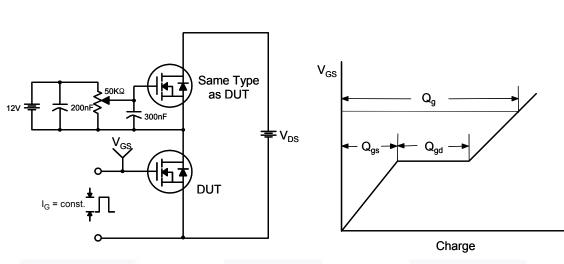


Figure 12. Gate Charge Test Circuit & Waveform

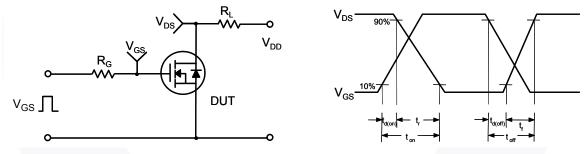


Figure 13. Resistive Switching Test Circuit & Waveforms

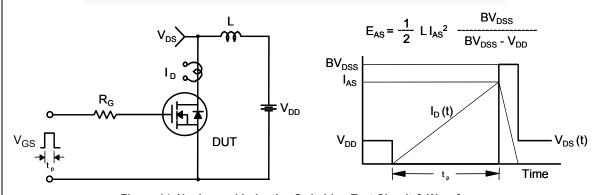
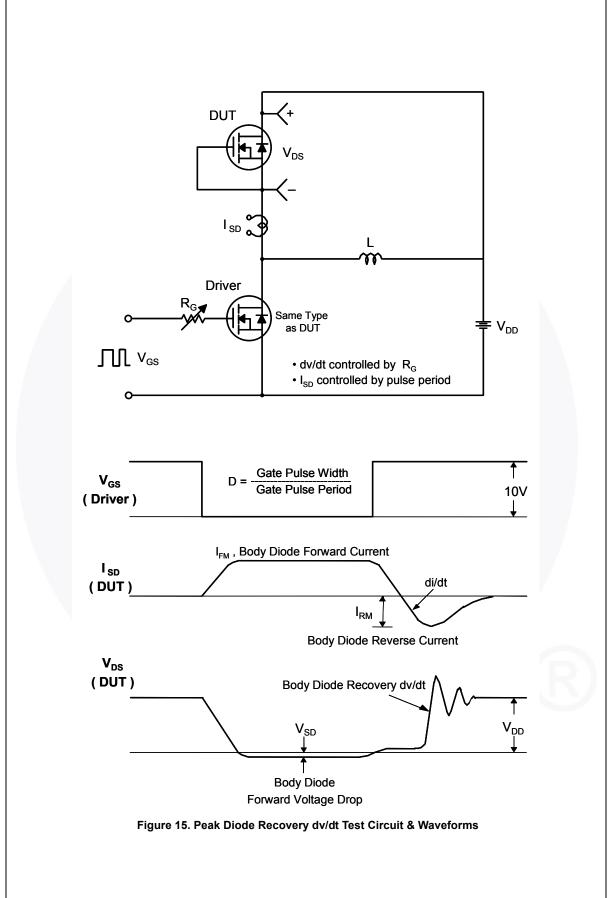


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



### **Mechanical Dimensions**

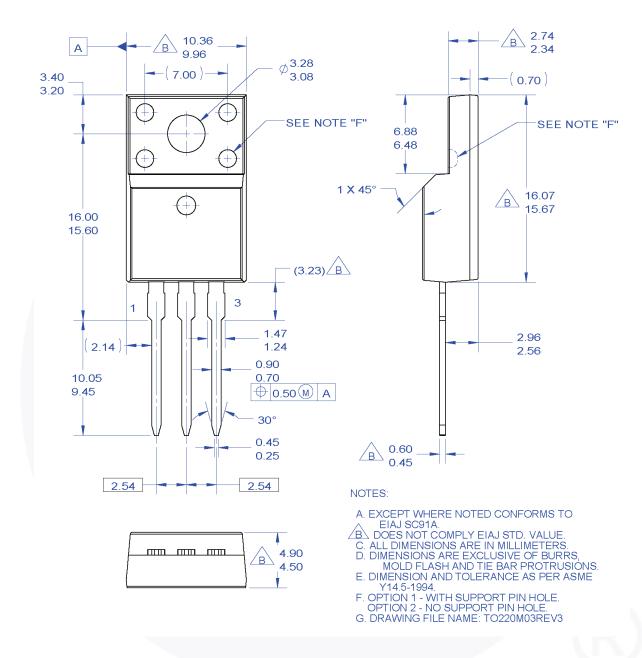


Figure 16. TO220, Molded, 3-Lead, Full Pack, EIAJ SC91, Straight Lead

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