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ON Semiconductor®

FQT1N80TF-WS

N-Channel QFET® MOSFET 800V, 0.2 A, 20 Ω

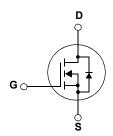
Description

This N-Channel enhancement mode power MOSFET is produced using ON Semiconductor®'s proprietary planar stripe and DMOS technology. This advanced MOSFET technology has been especially tailored to reduce onstate resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, active power factor correction (PFC), and electronic lamp ballasts.

Features

- * 0.2 A, 800 V, $R_{DS(on)} = 15.5~\Omega(V^{\circ}]$.)@ $V_{GS} = 10$ V, $I_{D} = 0.1~A$
- Low Gate Charge (Typ. 5.5 nC)
- Low C_{rss} (Typ. 2.7 pF)
- · 100% Avalanche Tested
- RoHS Compliant





MOSFET Maximum Ratings T_C = 25°C unless otherwise noted*

Symbol		Parameter		FQT1N80TF-WS	Unit
V_{DSS}	Drain to Source Voltage	ain to Source Voltage		800	V
V_{GSS}	Gate to Source Voltage			±30	V
	Drain Current	-Continuous (T _C = 25°C)		0.2	۸
ID	DrainGurient	-Continuous (T _C = 100°C)		0.12	A
I _{DM}	Drain Current	- Pulsed	(Note 1)	0.8	Α
E _{AS}	Single Pulsed Avalanche E	nergy	(Note 2)	90	mJ
I _{AR}	Avalanche Current		(Note 1)	0.2	Α
E _{AR}	Repetitive Avalanche Energ	ЭУ	(Note 1)	0.2	mJ
dv/dt	Peak Diode Recovery dv/dt	t .	(Note 3)	4.0	V/ns
Б	Dawer Dissipation	$(T_C = 25^{\circ}C)$		2.1	W
P_{D}	Power Dissipation	- Derate above 25°C		0.02	W/°C
T _J , T _{STG}	Operating and Storage Tem	nperature Range		-55 to +150	°C
T _L	Maximum Lead Temperatur 1/8" from Case for 5 Secon	• •		300	°C

Thermal Characteristics

Symbol	Parameter	Min.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient*	-	60	°C/W

^{*} When mounted on the minimum pad size recommended (PCB Mount)

Package Marking and Ordering Information T_C = 25°C unless otherwise noted

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FQT1N80	FQT1N80TF-WS	SOT-223	330mm	12mm	4000

Electrical Characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Off Charac	cteristics					
BV _{DSS}	Drain to Source Breakdown Voltage	$I_D = 250 \mu A$, $V_{GS} = 0 V$, $T_J = 25 ^{\circ} C$	800	-	-	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I_D = 250 μ A, Referenced to 25 $^{\circ}$ C	-	0.8	-	V/°C
ı	Zero Gate Voltage Drain Current	V _{DS} = 800V, V _{GS} = 0V	-	-	25	^
IDSS	Zero Gate Voltage Drain Current	$V_{DS} = 640V, T_{C} = 125^{\circ}C$	-	-	250	μА
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	±100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_{D} = 250 \mu A$	3.0	-	5.0	V
R _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 10V, I_D = 0.1A$	-	15.5	20	Ω
9 _{FS}	Forward Transconductance	$V_{DS} = 40V, I_D = 0.1A$ (Note 4)	-	0.75	-	S

Dynamic Characteristics

C _{iss}	Input Capacitance	V 05V V 0V	-	150	195	pF
C _{oss}	Output Capacitance	V _{DS} = 25V, V _{GS} = 0V f = 1MHz	-	20	30	pF
C _{rss}	Reverse Transfer Capacitance	I - HVII IZ	-	2.7	5.0	pF
Q_{g}	Total Gate Charge at 10V		-	5.5	7.2	nC
Q_{gs}	Gate to Source Gate Charge	V _{DS} = 640V, I _D = 1A V _{GS} = 10V	-	1.1	-	nC
Q_{gd}	Gate to Drain "Miller" Charge	V _{GS} = 10V (Note 4,	5) -	3.3	-	nC

Switching Characteristics

t _{d(on)}	Turn-On Delay Time			-	10	30	ns
t_r	Turn-On Rise Time	$V_{DD} = 400V, I_{D} = 1A$		-	25	60	ns
t _{d(off)}	Turn-Off Delay Time	$R_G = 25\Omega$		-	15	40	ns
t _f	Turn-Off Fall Time		(Note 4, 5)	-	25	60	ns

Drain-Source Diode Characteristics

I _S	Maximum Continuous Drain to Source Diode Forward Current			-	-	0.2	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current			-	-	0.8	Α
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS} = 0V, I_{SD} = 0.2A$		-	-	1.4	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0V, I _{SD} = 1A		-	300	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100A/\mu s$	(Note 4)	-	0.6	-	μС

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 170mH, I_{AS} = 1A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25°C
- 3. I $_{SD}$ \leq 1A, di/dt \leq 200A/ μ s, V $_{DD}$ \leq BV $_{DSS}$, Starting T $_{J}$ = 25°C
- 4. Pulse Test: Pulse width $\leq 300 \mu s, \, Duty \, Cycle \leq 2\%$
- 5. Essentially Independent of Operating Temperature Typical Characteristics

Typical Performance Characteristics

Figure 1. On-Region Characteristics

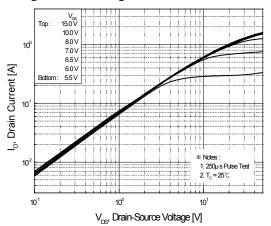


Figure 3. On-Resistance Variation vs.

Drain Current and Gate Voltage

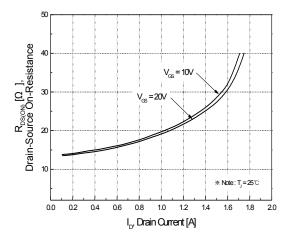


Figure 5. Capacitance Characteristics

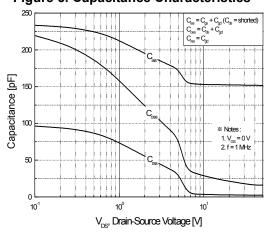


Figure 2. Transfer Characteristics

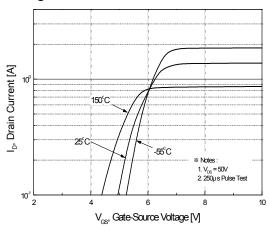


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

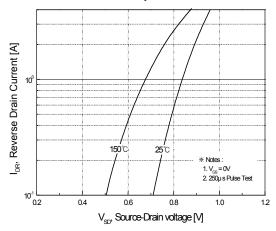
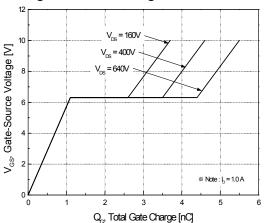


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

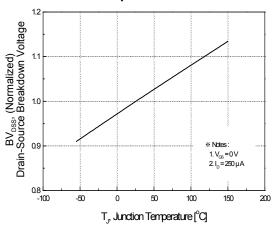


Figure 8. On-Resistance Variation vs. Temperature

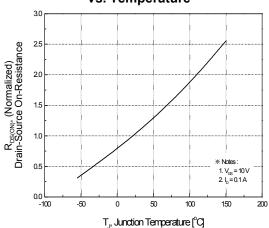


Figure 9. Maximum Safe Operating Area

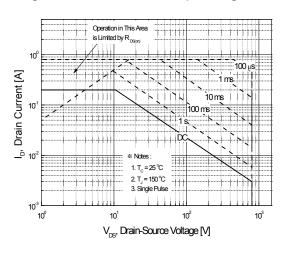


Figure 10. Maximum Drain Current vs. Case Temperature

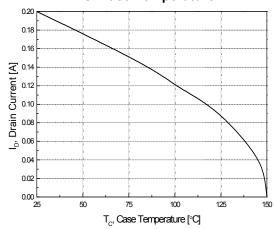
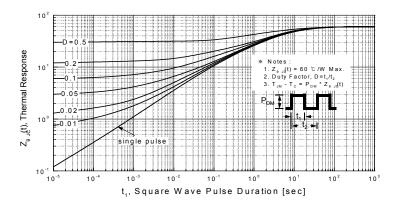
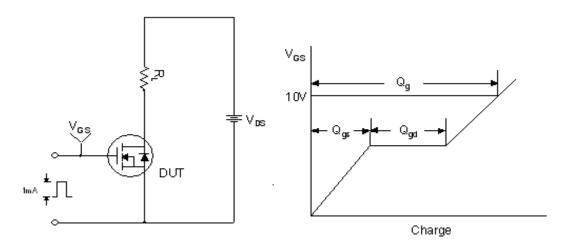


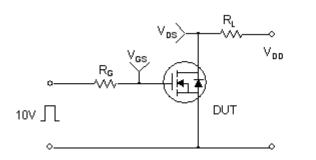
Figure 11. Transient Thermal Response Curve

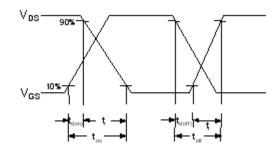


Gate Charge Test Circuit & Waveform

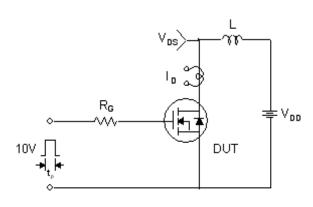


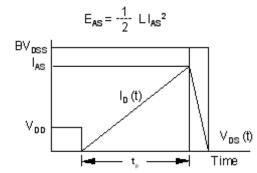
Resistive Switching Test Circuit & Waveforms



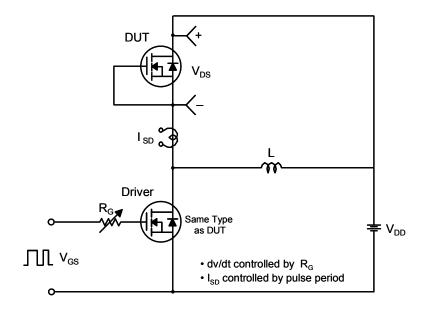


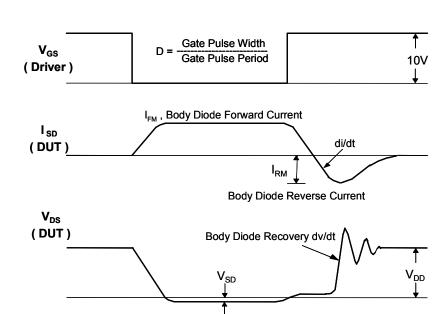
Unclamped Inductive Switching Test Circuit & Waveforms





Peak Diode Recovery dv/dt Test Circuit & Waveforms

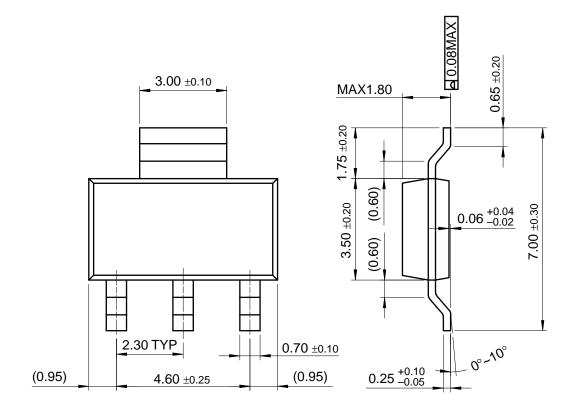


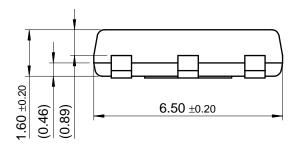


Body Diode Forward Voltage Drop

Mechanical Dimensions

SOT-223





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