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# FSBB30CH60 Motion SPM<sup>®</sup> 3 Series

### Features

- UL Certified No. E209204 (UL1557)
- 600 V 30 A 3-Phase IGBT Inverter with Integral Gate Drivers and Protection
- Low-Loss, Short-Circuit Rated IGBTs
- Very Low Thermal Resistance Using AIN DBC Substrate
- Dedicated Vs Pins Simplify PCB Layout
- Separate Open-Emitter Pins from Low-Side IGBTs for Three-Phase Current Sensing
- Single-Grounded Power Supply
- Isolation Rating: 2500 V<sub>rms</sub> / min.

### Applications

• Motion Control - Home Appliance / Industrial Motor

### **Related Resources**

• AN-9035 - Motion SPM 3 Series Ver.2 User's Guide

### **General Description**

FSBB30CH60 is a Motion SPM<sup>®</sup> 3 module providing a fully-featured, high-performance inverter output stage for AC Induction, BLDC, and PMSM motors. These modules integrate optimized gate drive of the built-in IGBTs to minimize EMI and losses, while also providing multiple on-module protection features including under-voltage lockouts, over-current shutdown, and fault reporting. The built-in, high-speed HVIC requires only a single supply voltage and translates the incoming logic-level gate inputs to the high-voltage, high-current drive signals required to properly drive the module's internal IGBTs. Separate negative IGBT terminals are available for each phase to support the widest variety of control algorithms.

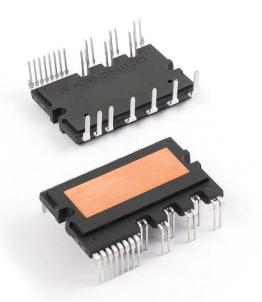


Figure 1. Package Overview

### Package Marking and Ordering Information

Device	Device Marking Package		Packing Type	Quantity
FSBB30CH60	FSBB30CH60	SPMEA-027	Rail	10

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FSBB30CH60 Motion SPM® 3 Series

### **Integrated Power Functions**

• 600 V - 30 A IGBT inverter for three-phase DC / AC power conversion (please refer to Figure 3)

### Integrated Drive, Protection and System Control Functions

• For inverter high-side IGBTs: gate drive circuit, high-voltage isolated high-speed level shifting

control circuit Under-Voltage Lock-Out Protection (UVLO) Note: Available bootstrap circuit example is given in Figures 10 and 11.

- For inverter low-side IGBTs: gate drive circuit, Short-Circuit Protection (SCP)
  control supply circuit Under-Voltage Lock-Out Protection (UVLO)
- Fault signaling: corresponding to UVLO (low-side supply) and SC faults
- Input interface: active-HIGH interface, works with 3.3 / 5 V logic, Schmitt-trigger input

### **Pin Configuration**

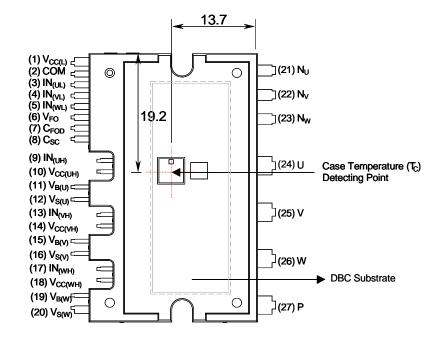
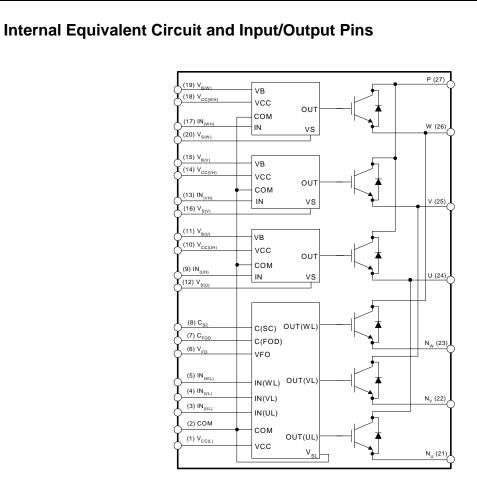


Figure 2. Top View

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Pin Number	Pin Name	Pin Description	
1	V <sub>CC(L)</sub>	Low-Side Common Bias Voltage for IC and IGBTs Driving	
2	СОМ	Common Supply Ground	
3	IN <sub>(UL)</sub>	Signal Input for Low-Side U-Phase	
4	IN <sub>(VL)</sub>	Signal Input for Low-Side V-Phase	
5	IN <sub>(WL)</sub>	Signal Input for Low-Side W-Phase	
6	V <sub>FO</sub>	Fault Output	
7	C <sub>FOD</sub>	pacitor for Fault Output Duration Selection	
8	C <sub>SC</sub>	Capacitor (Low-pass Filter) for Short-Circuit Current Detection Input	
9	IN <sub>(UH)</sub>	Signal Input for High-Side U-Phase	
10	V <sub>CC(UH)</sub>	High-Side Bias Voltage for U-Phase IC	
11	V <sub>B(U)</sub>	High-Side Bias Voltage for U-Phase IGBT Driving	
12	V <sub>S(U)</sub>	High-Side Bias Voltage Ground for U-Phase IGBT Driving	
13	IN <sub>(VH)</sub>	Signal Input for High-Side V-Phase	
14	V <sub>CC(VH)</sub>	High-Side Bias Voltage for V-Phase IC	
15	V <sub>B(V)</sub>	High-Side Bias Voltage for V-Phase IGBT Driving	
16	V <sub>S(V)</sub>	High-Side Bias Voltage Ground for V-Phase IGBT Driving	
17	IN <sub>(WH)</sub>	Signal Input for High-Side W Phase	
18	V <sub>CC(WH)</sub>	High-Side Bias Voltage for W-Phase IC	
19	V <sub>B(W)</sub>	High-Side Bias Voltage for W-Phase IGBT Driving	
20	V <sub>S(W)</sub>	High-Side Bias Voltage Ground for W-Phase IGBT Driving	
21	NU	Negative DC-Link Input for U-Phase	
22	N <sub>V</sub>	Negative DC-Link Input for V-Phase	
23	N <sub>W</sub>	Negative DC-Link Input for W-Phase	
24	U	Output for U-Phase	
25	V	Output for V-Phase	
26	W	Output for W-Phase	
27	Р	Positive DC-Link Input	



### Figure 3. Internal Block Diagram

#### 1st Notes:

1. Inverter low-side is composed of three IGBTs, freewheeling diodes for each IGBT, and one control IC. It has gate drive and protection functions.

2. Inverter power side is composed of four inverter DC-link input terminals and three inverter output terminals.

3. Inverter high-side is composed of three IGBTs, freewheeling diodes, and three drive ICs for each IGBT.

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### Absolute Maximum Ratings ( $T_J = 25^{\circ}C$ , unless otherwise specified.)

### Inverter Part

Symbol	Parameter	Conditions	Rating	Unit
V <sub>PN</sub>	Supply Voltage	Applied between P- $N_U$ , $N_V$ , $N_W$	450	V
V <sub>PN(Surge)</sub>	Supply Voltage (Surge)	Applied between P- $N_U$ , $N_V$ , $N_W$	500	V
V <sub>CES</sub>	Collector - Emitter Voltage		600	V
± I <sub>C</sub>	Each IGBT Collector Current	$T_{C} = 25^{\circ}C$	30	А
± I <sub>CP</sub>	Each IGBT Collector Current (Peak)	$T_{C} = 25^{\circ}C$ , Under 1ms Pulse Width	45	А
P <sub>C</sub>	Collector Dissipation	$T_{\rm C} = 25^{\circ}{\rm C}$ per Chip	103	W
ТJ	Operating Junction Temperature	(2nd Note 1)	-20 ~ 125	°C

2nd Notes:

1. The maximum junction temperature rating of the power chips integrated within the Motion SPM<sup>®</sup> 3 product is 150°C (at  $T_C \le 100°C$ ). However, to insure safe operation of the Motion SPM 3 product, the average junction temperature should be limited to  $T_{J(ave)} \le 125°C$  (at  $T_C \le 100°C$ )

### **Control Part**

Symbol	Parameter	Conditions	Rating	Unit
V <sub>CC</sub> Control Supply Voltage		Applied between $V_{CC(UH)},V_{CC(VH)},V_{CC(WH)},V_{CC(L)}$ - COM	20	V
V <sub>BS</sub> High-Side Control Bias Voltage		Applied between V_B(U) - V_S(U), V_B(V) - V_S(V), V_B(W) - V_S(W)	20	V
V <sub>IN</sub>	Input Signal Voltage	Applied between IN <sub>(UH)</sub> , IN <sub>(VH)</sub> , IN <sub>(WH)</sub> , IN <sub>(UL)</sub> , IN <sub>(VL)</sub> , IN <sub>(VL)</sub> , IN <sub>(WL)</sub> - COM	-0.3 ~ 17	V
V <sub>FO</sub>	Fault Output Supply Voltage	Applied between V <sub>FO</sub> - COM	-0.3 ~ V <sub>CC</sub> +0.3	V
I <sub>FO</sub>	Fault Output Current	Sink Current at V <sub>FO</sub> Pin	5	mA
V <sub>SC</sub>	Current-Sensing Input Voltage	Applied between C <sub>SC</sub> - COM	-0.3 ~ V <sub>CC</sub> +0.3	V

### **Total System**

Symbol	Parameter	Conditions	Rating	Unit
V <sub>PN(PROT)</sub> Self-Protection Supply Voltage Limit (Short-Circuit Protection Capability)		$V_{CC} = V_{BS} = 13.5 \sim 16.5 V$ T <sub>J</sub> = 125°C, Non-Repetitive, < 2 µs	400	V
T <sub>C</sub>	Module Case Operation Temperature	-20°C $\leq$ T <sub>J</sub> $\leq$ 125°C, See Figure 2	-20 ~ 100	°C
T <sub>STG</sub>	Storage Temperature		-40 ~ 125	°C
V <sub>ISO</sub>	Isolation Voltage	60 Hz, Sinusoidal, AC 1 Minute, Connect Pins to Heat Sink Plate	2500	V <sub>rms</sub>

### **Thermal Resistance**

Symbol	Parameter	Condition	Min.	Тур.	Max.	Unit
R <sub>th(j-c)Q</sub>	Junction to Case Thermal	Inverter IGBT Part (per 1 / 6 module)	-	-	0.97	°C/W
R <sub>th(j-c)F</sub>	Resistance	Inverter FWD Part (per 1 / 6 module)	-	-	1.77	°C/W

2nd Notes:

2. For the measurement point of case temperature(T\_C), please refer to Figure 2.

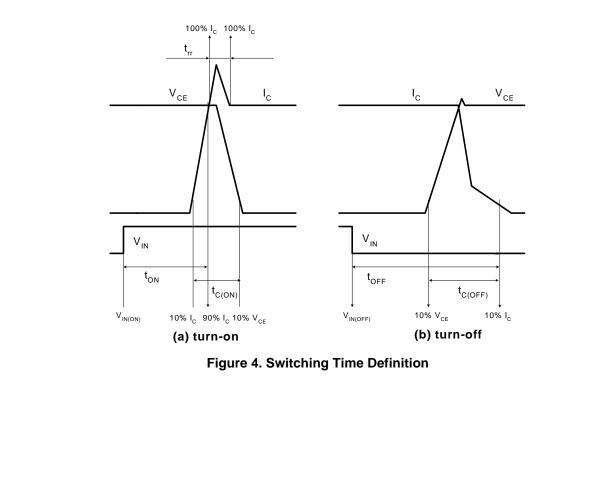
### **Electrical Characteristics** ( $T_J = 25^{\circ}C$ , unless otherwise specified.)

### **Inverter Part**

S	ymbol	Parameter	Cond	Conditions		Тур.	Max.	Unit
V	CE(SAT)	Collector - Emitter Saturation Voltage			2.75	V		
	V <sub>F</sub>	FWDi Forward Voltage	V <sub>IN</sub> = 0 V	I <sub>C</sub> =30 A, T <sub>J</sub> = 25°C	-	-	2.4	V
HS	t <sub>ON</sub>	Switching Times		$V_{PN} = 300 \text{ V}, V_{CC} = V_{BS} = 15 \text{ V}$ $I_C = 30 \text{ A}$ $V_{IN} = 0 \text{ V} \leftrightarrow 5 \text{ V}$ , Inductive Load		0.49	-	μS
	t <sub>C(ON)</sub>					0.34	-	μS
	t <sub>OFF</sub>		(2nd Note 3)	-	0.86	-	μS	
	t <sub>C(OFF)</sub>				-	0.52	-	μS
	t <sub>rr</sub>				-	0.10	-	μS
LS	t <sub>ON</sub>		$V_{PN}$ = 300 V, $V_{CC}$ = $V_B$	<sub>S</sub> = 15 V	-	0.68	-	μS
	t <sub>C(ON)</sub>		$I_{C} = 30 \text{ A}$ $V_{IN} = 0 \text{ V} \leftrightarrow 5 \text{ V}$ , Induc	tive Load	-	0.47	-	μS
	t <sub>OFF</sub>	-	(2nd Note 3)		-	0.90	-	μS
	t <sub>C(OFF)</sub>				-	0.50	-	μS
	t <sub>rr</sub>				-	0.10	-	μS
	I <sub>CES</sub>	Collector - Emitter Leakage Current	$V_{CE} = V_{CES}$		-	-	250	μΑ

#### 2nd Notes:

3. t<sub>ON</sub> and t<sub>OFF</sub> include the propagation delay of the internal drive IC. t<sub>C(ON)</sub> and t<sub>C(OFF)</sub> are the switching time of IGBT itself under the given gate driving condition internally. For the detailed information, please see Figure 4.



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### **Electrical Characteristics** ( $T_J = 25^{\circ}C$ , unless otherwise specified.)

Symbol	Parameter	Co	nditions	Min.	Тур.	Max.	Unit
IQCCL	Quiescent V <sub>CC</sub> Supply Current	V <sub>CC</sub> = 15 V IN <sub>(UL, VL, WL)</sub> = 0 V	V <sub>CC(L)</sub> - COM	-	-	23	mA
I <sub>QCCH</sub>		V <sub>CC</sub> = 15 V IN <sub>(UH, VH, WH)</sub> = 0 V	V <sub>CC(UH)</sub> , V <sub>CC(VH)</sub> , V <sub>CC(WH)</sub> - COM	-	-	100	μΑ
$I_{QBS}$	Quiescent V <sub>BS</sub> Supply Current	V <sub>BS</sub> = 15 V IN <sub>(UH, VH, WH)</sub> = 0 V	$ \begin{array}{l} V_{B(U)} \text{ - } V_{S(U)},  V_{B(V)} \text{ - } V_{S(V)}, \\ V_{B(W)} \text{ - } V_{S(W)} \end{array} $	-	-	500	μA
V <sub>FOH</sub>	Fault Output Voltage	$V_{SC}$ = 0 V, $V_{FO}$ Circuit: 4.7 k $\Omega$ to 5 V Pull-up		4.5	-	-	V
V <sub>FOL</sub>		$V_{SC}$ = 1 V, $V_{FO}$ Circuit: 4.7 k $\Omega$ to 5 V Pull-up		-	-	0.8	V
V <sub>SC(ref)</sub>	Short-Circuit Current Trip Level	V <sub>CC</sub> = 15 V (2nd Note 4)		0.45	0.50	0.55	V
UV <sub>CCD</sub>	Supply Circuit Under-Voltage	Detection Level		10.7	11.9	13.0	V
UV <sub>CCR</sub>	Protection	Reset Level		11.2	12.4	13.2	V
UV <sub>BSD</sub>		Detection Level		10.1	11.3	12.5	V
UV <sub>BSR</sub>		Reset Level		10.5	11.7	12.9	V
t <sub>FOD</sub>	Fault-Out Pulse Width	C <sub>FOD</sub> = 33 nF (2nd Note 5)		1.0	1.8	-	ms
V <sub>IN(ON)</sub>	ON Threshold Voltage	Applied between IN <sub>(UH)</sub> , IN <sub>(VH)</sub> , IN <sub>(WH)</sub> , IN <sub>(UL)</sub> ,		3.0	-	-	V
VIN(OFF)	OFF Threshold Voltage	$IN_{(VL)}$ , $IN_{(WL)}$ - COM		-	-	0.8	V

2nd Notes:

4. Short-circuit protection is functioning only at the low-sides.

5. The fault-out pulse width  $t_{FOD}$  depends on the capacitance value of  $C_{FOD}$  according to the following approximate equation:  $C_{FOD} = 18.3 \times 10^{-6} \times t_{FOD} [F]$ 

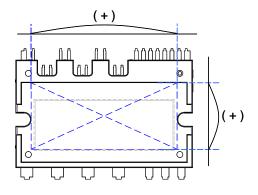
### **Recommended Operating Conditions**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
V <sub>PN</sub>	Supply Voltage	Applied between P - $N_U$ , $N_V$ , $N_W$	-	300	400	V
V <sub>CC</sub>	Control Supply Voltage	Applied between V <sub>CC(UH)</sub> , V <sub>CC(VH)</sub> , V <sub>CC(WH)</sub> , V <sub>CC(L)</sub> - COM	13.5	-	16.5	V
V <sub>BS</sub>	High-Side Bias Voltage	Applied between V_B(U) - V_S(U), V_B(V) - V_S(V), V_B(W) - V_S(W)	13	-	18.5	V
dV <sub>CC</sub> / dt, dV <sub>BS</sub> / dt	Control Supply Variation		-1	-	1	V / μs
t <sub>dead</sub>	Blanking Time for Preventing Arm-Short	For Each Input Signal	2.5	-	-	μs
f <sub>PWM</sub>	PWM Input Signal	$-20^{\circ}C \leq T_C \leq 100^{\circ}C, \ -20^{\circ}C \leq T_J \leq 125^{\circ}C$	-	-	20	kHz
V <sub>SEN</sub>	Voltage for Current Sensing	Applied between N <sub>U</sub> , N <sub>V</sub> , N <sub>W</sub> - COM (Including Surge Voltage)	-4		4	V

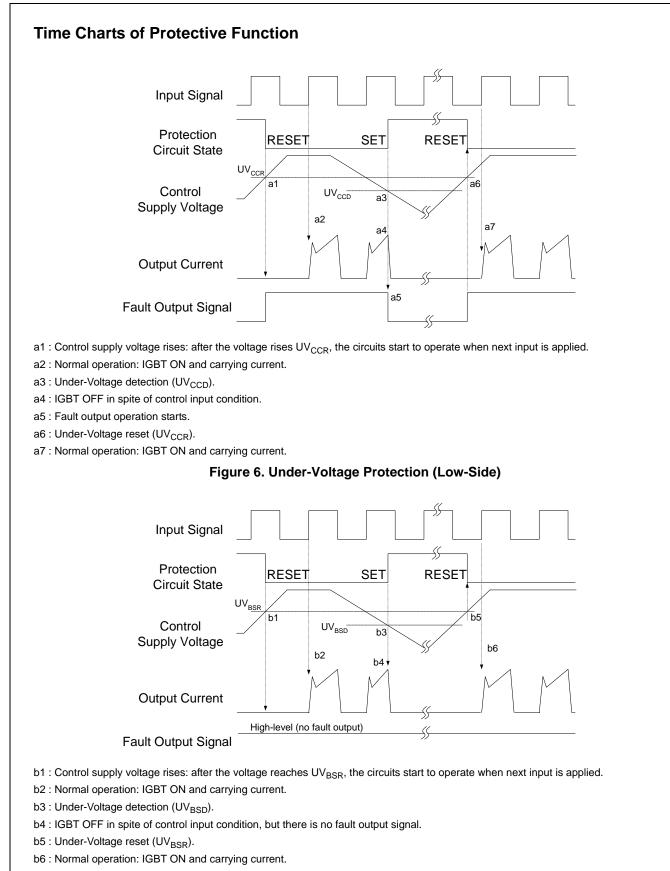
<b>Motion SPM® 3 Series</b>	FSBB30CH60	
	otion SPM® 3 Serie	

### **Mechanical Characteristics and Ratings**

Parameter	Сог	Min.	Тур.	Max.	Unit	
Mounting Torque	Mounting Screw: M3	Recommended 0.62 N•m	0.51	0.62	0.72	N∙m
Device Flatness		See Figure 5	0	-	+120	μm
Weight			-	15.00	-	g

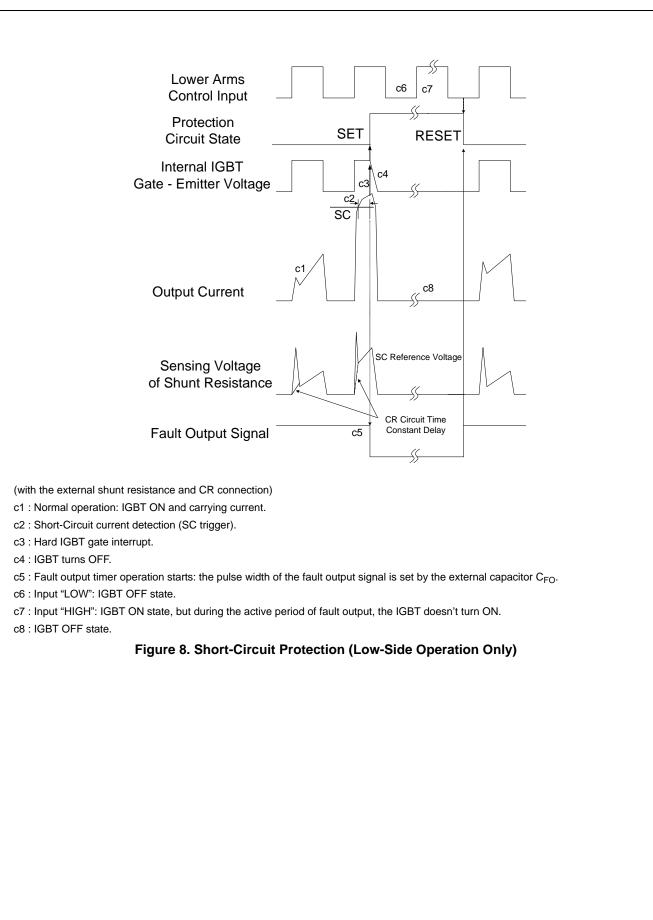


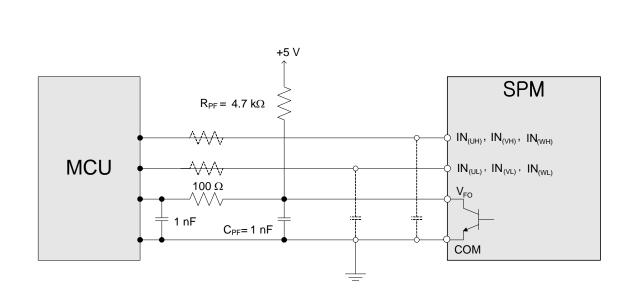




### Figure 7. Under-Voltage Protection (High-Side)

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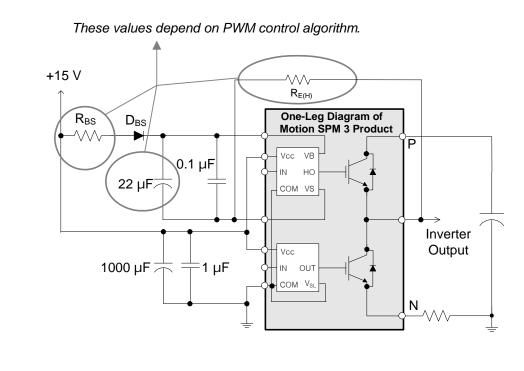




#### Figure 9. Recommended MCU I/O Interface Circuit

#### 3rd Notes:

- 1. RC coupling at each input (parts shown dotted) might change depending on the PWM control scheme in the application and the wiring impedance of the application's printed circuit board. The Motion SPM<sup>®</sup> 3 Product input signal section integrates a 3.3 kΩ(typ.) pull-down resistor. Therefore, when using an external filtering resistor, pay attention to the signal voltage drop at input terminal.
- 2. The logic input works with standard CMOS or LSTTL outputs.



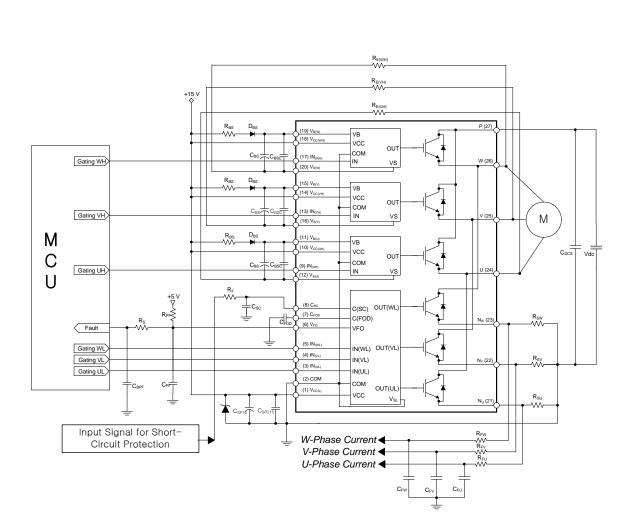
### Figure 10. Recommended Bootstrap Operation Circuit and Parameters

#### 3rd Notes:

3. It would be recommended that the bootstrap diode, D<sub>BS</sub>, has soft and fast recovery characteristics.

4. The bootstrap resistor ( $R_{BS}$ ) should be three times greater than  $R_{E(H)}$ . The recommended value of  $R_{E(H)}$  is 5.6  $\Omega$ , but it can be increased up to 20  $\Omega$  (maximum) for a slower dv/ dt of high-side.

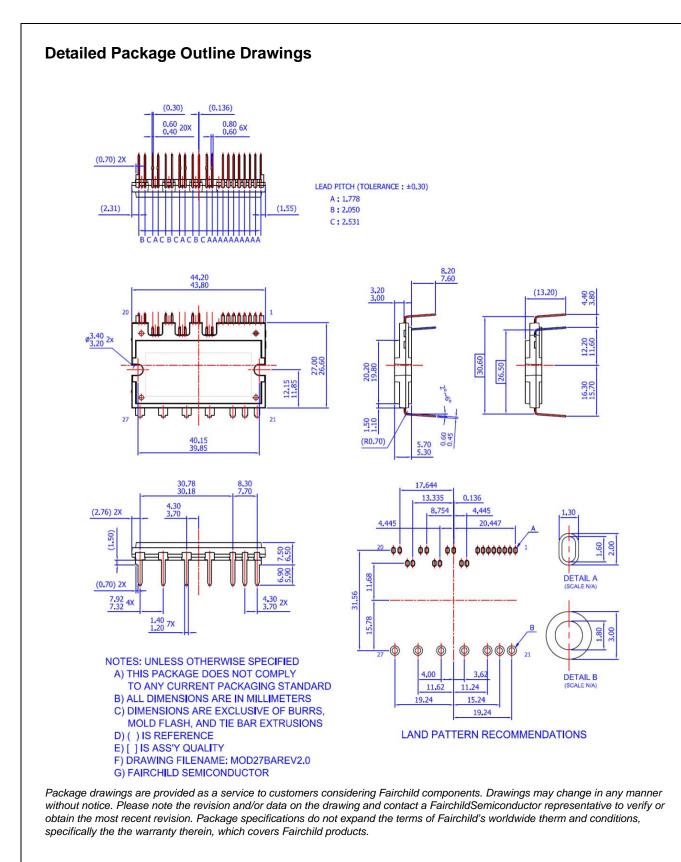
5. The ceramic capacitor placed between  $V_{CC}$  - COM should be over 1  $\mu$ F and mounted as close to the pins of the Motion SPM 3 product as possible.



### Figure 11. Typical Application Circuit

#### 4th Notes:

- 1. To avoid malfunction, the wiring of each input should be as short as possible (less than 2 3 cm).
- 2. By virtue of integrating an application-specific type of HVIC inside the Motion SPM<sup>®</sup> 3 product, direct coupling to MCU terminals without any optocoupler or transformer isolation is possible.
- 3. V<sub>FO</sub> output is open-collector type. This signal line should be pulled up to the positive side of the 5 V power supply with approximately 4.7 kΩ resistance (please refer to Figure 9).
- 4.  $C_{\mbox{\scriptsize SP15}}$  of around seven times larger than bootstrap capacitor  $C_{\mbox{\scriptsize BS}}$  is recommended.
- 5.  $V_{FO}$  output pulse width should be determined by connecting an external capacitor ( $C_{FOD}$ ) between  $C_{FOD}$  (pin 7) and COM (pin 2). (Example : if  $C_{FOD}$  = 33 nF, then  $t_{FO}$  = 1.8 ms (typ.)) Please refer to the 2nd note 5 for calculation method.
- 6. Input signal is active-HIGH type. There is a 3.3 kΩ resistor inside the IC to pull down each input signal line to GND. When employing RC coupling circuits, set up such RC couple that input signal agree with turn-off / turn-on threshold voltage.
- 7. To prevent errors of the protection function, the wiring around  $R_{\textrm{F}}$  and  $C_{\textrm{SC}}$  should be as short as possible.
- 8. In the short-circuit protection circuit, please select the  $R_F C_{SC}$  time constant in the range 1.5 ~ 2  $\mu s.$
- 9. Each capacitor should be mounted as close to the pins of the Motion SPM 3 product as possible.
- 10. To prevent surge destruction, the wiring between the smoothing capacitor and the P & GND pins should be as short as possible. The use of a high-frequency non-inductive capacitor of around 0.1 ~ 0.22 μF between the P & GND pins is recommended.
- Relays are used in almost every systems of electrical equipment in home appliances. In these cases, there should be sufficient distance between the MCU and the relays.
  C<sub>SPC15</sub> should be over 1 µF and mounted as close to the pins of the Motion SPM 3 product as possible.



Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:

http://www.fairchildsemi.com/dwg/MO/MOD27BA.pdf

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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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