**ON Semiconductor** 

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# Onsemi

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# **ON Semiconductor® IRFR220B / IRFU220B** 200V N-Channel MOSFET

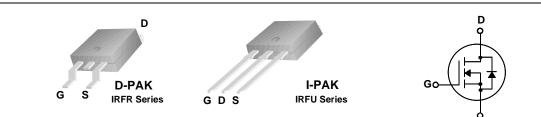
### **General Description**

These N-Channel enhancement mode power field effect transistors are produced using ON Semiconductor's proprietary, planar, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switching DC/DC converters, switch mode power supplies, DC-AC converters for uninterrupted power supply and motor control.

### Features

- 4.6A, 200V,  $R_{DS(on)} = 0.8\Omega @V_{GS} = 10 V$  Low gate charge ( typical 12 nC)
- Low Crss (typical 10 pF) •
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



## Absolute Maximum Ratings $T_{c} = 25^{\circ}C$ unless otherwise noted

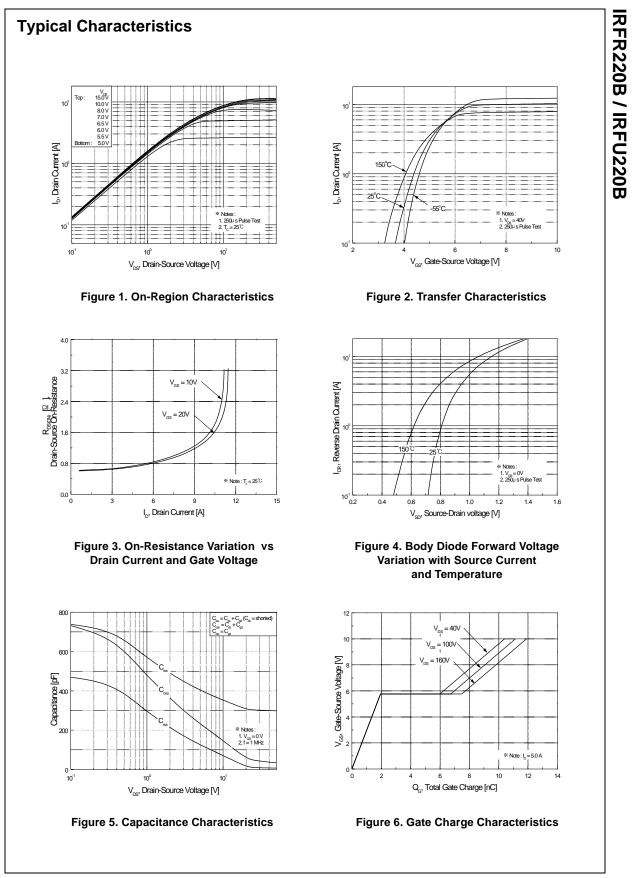
Symbol	Parameter		IRFR220B / IRFU220B	Units
V <sub>DSS</sub>	Drain-Source Voltage		200	V
ID	Drain Current - Continuous (T <sub>C</sub> = 25	(O°	4.6	А
	- Continuous (T <sub>C</sub> = 100°C)		2.9	А
I <sub>DM</sub>	Drain Current - Pulsed	(Note 1)	18	А
V <sub>GSS</sub>	Gate-Source Voltage		± 30	V
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)		65	mJ
I <sub>AR</sub>	Avalanche Current (Nor		4.6	А
E <sub>AR</sub>	Repetitive Avalanche Energy	(Note 1)	4.0	mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	5.5	V/ns
P <sub>D</sub>	Power Dissipation ( $T_A = 25^{\circ}C$ ) *		2.5	W
	Power Dissipation ( $T_C = 25^{\circ}C$ )		40	W
	- Derate above 25°C		0.32	W/°C
T <sub>J</sub> , T <sub>stg</sub>	Operating and Storage Temperature Range		-55 to +150	°C
TL	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300	°C

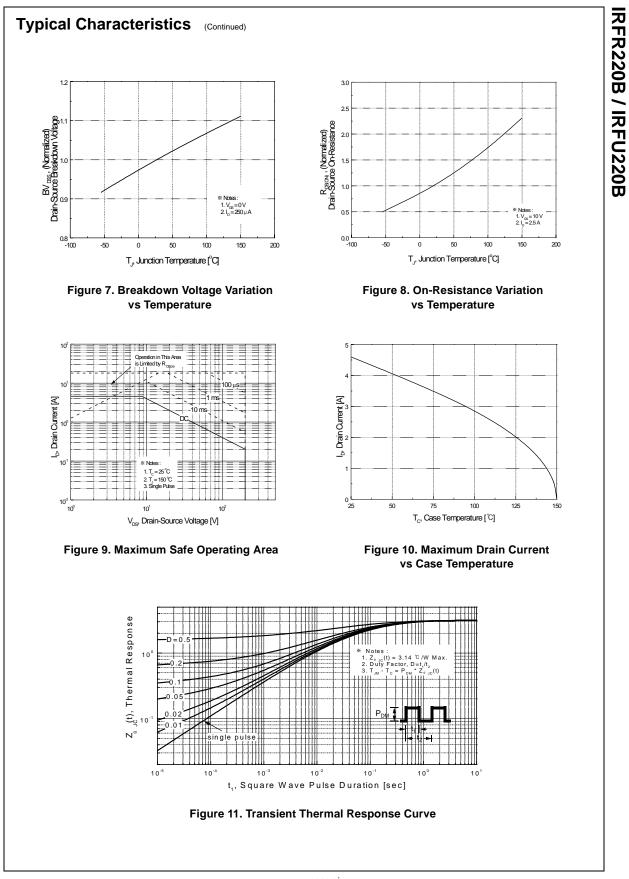
### **Thermal Characteristics**

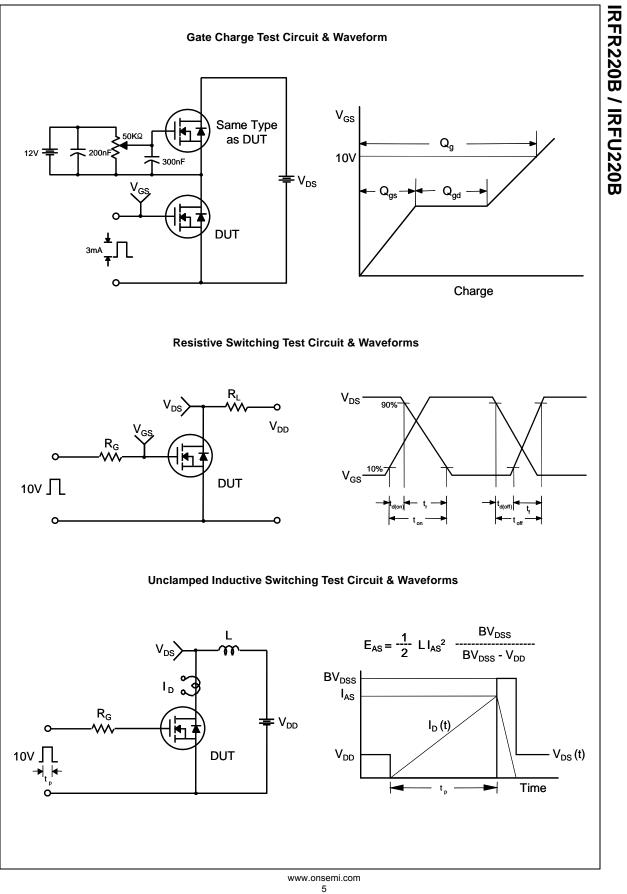
Symbol	Parameter	Тур	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case		3.14	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient *		50	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient		110	°C/W

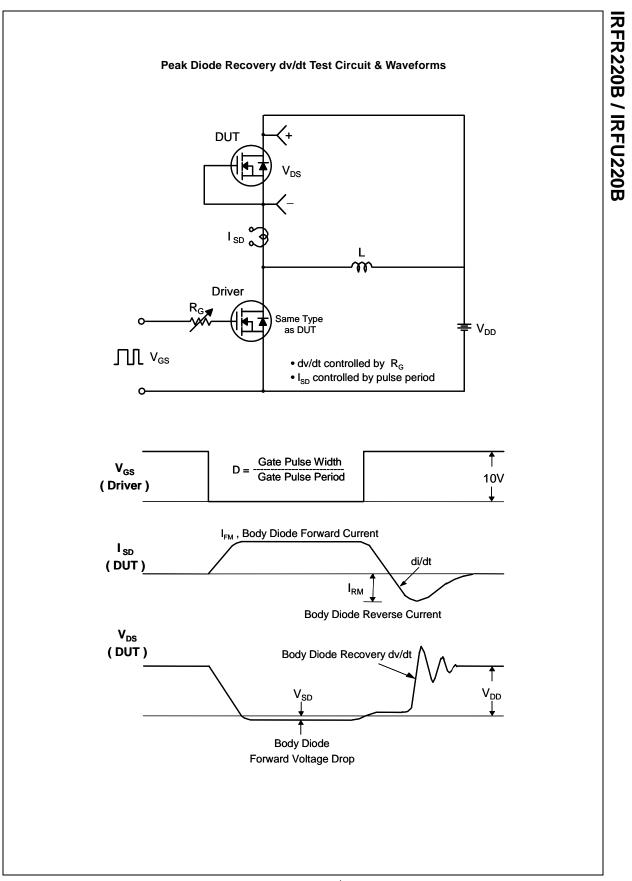
Symbol	Parameter	Test Conditions		Min	Тур	Max	Units
Off Cha	racteristics						
3V <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA		200			V
∆BV <sub>DSS</sub> ∆T <sub>J</sub>	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu\text{A}$ , Referenced to $25^{\circ}\text{C}$			0.2		V/°C
DSS	Zero Gate Voltage Drain Current	$V_{DS} = 200 \text{ V}, V_{GS} = 0 \text{ V}$				10	μA
		V <sub>DS</sub> = 160 V, T <sub>C</sub> = 125°C				100	μA
GSSF	Gate-Body Leakage Current, Forward	$V_{GS} = 30 \text{ V}, V_{DS} = 0 \text{ V}$				100	nA
GSSR	Gate-Body Leakage Current, Reverse	$V_{GS} = -30 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$				-100	nA
)n Cha	racteristics						
/ <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA		2.0		4.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 2.3 \text{ A}$			0.65	0.8	Ω
FS	Forward Transconductance	V <sub>DS</sub> = 40 V, I <sub>D</sub> = 2.3 A	(Note 4)		3.7		S
Dynami	ic Characteristics						
Ciss	Input Capacitance	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0 MHz			300	390	pF
Coss	Output Capacitance				50	65	pF
C <sub>rss</sub>	Reverse Transfer Capacitance				10	13	pF
Switchi	ng Characteristics						
d(on)	Turn-On Delay Time				6.8	24	ns
r	Turn-On Rise Time	$V_{DD} = 100 \text{ V}, \text{ I}_{D} = 5.0 \text{ A},$ R <sub>G</sub> = 25 Ω			45	100	ns
d(off)	Turn-Off Delay Time	$R_{G} = 23.22$			30	70	ns
f	Turn-Off Fall Time	(Note 4, 5	(Note 4, 5)		40	90	ns
ל <sup>מ</sup>	Total Gate Charge	V <sub>DS</sub> = 160 V, I <sub>D</sub> = 5.0 A,			12	16	nC
ວ <sub>gs</sub>	Gate-Source Charge	$V_{GS} = 10 \text{ V}$			2.0		nC
ג ג <sub>gd</sub>	Gate-Drain Charge		(Note 4, 5)		5.5		nC
Drain-S	ource Diode Characteristics a	nd Maximum Rating	2				
s	Maximum Continuous Drain-Source Did	-	5			4.6	А
SM	Maximum Pulsed Drain-Source Diode F					18	A
/ <sub>SD</sub>	Drain-Source Diode Forward Voltage					1.5	V
rr	Reverse Recovery Time	$V_{GS} = 0 \text{ V}, \text{ I}_{S} = 5.0 \text{ A},$			130		ns
יי ג <sup>וו</sup>	Reverse Recovery Charge	dl <sub>F</sub> / dt = 100 A/µs	(Note 4)		0.58		μC
otes: Repetitive R L = 4.6mH, I I <sub>SD</sub> ≤ 5.0A,	ating : Pulse width limited by maximum junction tempe $_{AS}$ = 4.6A, V <sub>DD</sub> = 50V, R <sub>G</sub> = 25 Ω, Starting T <sub>J</sub> = 25°C di/dt ≤ 300A/µs, V <sub>DD</sub> ≤ BV <sub>DSS</sub> , Starting T <sub>J</sub> = 25°C Pulse width ≤ 300us, Duty cycle ≤ 2%	rature					

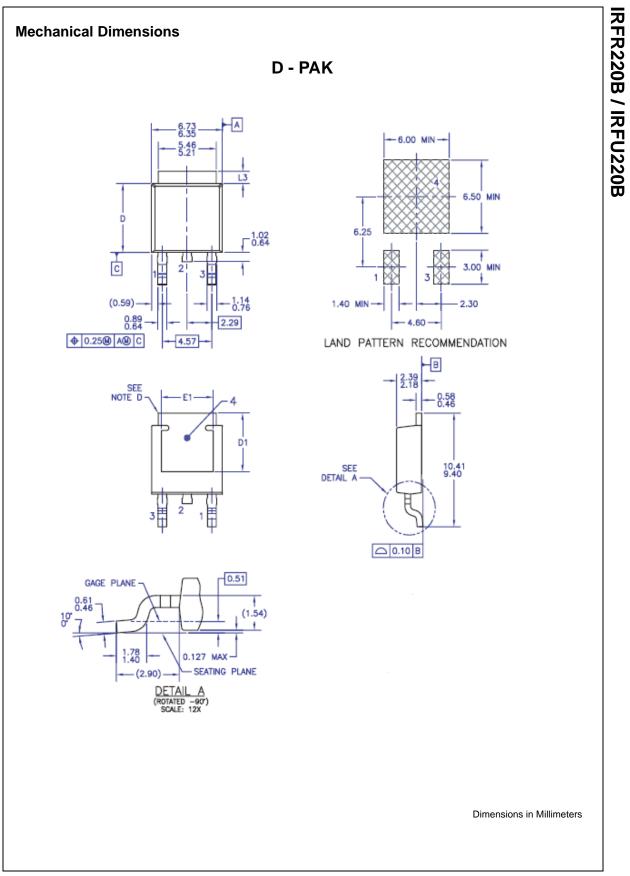
IRFR220B / IRFU220B

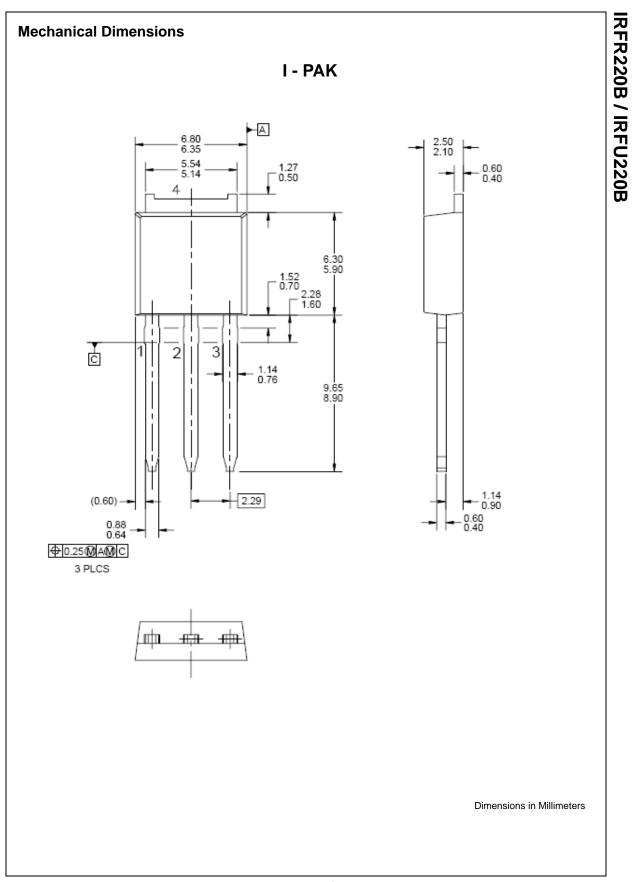












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