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ISL9K460P3 8 A, 600 V, STEALTH™ II Diode

Features

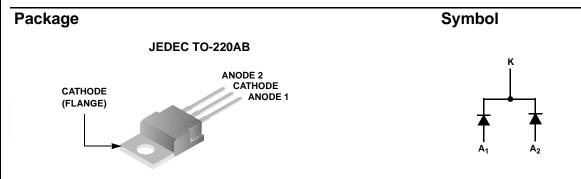
- Stealth Recovery t_{rr} = 17 ns (@ I_F = 4 A)
- Max Forward Voltage, V_F = 2.4 V (@ T_C = 25°C)
- 600 V Reverse Voltage and High Reliability
- Avalanche Energy Rated
- RoHS Compliant

Applications

- SMPS FWD
- Hard Switched PFC Boost Diode
- UPS Free Wheeling Diode
- Motor Drive FWD
- Snubber Diode

Description

The ISL9K460P3 is a STEALTH[™] dual diode optimized for low loss performance in high frequency hard switched applications. The STEALTH[™] family exhibits low reverse recovery current (I_{rr}) and exceptionally soft recovery under typical operating conditions. This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low I_{rr} and short ta phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the STEALTH[™] diode with an SMPS IGBT to provide the most efficient and highest power density design at lower cost.

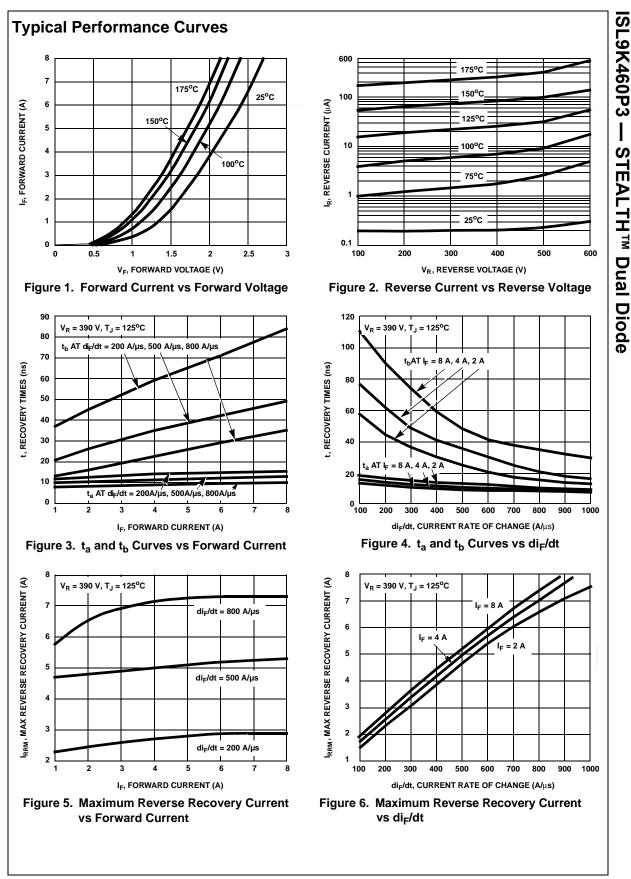


Device Maximum Ratings (per leg) T_C= 25°C unless otherwise noted

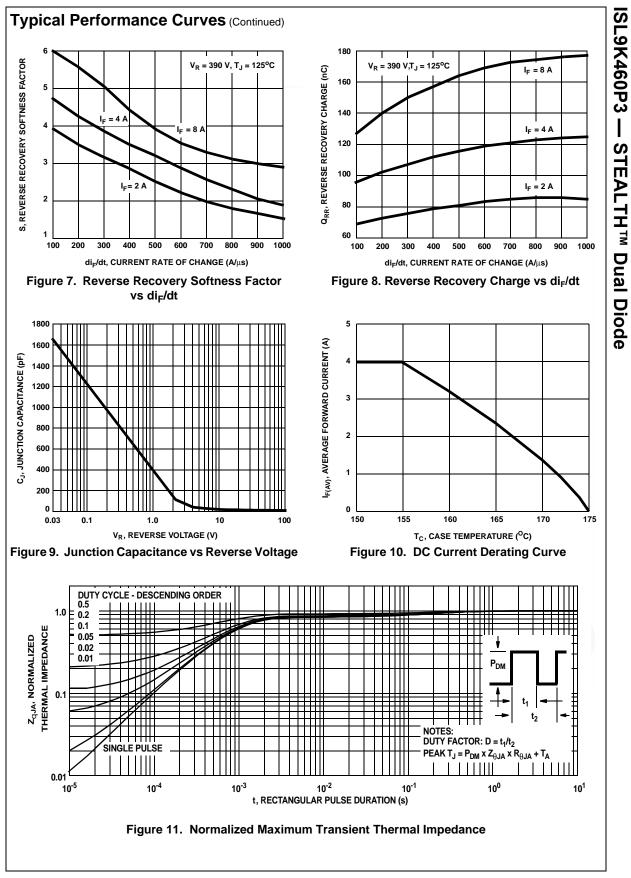
Symbol	Parameter	Rating	Unit V	
V _{RRM}	Peak Repetitive Reverse Voltage	600		
V _{RWM}	Working Peak Reverse Voltage	600	V	
V _R	DC Blocking Voltage	600	V	
I _{F(AV)}	Average Rectified Forward Current (T _C = 155°C)	4	Α	
	Total Device Current (Both Legs)	8	Α	
I _{FRM}	Repetitive Peak Surge Current (20kHz Square Wave)	8	Α	
I _{FSM}	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)	50	Α	
PD	Power Dissipation	58	W	
E _{AVL}	Avalanche Energy (0.5A, 80mH)	10	mJ	
Г _Ј , Т _{STG}	Operating and Storage Temperature Range	-55 to 175	°C	
ΤL	Maximum Temperature for Soldering	300	°C	
T _{PKG}	Leads at 0.063in (1.6mm) from Case for 10s	260	°C	
	Package Body for 10s, See Techbrief TB334			

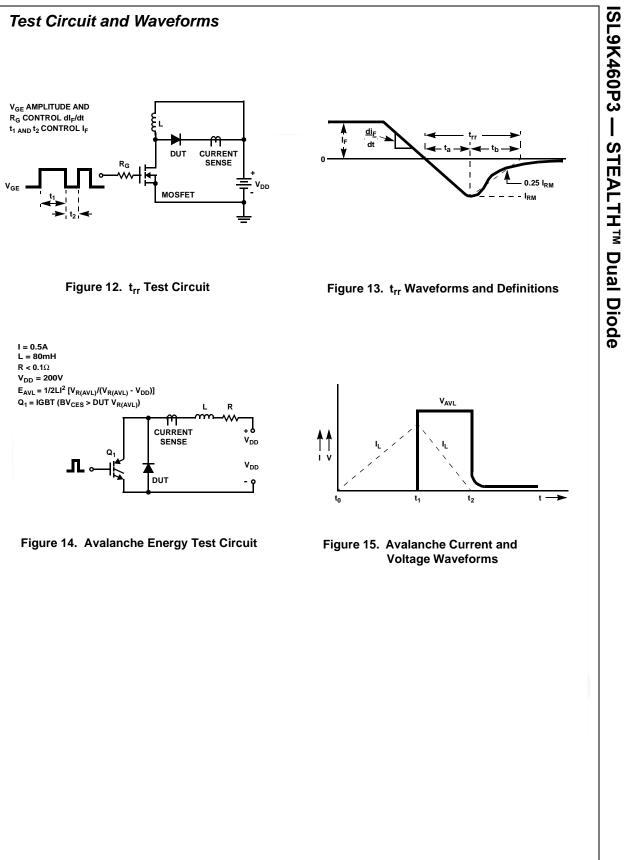
Part NumberTop MarkISL9K460P3K460P3		Top Mark	Package	Packing Method	Reel Size	Tape Width		Qı	Quantity 50	
		K460P3	TO-220	Tube	N/A					
Electric	cal C	Characteris	stics (per leg) T _C = 25°C unless of	herwise noted					
Symbol		Paran	neter	Test Con	ditions	Min	Тур	Max	Unit	
Off State	Cha	racteristics								
I _R	Instantaneous Reverse Current			V _R = 600 V	T _C = 25°C	-	_	100	μA	
·ĸ				IR COOL	$T_{\rm C} = 125^{\circ}{\rm C}$	-	-	1.0	mA	
On State	Cha	racteristics								
V _F				I _F = 4 A	T _C = 25°C	-	2.0	2.4	V	
					$T_{\rm C} = 125^{\circ}{\rm C}$	-	1.6	2.0	v	
					10 120 0		1.0	2.0		
-		racteristics				1	4.5		-	
CJ	Junction Capacitance			V _R = 10 V, I _F = 0 A		-	19	-	pF	
Switchin	g Ch	aracteristics	5							
t _{rr} Reverse Recovery Time				I _F = 1 A, di _F /dt = 10	0 A/μs, V _P = 30 V	-	17	20	ns	
						19	22	ns		
t _{rr}	Reve	erse Recovery Ti	me	I _F = 4 A,	$I_F = 4 \text{ A}, \text{ di}_F/\text{dt} = 100 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$ $I_F = 4 \text{ A},$ $\text{di}_F/\text{dt} = 200 \text{ A}/\mu\text{s}, \text{ V}_R = 390 \text{ V},$			-	ns	
I _{rr}		erse Recovery C						-	Α	
Q _{rr}		erse Recovery C		$T_{\rm C} = 25^{\circ}{\rm C}$		-	22	-	nC	
t _{rr}		erse Recovery Ti	-	I _F = 4 A,	I _E = 4 A,		77	-	ns	
S	Softness Factor (t _b /t _a) Reverse Recovery Current			di _F /dt = 200 A/μs, V _R = 390 V,		-	4.2	-		
١ _m						-	2.8	-	Α	
Q _{rr}	_	erse Recovery C		$-T_{C} = 125^{\circ}C$	– T _C = 125°C			-	nC	
t _{rr}	Reverse Recovery Time Softness Factor (t _b /t _a) Reverse Recovery Current			I _E = 4 A,		-	100 54	-	ns	
S				di _F /dt = 400 A/μs,	V _R = 390 V,		3.5	-		
۱ _۳							4.3	-	Α	
Q _{rr}	_	erse Recovery C		— T _C = 125°C	$I_{\rm C} = 125^{\circ}{\rm C}$		110	-	nC	
dI _M /dt		mum di/dt during				-	500	-	A/µs	
		racteristics		1						
R _{θJC}			Junction to Case			-	_	2.6	°C/W	
R _{θJA}			Junction to Ambie	nt TO-220		-	-	62	°C/W	

ISL9K460P3 — STEALTH™ Dual Diode



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Mechanical Dimensions ø^{4.09} 3.50∠ ⊕ 0,36 M B AM в 4.83 3.56 А 10.67 8.89 3.43 2.54 6.86 5.84 △13.40 12.19 16.51 14,22 △9.40 8.38 3 2 1 5° 5° С 6.35 MAX 14.73 12.70 0,61 1,78 (1.91)1.02 - 2.92 2.03 ⊕ 0.36 M B AM 2.54 NOTES: UNLESS OTHERWISE SPECIFIED A) REFERENCE JEDEC, TO-20, ISSUE K, VARIATION AB, DATED APRIL, 2002. B) ALL DIMENSIONS ARE IN MILLIMETERS. C) DIMENSIONING AND TOLERANCING PER ANSI Y14,5 - 1973 5.08 ANSI Y14,5 - 19/3 D) LOCATION OF THE PIN HOLE MAY VARY (LOWER LEFT CORNER, LOWER CENTER AND CENTER OF THE PACKAGE) DOES NOT COMPLY JEDEC STANDARD VALUE, F) *A'T DIMENSIONS REPRESENT LIKE BELOW: SINGLE GAUGE = 0.51 - 0.61 DUAL GAUGE = 1,14 - 1,40 ᆂ шłа G) DRAWING FILE NAME: TO220B03REV6 Figure 16. TO-220 3L - TO-220, MOLDED, 3LEAD, JEDEC VARIATION AB ON Semiconductor and 💷 are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages.

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