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December 2014

ISL9R1560P2_F085 15A, 600V Stealth Rectifier

Features

- High Speed Switching (t_{rr}=30ns(Typ.) @ I_F=15A)
- Low Forward Voltage(V_F=2.2V(Max.) @ I_F=15A)
- · Avalanche Energy Rated
- · AEC-Q101 Qualified

Applications

- · Automotive DCDC Converter
- · Automotive On Board Charger
- · Switching Power Supply
- · Power Switching Circuits

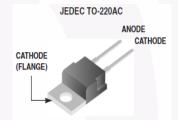
Max Ratings (600V, 15A)

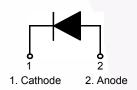
The ISL9R1560P2_F085 is a Stealth™ diode with soft recovery characteristics (trr < 30ns). It has a low forward-voltage drop and is of silicon nitride passivated, ion-implanted, epitaxial construction.

This device is intended for use as a freewheel/clamping diode in various automotive switching power supplies and other power switching applications.

Its low stored charge as well as Stealth™ and soft recovery characteristics minimize ringing and electrical noise while reduce the overall power loss.

Pin Assignments





Absolute Maximum Ratings T_C = 25°C unless otherwise noted

Symbol	Parameter	Ratings	Units	
V _{RRM}	Peak Repetitive Reverse Voltage	600	V	
V _{RWM}	Working Peak Reverse Voltage 600 V			
V _R	DC Blocking Voltage 600			
I _{F(AV)}	Average Rectified Forward Current @ T _C = 25°C	15	Α	
I _{FSM}	Non-repetitive Peak Surge Current (Halfwave 1 Phase 50Hz)	45	Α	
E _{AVL}	Avalanche Energy (1A, 40mH)	20	mJ	
T _{J,} T _{STG}	Operating Junction and Storage Temperature	- 55 to +175	°C	

Thermal Characteristics T_C = 25°C unless otherwise noted

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Maximum Thermal Resistance, Junction to Case	0.93	°C/W
$R_{\theta JA}$	Maximum Thermal Resistance, Junction to Ambient	62	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Tube	Quantity	
ISL9R1560P2		TO-220AC -		50	

Electrical Characteristics $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Conditions		Min.	Тур.	Max	Units
I_R	Instantaneous Reverse Current	V _R = 600V	T _C = 25 °C	-	-	100	uA
			T _C = 175 °C	-	-	1000	uA
V _{FM} ¹	Instantaneous Forward Voltage	I _F = 15A	T _C = 25 °C T _C = 175 °C	-	1.65 1.24	2.2 1.7	V V
t _{rr} ²	Reverse Recovery Time	I_F =1A, di/dt = 200A/ μ s, V_R = 390V	T _C = 25 °C	-	22	30	ns
		I_F =15A, di/dt = 200A/ μ s, V _R = 390V	T _C = 25 °C T _C = 175 °C	-	30 127		ns ns
t _a t _b	Reverse Recovery Time	I_F =15A, di/dt = 200A/ μ s, V_R = 390V	T _C = 25 °C	-	17 13		ns ns
Q_{rr}	Reverse Recovery Charge			-	48	-	nC

Notes:

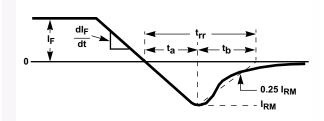
- 1. Pulse : Test Pulse width = 300μs, Duty Cycle = 2%
- 2. Guaranteed by design

Test Circuit and Waveforms

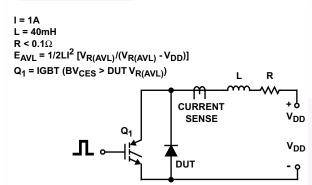
t_{rr} Test Circuit

V_{GE} AMPLITUDE AND R_G CONTROL di_F/dt t₁ AND t₂ CONTROL l_F UUT CURRENT SENSE V_{GE} V_{DD}

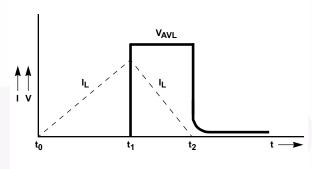
t_{rr} Waveforms and Definitions



Avalanche Energy Test Circuit



Avalanche Current and Voltage Waveforms



Typical Performance Characteristics

Figure 1. Typical Forward Voltage Drop vs. Forward Current

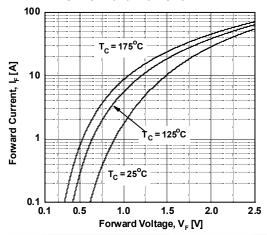


Figure 3.Typical Junction Capacitance

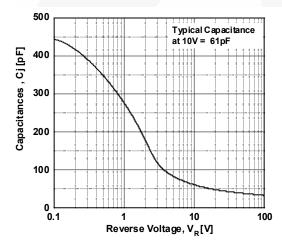


Figure 5. Typical Reverse Recovery Current vs. di/dt

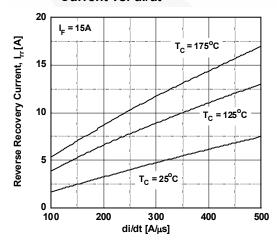


Figure 2. Typical Reverse Current vs. Reverse Voltage

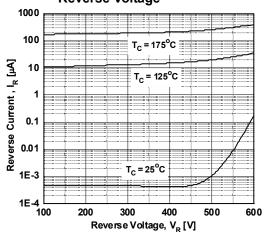


Figure 4. Typical Reverse Recovery Time vs. di/dt

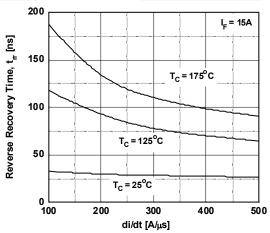
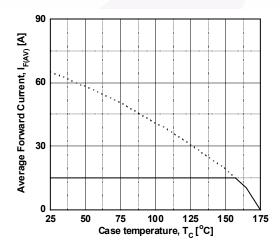


Figure 6. Forward Current Derating Curve



Typical Performance Characteristics (Continued)

Figure 7. Reverse Recovery Charge

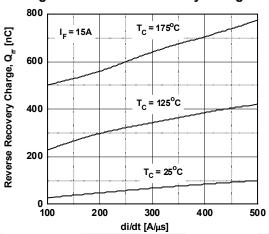
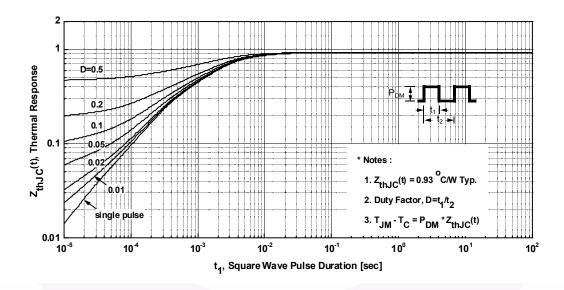
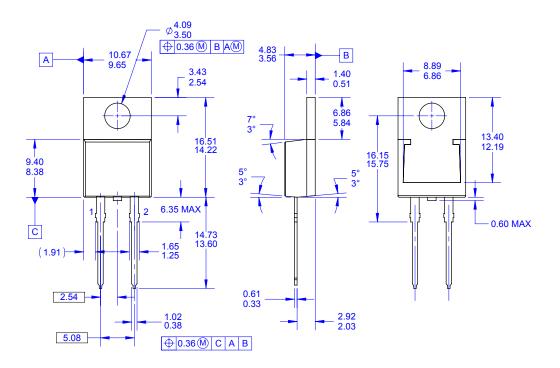


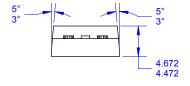
Figure 8. Transient Thermal Response Curve



Mechanical Dimensions

TO-220-2L







NOTES:

- A. PACKAGE REFERENCE: JEDEC TO220,ISSUE K, VARIATION AC, DATED APRIL 2002.

 B. ALL DIMENSIONS ARE IN MILLIMETERS.
 C. DIMENSION AND TOLERANCE AS PER ASME Y14.5-2009.

 D. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
 E. DRAWING FILE NAME: TO220A02REV04.

Dimensions in Millimeters



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Definition of Terms

Datasheet Identification	Product Status	Definition		
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
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