## **N-Channel JFET**

# J109, MMBFJ108

### Features

- This Device is Designed for Digital Switching Applications where Very Low On Resistance is Mandatory
- Sourced from Process 58
- These are Pb-Free Devices

<b>MAXIMUM RATINGS</b> ( $T_A = 25^{\circ}C$ unless otherwise specified) (Notes 1, 2)
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Symbol	Parameter	Value	Unit
V <sub>DG</sub>	Drain-Gate Voltage	25	V
V <sub>GS</sub>	Gate-Source Voltage	-25	V
I <sub>GF</sub>	Forward Gate Current	10	mA
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range	–55 to 150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. These ratings are based on a maximum junction temperature of 150°C.
- These are steady-state limits. ON Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

		Max		
Symbol	Parameter	<b>J109</b> (Note 3)	MMBFJ108 (Note 4)	Unit
PD	Total Device Dissipation	625	350	mW
	Derate Above 25°C	5.0	2.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	125	-	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	200	357	°C/W

#### **THERMAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise specified)

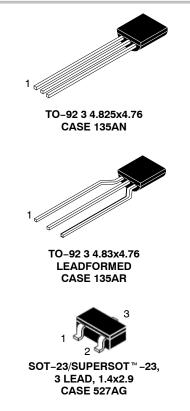
3. PCB size: FR-4, 76 mm x 114 mm x 1.57 mm (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

 Device mounted on FR-4 PCB 36 mm x 18 mm x 1.5 mm; mounting pad for the collector lead minimum 6 cm<sup>2</sup>.



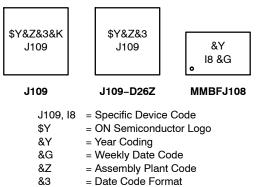
### **ON Semiconductor®**

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1. Drain, 2. Source, 3. Gate

### MARKING DIAGRAM



&K = Lot Run Traceability Code

### ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conc	lition	Min	Max	Unit
OFF CHARACTERISTICS						
V <sub>(BR)GSS</sub>	Gate-Source Breakdown Voltage	$I_{G} = -10 \ \mu A, V_{DS} = 0$		-25	_	V
I <sub>GSS</sub>	Gate Reverse Current	$V_{GS} = -15 \text{ V}, \text{ V}_{DS} = 0$		-	-3.0	nA
		$V_{GS} = -15 \text{ V}, \text{ V}_{DS} = 0, \text{ T}_{A}$	_= 100°C	-	-200	
V <sub>GS</sub> (off)	Gate-Source Cut-Off Voltage	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 10 nA	MMBFJ108	-3.0	-10.0	V
			J109	-2.0	-6.0	

#### **ON CHARACTERISTICS**

I <sub>DSS</sub>	Zero-Gate Voltage Drain Current (Note 5)	$V_{DS} = 15 V, V_{GS} = 0$	MMBFJ108	80	-	mA
			J109	40	-	
r <sub>DS</sub> (on)	Drain-Source On Resistance	$V_{DS}{\leq}0.1$ V, $V_{GS}{=}0$	MMBFJ108	-	8.0	Ω
			J109	-	12	

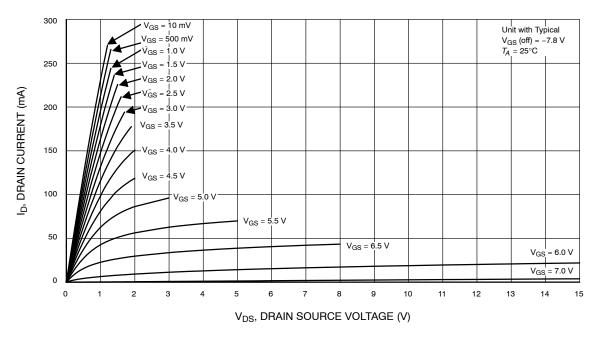
#### SMALL SIGNAL CHARACTERISTICS

C <sub>dg</sub> (on) C <sub>sg</sub> (off)	Drain-Gate & Source-Gate On Capacitance	$V_{DS}$ = 0, $V_{GS}$ = 0, f = 1.0 MHz	_	85	pF
C <sub>dg</sub> (off)	Drain-Gate Off Capacitance	$V_{DS}$ = 0, $V_{GS}$ = -10 V, f = 1.0 MHz	-	15	pF
C <sub>sg</sub> (off)	Source-Gate Off Capacitance	$V_{DS} = 0, V_{GS} = -10 \text{ V}, \text{ f} = 1.0 \text{ MHz}$	-	15	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

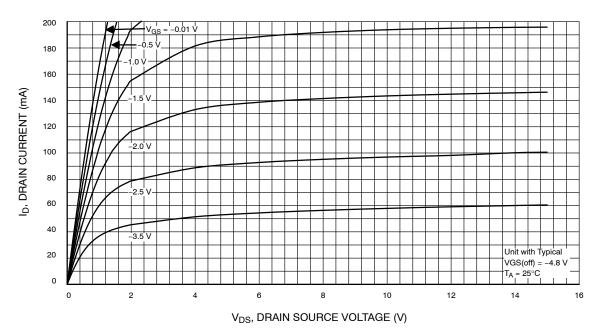
5. Pulse test: pulse width  $\leq$  300 µs, duty cycle  $\leq$  2%.

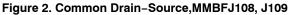
### **TYPICAL PERFORMANCE CHARACTERISTICS**





### TYPICAL PERFORMANCE CHARACTERISTICS (continued)





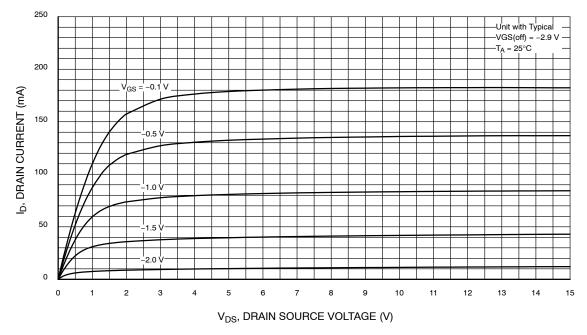
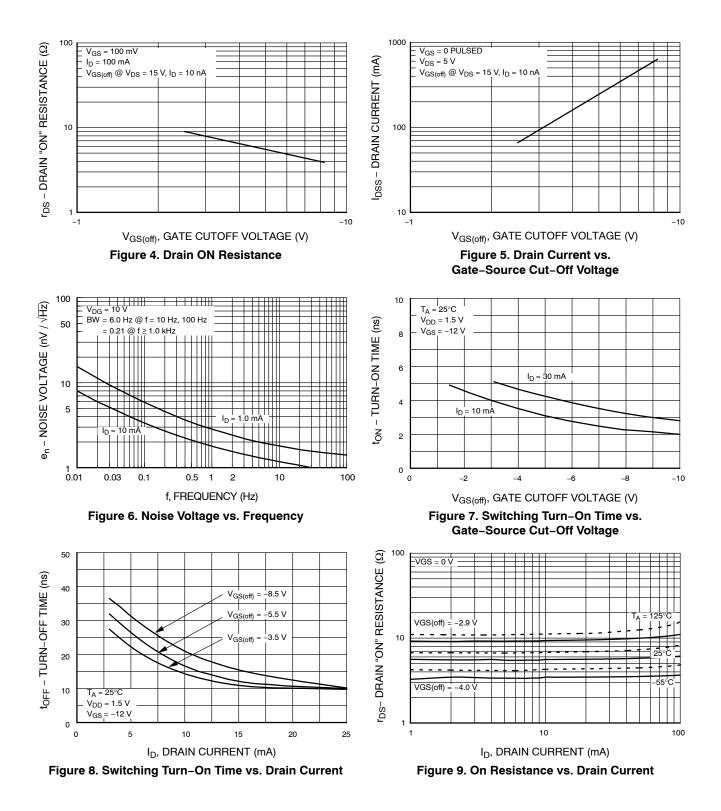


Figure 3. Common Drain–Source, J109

### TYPICAL PERFORMANCE CHARACTERISTICS (continued)



### TYPICAL PERFORMANCE CHARACTERISTICS (continued)

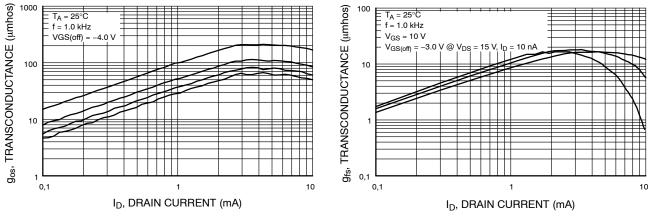


Figure 10. Output Conductance vs. Drain Current

Figure 11. Output Conductance vs. Drain Current

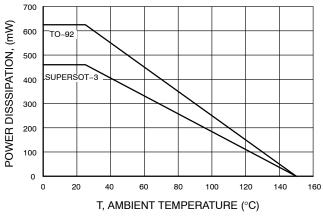


Figure 12. Power Dissipation vs. Ambient Temperature

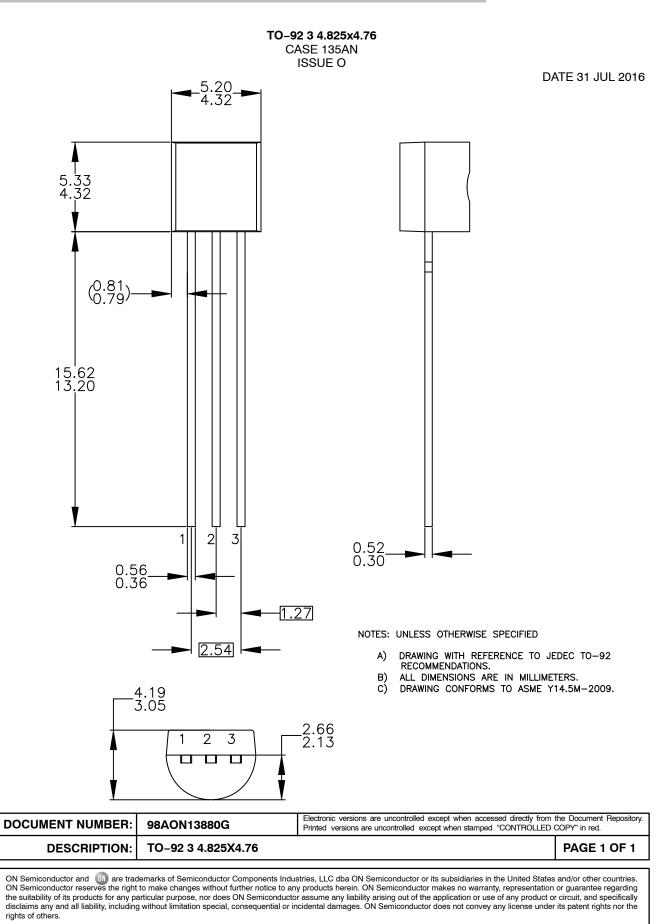
### **ORDERING INFORMATION**

Part Number	Top Mark	Package	Shipping <sup>†</sup>
J109	J109	TO-92 3L (Pb-Free)	10000 Units / Bulk
J109-D26Z	J109	TO-92 3L (Pb-Free)	2000 / Tape & Reel
MMBFJ108	18	SSOT 3L (Pb–Free)	3000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

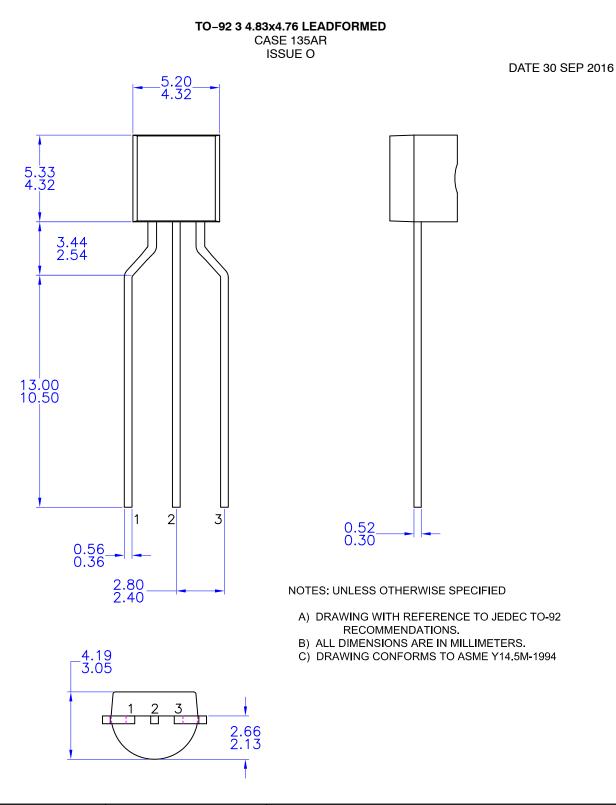
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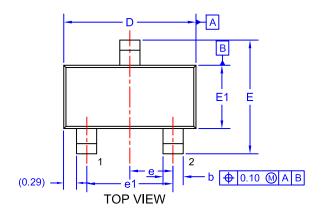
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### **MECHANICAL CASE OUTLINE** PACKAGE DIMENSIONS



#### SOT-23/SUPERSOT <sup>™</sup> -23, 3 LEAD, 1.4x2.9 CASE 527AG **ISSUE A**

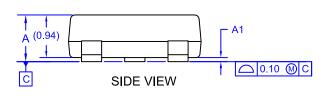
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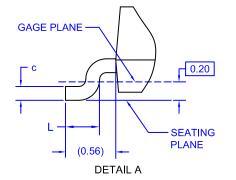


<ol> <li>ASME Y14.3M, 2009.</li> <li>ALL DIMENSIONS ARE IN MILLIMETERS.</li> <li>DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR EXTRUSIONS.</li> </ol>						
DIM	MIN.	NOM.	MAX.			
А	0.85	0.95	1.12			
A1	0.00	0.05	0.10			
b	0.370	0.435	0.508			
с	0.085	0.150	0.180			
D	2.80	3.04				
Е	2.31	2.51	2.71			
E1 1.20 1.40 1.52						
е	e 0.95 BSC					
e1	1.90 BSC					
L	0.33	0.38	0.43			

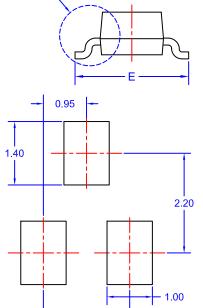
NOTES: UNLESS OTHERWISE SPECIFIED

1. DIMENSIONING AND TOLERANCING PER









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- 1.90 -

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may

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•	(Note: Microdot may be in	either location) not follow the Generic Marking.	

XXX = Specific Device Code

= Pb-Free Package

= Month Code

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GENERIC **MARKING DIAGRAM\*** 

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