

Is Now Part of



ON Semiconductor®

To learn more about ON Semiconductor, please visit our website at <u>www.onsemi.com</u>

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild_questions@onsemi.com.

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized applications, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an equif prese





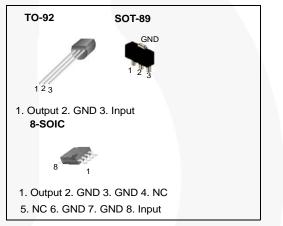
KA78LXXA / KA78L05AA 3-Terminal 0.1 A Positive Voltage Regulator

Features

- Maximum Output Current of 100 mA
- Output Voltage of 5 V, 6 V, 8 V, 9 V, 10 V, 12 V, 15 V and 18 V
- Thermal Overload Protection
- Short-Circuit Current Limiting
- Output Voltage Offered in ± 5% Tolerance

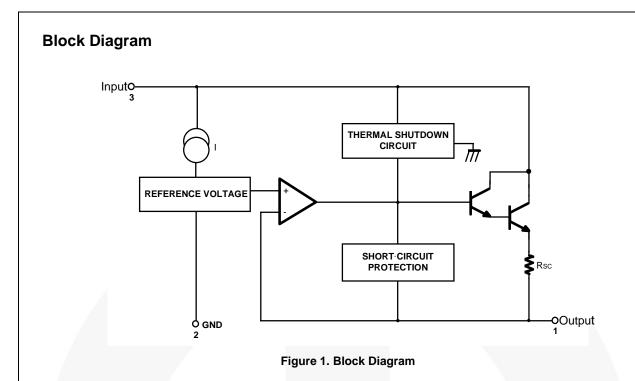
Description

The KA78LXXA / KA78L05AA series of fixed-voltage, monolithic, integrated circuit, voltage regulators are suitable for applications that require supply current up to 100 mA.



Ordering Information

Product Number	Package	Packing Method	Output Voltage Tolerance	Operating Temperature
KA78L05AZTA		Ammo		
KA78L05AZBU		Bulk		
KA78L06AZTA		Ammo		
KA78L08AZTA		Ammo		
KA78L09AZTA	TO-92	Ammo		
KA78L10AZTA		Ammo		
KA78L12AZTA		Ammo	± 5%	-40 to +125 °C
KA78L15AZTA		Ammo		
KA78L18AZTA		Ammo		
KA78L05AMTF		Tape & Reel		
KA78L08AMTF	SOT-89	Tape & Reel	1	
KA78L12AMTF		Tape & Reel		
KA78L05ADTF	8-SOIC	Tape & Reel		
KA78L05AAZTA	TO-92	Ammo	± 3%	0 to +125 °C



Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parar	neter	Value	Unit
V		$V_0 = 5 V \text{ to } 8 V$	30	V
VI	Input Voltage	V _O = 12 V to 18 V	35	V
т	Operating Temperature Dance	KA78LXXA	-40 to +125	°C
T _{OPR}	Operating Temperature Range	KA78L05AA	0 to +125	
T _{J(MAX)}	Maximum Junction Temperature		150	°C
T _{STG}	Storage Temperature Range		-65 to +150	°C
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction-Case	TO-92	50	°C/W
		TO-92	150	°C/W
$R_{ hetaJA}$	Thermal Resistance, Junction-Air	SOT-89	225	°C/W
		8-SOIC	160	°C/W

Electrical Characteristics (KA78L05A)

 $V_I = 10 \text{ V}, I_O = 40 \text{ mA}, -40^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}, \text{ } C_I = 0.33 \text{ } \mu\text{F}, \text{ } C_O = 0.1 \text{ } \mu\text{F}, \text{ unless otherwise specified}.$

Symbol	Paramete	Parameter		ditions	Min.	Тур.	Max.	Unit
V _O	Output Voltage		$T_J = 25^{\circ}C$		4.8	5.0	5.2	V
41/	Line Regulation ⁽¹⁾		T _ 25°C	$7 \text{ V} \leq \text{V}_{I} \leq 20 \text{ V}$		8	150	mV
ΔV_{O}			T _J = 25°C	$8 \text{ V} \le \text{V}_{I} \le 20 \text{ V}$		6	100	mV
A\/	Load Regulation ⁽¹⁾		T _{.1} = 25°C	$1 \text{ mA} \le I_O \le 100 \text{ mA}$		11	60	mV
ΔV_{O}			$I_{\rm J} = 25 {\rm C}$	$1 \text{ mA} \le I_O \le 40 \text{ mA}$		5.0	30	mV
V			$7 \text{ V} \leq \text{V}_{\text{I}} \leq 20 \text{ V}$	$1 \text{ mA} \le I_O \le 40 \text{ mA}$			5.25	V
Vo	Output Voltage	Output Voltage		$1 \text{ mA} \le I_O \le 70 \text{ mA}$	4.75		5.25	V
Ι _Q	Quiescent Current		$T_J = 25^{\circ}C$			2.0	5.5	mA
ΔI_Q	Quiescent Current	With Line	$8 \text{ V} \leq \text{V}_{I} \leq 20 \text{ V}$				1.5	mA
ΔI_Q	Change	With Load	$1 \text{ mA} \le I_O \le 40 \text{ mA}$	4			0.1	mA
V _N	Output Noise Voltag	е	T _A = 25°C, 10 Hz	≤ f ≤ 100 kHz		40		μV/Vc
$\Delta V_O / \Delta T$	Temperature Coeffic	cient of V _O	I _O = 5 mA			-0.65		mV/°C
RR	Ripple Rejection		f = 120 Hz, 8 V ≤ '	$V_{I} \le 18 \text{ V}, \text{ T}_{J} = 25^{\circ}\text{C}$	41	80		dB
V _D	Dropout Voltage		T _J = 25°C			1.7		V

Notes:

1. The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests.

2. Power dissipation: $P_D \le 0.75$ W.

Electrical Characteristics (KA78L06A)

 $V_I = 12 \text{ V}, I_O = 40 \text{ mA}, -40^{\circ}C \leq T_J \leq 125^{\circ}C, C_I = 0.33 \text{ }\mu\text{F}, C_O = 0.1 \text{ }\mu\text{F}, \text{ unless otherwise specified}.$

Symbol	Paramet	er	Co	onditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage		T _J = 25°C		5.75	6.00	6.25	V
A. \ /	Line Regulation ⁽³⁾		т огоо	$8.5 \text{ V} \le \text{V}_{I} \le 20 \text{ V}$		64	175	mV
ΔV_O	Line Regulation (*)		T _J = 25°C	$9 \text{ V} \le \text{V}_{\text{I}} \le 20 \text{ V}$		54	125	mV
A) (Load Deculation ⁽³⁾	(3)		$1 \text{ mA} \le I_O \le 100 \text{ mA}$		12.8	80.0	mV
ΔV_O	Load Regulation (oad Regulation ⁽³⁾		$1 \text{ mA} \le I_O \le 70 \text{ mA}$		5.8	40.0	mV
V			$8.5~V \le V_I \le 20$	V, 1 mA \leq I _O \leq 40 mA			6.3	V
Vo	Output Voltage		$8.5 \text{ V} \le \text{V}_{\text{I}} \le \text{V}_{\text{MA}}$	$_{AX}^{(4)}$, 1 mA \le I _O \le 70 mA	5.7		6.3	V
	Quiescent Current		T _J = 25°C				5.5	mA
Ι _Q	Quiescent Current		T _J = 125°C			3.9	6.0	mA
ΔI_Q	Quiescent Current	With Line	$9 \text{ V} \leq \text{V}_{I} \leq 20 \text{ V}$				1.5	mA
ΔI_Q	Change	With Load	$1 \text{ mA} \le I_O \le 40$	mA			0.1	mA
V _N	Output Noise Voltag	le	T _A = 25°C, 10 ⊦	Hz ≤ f ≤ 100 kHz		40		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coeffic	cient of V _O	I _O = 5 mA			0.75		mV/°C
RR	Ripple Rejection		f = 120 Hz, 10 \	$V \le V_{I} \le 20 \text{ V}, \text{ T}_{J} = 25^{\circ}\text{C}$	40	46		dB
VD	Dropout Voltage		T _J = 25°С			1.7		V

Notes:

3. The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests. 4. Power dissipation: $P_D \le 0.75$ W.

Electrical Characteristics (KA78L08A)

 $V_I = 14 \text{ V}, I_O = 40 \text{ mA}, -40^{\circ}\text{C} \le T_J \le 125^{\circ}\text{C}, C_I = 0.33 \text{ }\mu\text{F}, C_O = 0.1 \text{ }\mu\text{F}, \text{ unless otherwise specified}.$

Symbol	Parameter	Parameter		tions	Min.	Тур.	Max.	Unit
Vo	Output Voltage		$T_J = 25^{\circ}C$		7.7	8.0	8.3	V
A) /	Line Regulation (5	5)	T _{.1} = 25°C	$10.5~V \leq V_I \leq 23~V$		10	175	mV
ΔV_{O}	Line Regulation	,	$T_{\rm J} = 25 \rm C$	$11~V \leq V_{I} \leq 23~V$		8	125	mV
A) /	Load Regulation	5)	T - 25°C	$1 \text{ mA} \le I_O \le 100 \text{ mA}$		15	80	mV
ΔV_O	LOAU Regulation		T _J = 25°C	$1 \text{ mA} \le I_O \le 40 \text{ mA}$		8	40	mV
V			$10.5 \text{ V} \le \text{V}_{\text{I}} \le 23 \text{ V}$	$1 \text{ mA} \le I_O \le 40 \text{ mA}$	7.6		8.4	V
Vo	Output Voltage		$10.5 \text{ V} \le \text{V}_{\text{I}} \le \text{V}_{\text{MAX}}^{(6)}$	$1 \text{ mA} \le I_O \le 70 \text{ mA}$	7.6		8.4	V
Ι _Q	Quiescent Curren	t	$T_J = 25^{\circ}C$			2.0	5.5	mA
ΔI_Q	Quiescent	With Line	$11 \text{ V} \leq \text{V}_{I} \leq 23 \text{ V}$				1.5	mA
ΔI_Q	Current Change	With Load	$1 \text{ mA} \le I_O \le 40 \text{ mA}$				0.1	mA
V _N	Output Noise Volt	age	$T_A = 25^{\circ}C$, 10 Hz $\leq f$	≤100 kHz		60		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coe V _O	fficient of	l _O = 5 mA			-0.8		mV/°C
RR	Ripple Rejection		f = 120 Hz, 11 V ≤ V _I :	≤ 21 V, T _J = 25°C	39	70		dB
VD	Dropout Voltage		T _J = 25°C			1.7		V

Notes:

5. The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests. 6. Power dissipation: $P_D \le 0.75$ W.

Electrical Characteristics (KA78L09A)

 $V_I = 15 \text{ V}, I_O = 40 \text{ mA}, -40^{\circ}\text{C} \le T_J \le 125^{\circ}\text{C}, \text{ } C_I = 0.33 \text{ } \mu\text{F}, \text{ } C_O = 0.1 \text{ } \mu\text{F}, \text{ unless otherwise specified}.$

Symbol	Paramet	er	Condi	tions	Min.	Тур.	Max.	Unit
Vo	Output Voltage		$T_J = 25^{\circ}C$		8.64	9.00	9.36	V
ΔV _O	Line Regulation (7)		T ₁ = 25°C	$11.5~V \le V_I \le 24~V$		90	200	mV
Δvo			1 _j = 25 C	$13 \text{ V} \leq \text{V}_{\text{I}} \leq 24 \text{ V}$		100	150	mV
۸\/	Load Regulation (7))	T _{.1} = 25°C	$1 \text{ mA} \le I_O \le 100 \text{ mA}$		20	90	mV
ΔV_O			1 _J = 25 C	$1 \text{ mA} \le I_O \le 40 \text{ mA}$		10	45	mV
V	Output Voltage		11.5 V \le V _I \le 24 V	$1 \text{ mA} \le I_O \le 40 \text{ mA}$	8.55		9.45	V
Vo	Oulput voltage		11.5 V \leq V _I \leq V _{MAX} ⁽⁸⁾	$1 \text{ mA} \le I_O \le 70 \text{ mA}$	8.55		9.45	V
Ι _Q	Quiescent Current		$T_J = 25^{\circ}C$			2.1	6.0	mA
ΔI_Q	Quiescent Current	With Line	$13 \text{ V} \le \text{V}_{\text{I}} \le 24 \text{ V}$				1.5	mA
ΔI_Q	Change	With Load	$1 \text{ mA} \le I_O \le 40 \text{ mA}$				0.1	mA
V _N	Output Noise Volta	ge	$T_A = 25^{\circ}C$, 10 Hz \leq f s	≤ 100 kHz		70		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coeff	icient of V _O	l _O = 5 mA			-0.9		mV/°C
RR	Ripple Rejection		f = 120 Hz, 12 V \leq V _I :	≤ 22 V, T _J = 25°C	38	44		dB
V _D	Dropout Voltage		T _J = 25°C			1.7		V

Notes:

 The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests. 8. Power dissipation: $P_D \le 0.75$ W.

Electrical Characteristics (KA78L10A)

 V_I = 16 V, I_O = 40 mA, -40 °C ≤ T_J ≤ 125 °C, C_I = 0.33 μ F, C_O = 0.1 μ F, unless otherwise specified.

Symbol	Paramete	ər		Conditions	Min.	Тур.	Max.	Unit
Vo	Output Voltage		$T_J = 25^{\circ}C$	T _J = 25°C		10.0	10.4	V
A\/	Line Regulation ⁽⁹⁾		T _ 25°C	$12.5 \text{ V} \le \text{V}_{\text{I}} \le 25 \text{ V}$		100	220	mV
ΔV_{O}			T _J = 25°C	$14 \text{ V} \leq \text{V}_{\text{I}} \leq 25 \text{ V}$		100	170	mV
A) /	Lood Regulation ⁽⁹⁾	(9)		$1 \text{ mA} \le I_O \le 100 \text{ mA}$		20	94	mV
ΔV_{O}	Load Regulation ⁽⁹⁾		T _J = 25°C	$1 \text{ mA} \le I_O \le 70 \text{ mA}$		10	47	mV
			$12.5 \text{ V} \le \text{V}_{\text{I}} \le 25 \text{ V}, 1 \text{ mA} \le \text{I}_{\text{O}} \le 40 \text{ mA}$		9.5		10.5	
V _O	Output Voltage	Output Voltage		≦ V _{MAX} ⁽¹⁰⁾ , 70 mA	9.5		10.5	V
	Quiescent Current		$T_J = 25^{\circ}C$				6.0	~^^
Ι _Q	Quiescent Current		T _J =125°C			4.2	6.5	mA
ΔI_Q	Quiescent Current	With Line	12.5 V ≤ V _I ≤	≦ 25 V			1.5	mA
ΔI_Q	Change	With Load	$1 \text{ mA} \le I_0 \le$	40 mA			0.1	mA
V _N	Output Noise Voltag	e	T _A = 25°C, 1	$0 \text{ Hz} \le f \le 100 \text{ kHz}$		74		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coeffic	ient of V _O	l _O = 5 mA			0.95		mV/°C
RR	Ripple Rejection		f = 120 Hz, 1	$5 \text{ V} \leq \text{V}_{\text{I}} \leq 25 \text{ V}, \text{T}_{\text{J}} = 25^{\circ}\text{C}$	38	43		dB
VD	Dropout Voltage		T _J = 25°C			1.7		V

Notes:

9. The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests. 10. Power dissipation: $P_D \le 0.75$ W.

Electrical Characteristics (KA78L12A)

 $V_I = 19 \text{ V}, I_O = 40 \text{ mA}, -40^{\circ}C \leq T_J \leq 125^{\circ}C, C_I = 0.33 \text{ }\mu\text{F}, C_O = 0.1 \text{ }\mu\text{F}, \text{ unless otherwise specified}.$

Symbol	Parame	ter	Condit	tions	Min.	Тур.	Max.	Unit
Vo	Output Voltage		$T_J = 25^{\circ}C$		11.5	12.0	12.5	V
ΔV _O	Line Regulation ⁽¹	1)	T _{.1} = 25°C	$14.5 \text{ V} \leq \text{V}_{\text{I}} \leq 27 \text{ V}$		20	250	mV
Δv ₀		,	1j = 25 C	$16 \text{ V} \le \text{V}_{\text{I}} \le 27 \text{ V}$		15	200	mV
ΔV _O	Load Regulation (11)	$T_J = 25^{\circ}C$	$1 \text{ mA} \le I_O \le 100 \text{ mA}$		20	100	mV
Δv0	Load Regulation	,		$1 \text{ mA} \le I_O \le 40 \text{ mA}$		10	50	mV
V	Output Voltage		14.5 V \le V _I \le 27 V	$1 \text{ mA} \le I_O \le 40 \text{ mA}$	11.4		12.6	V
Vo	Output voltage	Oulput Voltage		$1 \text{ mA} \le I_O \le 70 \text{ mA}$	11.4		12.6	V
۱ _Q	Quiescent Current	t	$T_J = 25^{\circ}C$			2.1	6.0	mA
ΔI_Q	Quiescent	With Line	16 V \leq V _I \leq 27 V				1.5	mA
ΔI_Q	Current Change	With Load	$1 \text{ mA} \le I_{O} \le 40 \text{ mA}$				0.1	mA
V _N	Output Noise Volta	age	$T_A = 25^{\circ}C$, 10 Hz \leq f \leq	100 kHz		80		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coef	fficient of V _O	l _O = 5 mA			-1.0		mV/°C
RR	Ripple Rejection		f = 120 Hz, 15 V \leq V _I \leq	≤ 25 V, T _J = 25°C	37	65		dB
V _D	Dropout Voltage		T _J = 25°C			1.7		V

Notes:

 The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests. 12. Power dissipation: $P_D \le 0.75$ W.

Electrical Characteristics (KA78L15A)

 $V_I = 23 \text{ V}, I_O = 40 \text{ mA}, -40^{\circ}\text{C} \le T_J \le 125^{\circ}\text{C}, C_I = 0.33 \text{ }\mu\text{F}, C_O = 0.1 \text{ }\mu\text{F}, \text{ unless otherwise specified}.$

Symbol	Parame	eter	Condit	ions	Min.	Тур.	Max.	Unit
Vo	Output Voltage		$T_J = 25^{\circ}C$		14.4	15.0	15.6	V
A) /	Line Regulation ⁽¹	3)	T _{.1} = 25°C	$17.5~V \leq V_I \leq 30~V$		25	300	mV
ΔV_O	Lifte Regulation	- /	1 _J = 25 C	$20~V \le V_I \le 30~V$		20	250	mV
A) /	Load Regulation	13)	T - 25°C	$1 \text{ mA} \le I_O \le 100 \text{ mA}$		25	150	mV
ΔV_O	LOAU Regulation		T _J = 25°C	$1 \text{ mA} \le I_O \le 40 \text{ mA}$		12	75	mV
V			$17.5 \text{ V} \le \text{V}_{\text{I}} \le 30 \text{ V}$	$1 \text{ mA} \le I_O \le 40 \text{ mA}$	14.25		15.75	V
Vo	Output Voltage		$17.5 V \le V_I \le V_{MAX}^{(14)}$	$1 \text{ mA} \le I_O \le 70 \text{ mA}$	14.25		15.75	V
Ι _Q	Quiescent Currer	nt	T _J = 25°C			2.1	6.0	mA
ΔI_Q	Quiescent	With Line	$20 \text{ V} \leq \text{V}_{\text{I}} \leq 30 \text{ V}$				1.5	mA
ΔI_Q	Current Change	With Load	$1 \text{ mA} \le I_O \le 40 \text{ mA}$				0.1	mA
V _N	Output Noise Vol	tage	$T_A = 25^{\circ}C$, 10 Hz $\leq f \leq$	100 kHz		90		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coe V _O	efficient of	l _O = 5 mA			-1.3		mV/°C
RR	Ripple Rejection		f = 120 Hz, 18.5 V ≤ V	l ≤ 28.5 V, T _J =25°C	34	60		dB
VD	Dropout Voltage		T _J = 25°C			1.7		V

Notes:

13. The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests. 14. Power dissipation: $P_D \le 0.75$ W.

Electrical Characteristics (KA78L18A)

 $V_I = 27V, I_O = 40mA, -40^{\circ}C \le T_J \le 125^{\circ}C, C_I = 0.33 \,\mu\text{F}, C_O = 0.1 \,\mu\text{F}, \text{ unless otherwise specified}.$

Symbol	Parame	eter	Condi	tions	Min.	Тур.	Max.	Unit
Vo	Output Voltage		$T_J = 25^{\circ}C$		17.3	18.0	18.7	V
ΔV _O	Line Regulation (1	5)	T _{.1} = 25°C	$21~V \leq V_I \leq 33~V$		145	300	mV
ΔvO		,	1j = 25 C	$22 \text{ V} \leq \text{V}_{\text{I}} \leq 33 \text{ V}$		135	250	mV
ΔV _O	Load Regulation (15)	T,₁ = 25°C	$1 \text{ mA} \le I_O \le 100 \text{ mA}$		30	170	mV
ΔvO	LUAU Regulation	· •	1 j = 25 C	$1 \text{ mA} \le I_O \le 40 \text{ mA}$		15	85	mV
V	Output Voltage		$21~V \le V_I \le 33~V$	$1 \text{ mA} \le I_O \le 40 \text{ mA}$	17.1		18.9	V
Vo	Output voltage	Oulput Voltage		$1 \text{ mA} \le I_O \le 70 \text{ mA}$	17.1		18.9	V
Ι _Q	Quiescent Curren	t	T _J = 25°C			2.2	6.0	mA
ΔI_Q	Quiescent	With Line	$21 \text{ V} \leq \text{V}_{\text{I}} \leq 33 \text{ V}$				1.5	mA
ΔI_Q	Current Change	With Load	$1 \text{ mA} \le I_O \le 40 \text{ mA}$				0.1	mA
V _N	Output Noise Volt	age	$T_A = 25^{\circ}C$, 10 Hz \leq f	≤ 100 kHz		150		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coe	fficient of V _O	l _O = 5 mA			-1.8		mV/°C
RR	Ripple Rejection		$f = 120 \text{ Hz}, 23 \text{ V} \le \text{V}$	≤ 33 V, T _J = 25°C	34	48		dB
V _D	Dropout Voltage		T _J = 25°C			1.7		V

Notes:

15. The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests. 16. Power dissipation: $P_D \le 0.75$ W.

Electrical Characteristics (KA78L05AA)

 $V_I = 10 \text{ V}, I_O = 40 \text{ mA}, 0^{\circ}\text{C} \leq T_J \leq 125^{\circ}\text{C}, C_I = 0.33 \text{ }\mu\text{F}, C_O = 0.1 \text{ }\mu\text{F}, \text{ unless otherwise specified}.$

Symbol	Paramet	er	Conc	litions		Min.	Тур.	Max.	Unit
Vo	Output Voltage		$T_J = 25^{\circ}C$			4.9	5.0	5.1	V
ΔV _O	Line Regulation (17)		T _{.1} = 25°C	$7 \text{ V} \leq \text{V}_{I} \leq 20 \text{ V}$	'		8	150	mV
Δv0			1j=250	$8 \text{ V} \le \text{V}_{I} \le 20 \text{ V}$	'		6	100	mV
ΔV _O	Load Regulation (17)		T _{.1} = 25°C	$1 \text{ mA} \le I_O \le 10$	0 mA		11	50	mV
Δvo	Load Regulation		1 j = 25 C	$1 \text{ mA} \le I_O \le 40$	mA		5.0	25	mV
Vo	Output Voltage		$7 \text{ V} \leq \text{V}_1 \leq 20 \text{ V}$	$1 \text{ mA} \le I_O \le 40$	mA			5.15	V
۷v	Oulput Voltage	Output voltage		$1 \text{ mA} \le I_O \le 70$	mA	4.85		5.15	V
Ι _Q	Quiescent Current		T _J = 25°C				2.0	5.5	mA
ΔI_Q	Quiescent Current	With Line	$8 \text{ V} \leq \text{V}_{I} \leq 20 \text{ V}$					1.5	mA
ΔI_Q	Change	With Load	$1 \text{ mA} \le I_O \le 40 \text{ mA}$					0.1	mA
V _N	Output Noise Voltag	ge	T _A = 25°C, 10 Hz ≤	≦f ≤ 100 kHz			40		μV/Vo
$\Delta V_O / \Delta T$	Temperature Coeffi	cient of V _O	l _O = 5 mA				-0.65		mV/°C
RR	Ripple Rejection		f = 120 Hz, 8 V ≤ V	I ≤ 18 V, T _J = 25	S°C	41	80		dB
V _D	Dropout Voltage		T _J = 25°C				1.7		V

Notes:

 The maximum steady-state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represents pulse test conditions with junction temperature as indicated at the initiation of tests. 18. Power dissipation: $P_D \le 0.75$ W.

KA78LXXA / KA78L05AA — 3-Terminal 0.1 A Positive Voltage Regulator

Typical Application

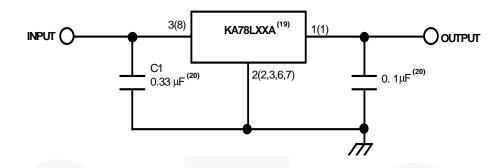
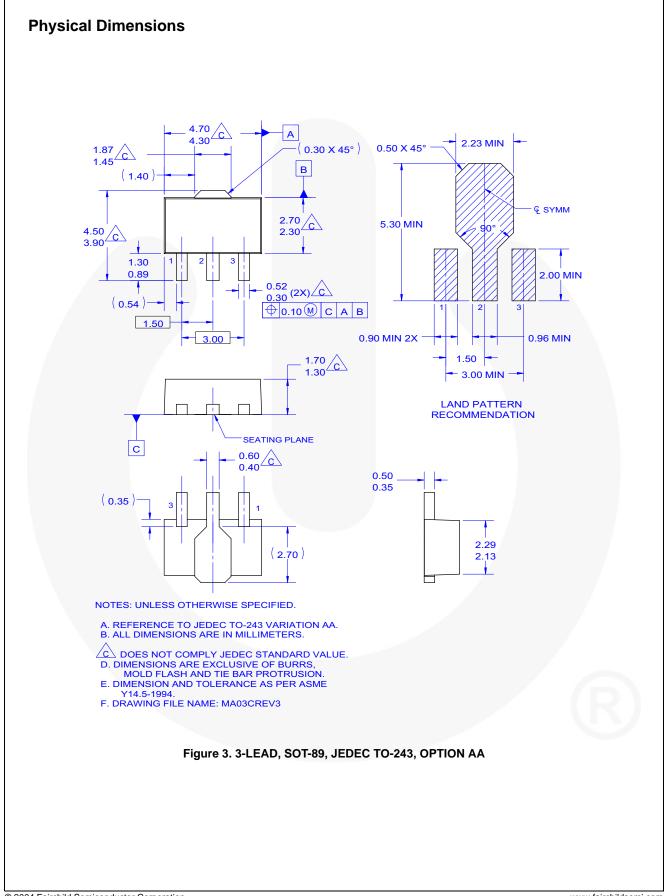
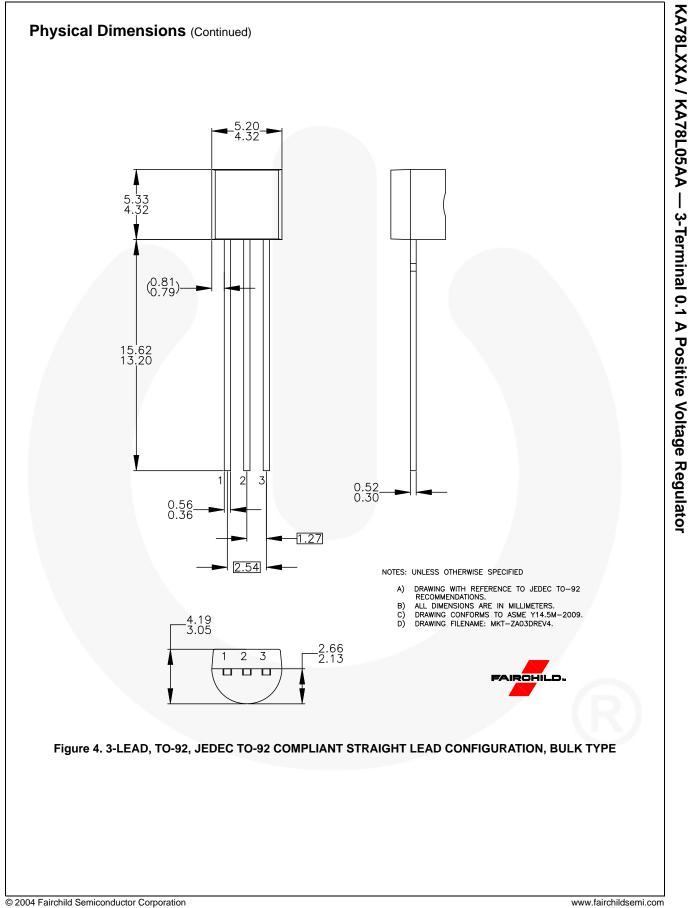


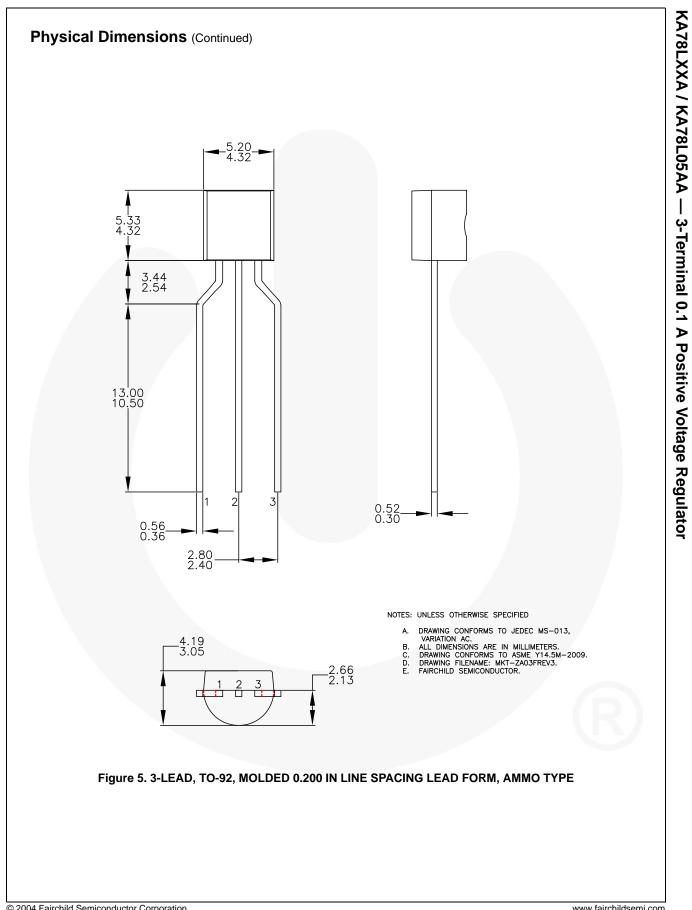
Figure 2. Typical Application

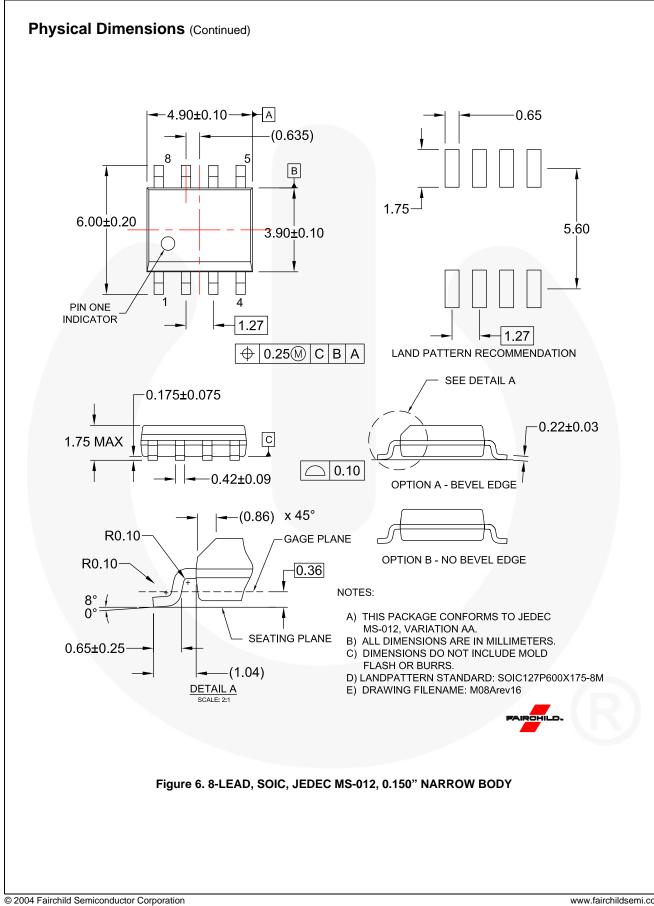
Notes:

- 19. To specify an output voltage, substitute voltage value for "XX".
- 20. Bypass capacitors are recommend for optimum stability and transient response and should be located as close as possible to the regulator.









KA78LXXA / KA78L05AA Rev. 6.9

KA78LXXA / KA78L05AA — 3-Terminal 0.1 A Positive Voltage Regulator

FAIRCHILD. TRADEMARKS The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks. F-PFS™ **OPTOPLANAR[®]** AccuPower™ AttitudeEngine™ **FRFET**[®] Awinda[®] AX-CAP[®]* Global Power ResourceSM ® TinyBoost® GreenBridge™ TinyBuck® Power Supply WebDesigner™ TinyCalc™ BitSiC™ Green FPS™ PowerTrench Build it Now™ TinyLogic® Green FPS™ e-Series™ PowerXS™ CorePI US™ Gmax™ TINYOPTO™ Programmable Active Droop™ CorePOWER™ GTO™ TinyPower™ QFĔT CROSSVOLT™ IntelliMAX™ TinyPWM™ QS™ TinvWire™ CTL™ ISOPI ANAR™ Quiet Series™ Current Transfer Logic™ TranSiC™ Making Small Speakers Sound Louder RapidConfigure™ TriFault Detect™ **DEUXPEED**[®] and Better Dual Cool™ TRUECURRENT®* MegaBuck™ Saving our world, 1mW/W/kW at a time™ **EcoSPARK[®]** MICROCOUPLER™ μSerDes™ SignalWise™ EfficientMax™ MicroFET™ SmartMax™ ESBC™ MicroPak™ SMART START™ MicroPak2™ F UHC Solutions for Your Success™ MillerDrive™ Ultra FRFET™ Fairchild® SPM[®] MotionMax™ UniFET™ Fairchild Semiconductor® STEALTH™ MotionGrid® VCX™ FACT Quiet Series™ SuperFET[®] MTi[®] VisualMax™ FACT[®] FAST[®] SuperSOT™-3 MTx® VoltagePlus™ SuperSOT™-6 MVN® XS™ FastvCore™ SuperSOT™-8 mWSaver® Xsens™ FETBench™ SupreMOS[®] OptoHiT™ 仙童™ **FPS**TM SyncFET™ **OPTOLOGIC®** Sync-Lock™

* Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. TO OBTAIN THE LATEST, MOST UP-TO-DATE DATASHEET AND PRODUCT INFORMATION, VISIT OUR WEBSITE AT <u>HTTP:///WWW.FAIRCHILDSEMI.COM</u>. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

AUTHORIZED USE

Unless otherwise specified in this data sheet, this product is a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability. This product may not be used in the following applications, unless specifically approved in writing by a Fairchild officer: (1) automotive or other transportation, (2) military/aerospace, (3) any safety critical application – including life critical medical equipment – where the failure of the Fairchild product reasonably would be expected to result in personal injury, death or property damage. Customer's use of this product is augement of this Authorized Use policy. In the event of an unauthorized use of Fairchild's product, Fairchild accepts no liability in the event of product failure. In other respects, this product shall be subject to Fairchild's Worldwide Terms and Conditions of Sale, unless a separate agreement has been signed by both Parties.

ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, www.fairchildsemi.com, under Terms of Use

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

PRODUCT STATUS DEFINITIONS

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. 175

www.fairchildsemi.com

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent-Marking.pdf</u>. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor has against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death ass

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor 19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910

Japan Customer Focus Center Phone: 81-3-5817-1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

© Semiconductor Components Industries, LLC

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for Linear Voltage Regulators category:

Click to view products by ON Semiconductor manufacturer:

Other Similar products are found below :

LV56831P-E LV5684PVD-XH MCDTSA6-2R L7815ACV-DG LV56801P-E TCR3DF13,LM(CT TCR3DF39,LM(CT TLE42794G L78L05CZ/ISX L78LR05DL-MA-E LM317T 636416C 714954EB LV5680P-E L78M15CV-DG L79M05T-E TLS202A1MBVHTSA1 L78LR05D-MA-E NCV317MBTG NTE7227 NCV78M09BDTRKG LV5680NPVC-XH LT1054CN8 ME6208A50M3G SL7533-8 ME6231A50M3G ME6231A50PG ME6231C50M5G AMS1117S-3.3 AMS1117-5.0 AMS1117S-5.0 AMS1117-3.3 MD5118 MD5121 MD5127 MD5128 MD5130 MD5144 MD5150 MD5115 MD5125 MD5133 MD5136 MD5140 MD5110 MD52E18WB6 MD52E33WB6 MD52E15QA3 MD52E21QA3 MD52E25QA3