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August 2009

# **KSA473 PNP Epitaxial Silicon Transistor**

#### **Features**

- · Low Frequency Power Amplifier, Power Regulator
- Collector Current : I<sub>C</sub>= -3A
- Collector Dissipation :  $P_C = 10W (T_C=25^{\circ}C)$
- Complement to KSC1173



### Absolute Maximum Ratings \* T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
$V_{CBO}$	Collector-Base Voltage	- 30	V
V <sub>CEO</sub>	Collector-Emitter Voltage	- 30	V
V <sub>EBO</sub>	Emitter-Base Voltage	- 5	V
I <sub>C</sub>	Collector Current	- 3	А
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	10	W
T <sub>J</sub>	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 55 to + 150	°C

<sup>\*</sup> These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

## **Electrical Characteristics** $T_A = 25$ °C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
BV <sub>CBO</sub>	Collector-Base Breakdown Voltage	$I_C = -500 \mu A, I_E = 0$	- 30			V
BV <sub>CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = - 10mA, I <sub>B</sub> = 0	- 30			V
BV <sub>EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = - 1mA, I <sub>C</sub> = 0	- 5			V
I <sub>CBO</sub>	Collector Cut-off Current	$V_{CB} = -20V, I_{E} = 0$			- 1.0	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$			- 1.0	μΑ
h <sub>FE1</sub> h <sub>FE2</sub>	DC Current Gain	$V_{CE} = -2V, I_{C} = -0.5A$ $V_{CE} = -2V, I_{C} = -2.5A$	70 25		240	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	$I_C = -2A$ , $I_B = -0.2A$		- 0.3	- 0.8	V
V <sub>BE</sub> (on)	Base-Emitter On Voltage	$V_{CE} = -2V, I_{C} = -0.5A$		- 0.75	- 1.0	V
f <sub>T</sub>	Current Gain Bandwidth Product	$V_{CE} = -2V, I_{C} = -0.5A$		100		MHz
C <sub>ob</sub>	Output Capacitance	$V_{CB} = -10V, I_{E} = 0,$ f = 1MHz		40		pF

## **h**FE Classification

Classification	0	Υ		
h <sub>FE1</sub>	70 ~ 140	120 ~ 240		

#### **Typical Performance Characteristics**

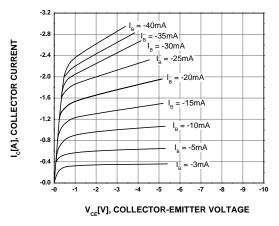


Figure 1. Static Characteristic

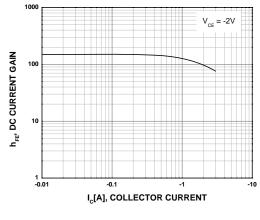


Figure 2. DC current Gain

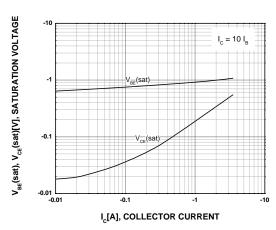


Figure 3. Base-Emitter Saturation Voltage Collector-Emitter Saturation Voltage

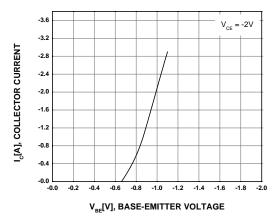


Figure 4. Base-Emitter On Voltage

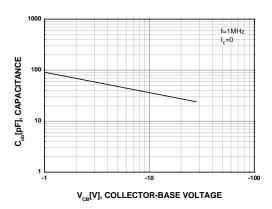
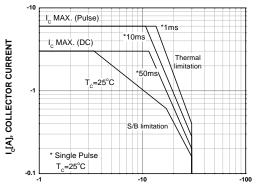


Figure 5. Collector Output Capacitance



 $V_{ce}[V]$ , COLLECTOR-EMITTER VOLTAGE

Figure 6. Safe Operating Area

## **Typical Performance Characteristics**

(Continued)

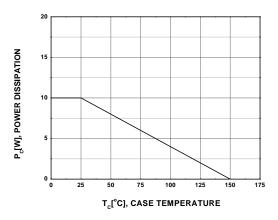
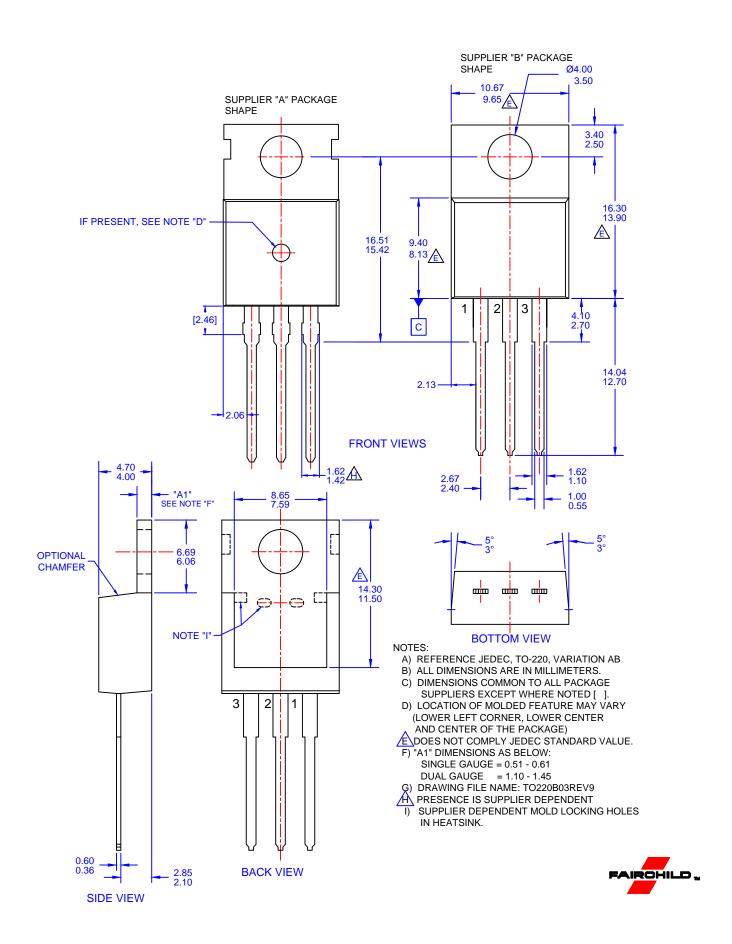


Figure 7. Power Derating



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