Ordering number : ENA2043

## **LA6586FA**

**Monolithic Linear IC** 

# BTL Driver Single-Phase Full-Wave Fan Motor Driver



http://onsemi.com

#### Overview

The LA6586FA is Single-phase bipolar fan motor is put into silent driving by means of BTL output linear drive, offering high efficiency and power saving by suppressing the reactive current. Lock protection and rotation signal (FG, RD) circuits are built in, ensuring optimum application to small fans for notebook PC, consumer equipment power supply, car audio system, CPU cooler, etc. that require high reliability and low noise.

### **Functions**

- Single-phase full-wave linear drive by BTL output (gain resistance  $1k\Omega$ -360k $\Omega$ , 51dB)
- Low-voltage operation possible, with wide operating voltage range (2.2 to 14.0V)
- Low saturation output (Upper + lower saturation voltage : Vosat (total)=1.2Vtyp, IO=200mA)
- Built-in lock protection and automatic reset circuits
- Built-in FG&RD outputs
- Built-in Hall bias (VHB=1.3V)
- Thermal protection circuit
- Extra-small package (Micro10)

## **Specitications**

## Absolute Maximum Ratings at Ta = 25 °C

Parameter	Symbol	Conditions	Ratings	Unit
Output voltage	V <sub>CC</sub> max		15	V
Allowable power dissipation	Pd max	Mounted on a specified board*	400	mW
Output current	I <sub>OUT</sub> max		0.6	Α
Output withstand voltage	V <sub>OUT</sub> max		15	V
RD/FG output pin output	VRD/FG max		15	V
withstand voltage				
RD/FG output current	IRD/FG max		10	mA
HB output current	IB max		10	mA
Operating temperature	Topr		-30 to +90	°C
Storage temperature	Tstg		-55 to +150	°C

<sup>\*</sup>Mounted on a specified board: 20mm×10mm×0.8mm, paper phenol

Caution 1) Absolute maximum ratings represent the value which cannot be exceeded for any length of time.

Caution 2) Even when the device is used within the range of absolute maximum ratings, as a result of continuous usage under high temperature, high current, high voltage, or drastic temperature change, the reliability of the IC may be degraded. Please contact us for the further details.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

## **Recommended Operating Range** at Ta = 25 °C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	VCC		2.2 to 14.0	V
Common-phase input voltage	VICM		0 to V <sub>CC</sub> -1.5	V
range of Hall input				

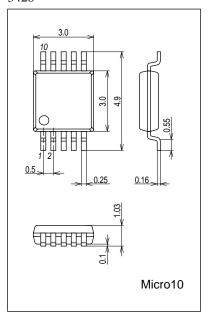
## Electrical Characteristics at $Ta=25~^{\circ}C,\ V_{CC}=12\ V$

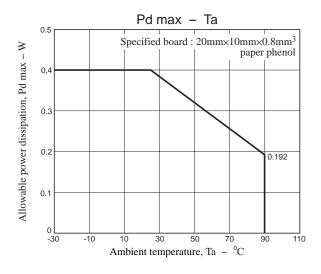
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Parameter	Symbol	Conditions	min	typ	max	Unit ax	
Circuit current	I <sub>CC</sub> 1	During drive (CT = L)	3	6	9	mA	
	I <sub>CC</sub> 2	During lock protection (CT = H)	2.5	5	7.5	mA	
Lock detection capacitor charge current	ICT1		0.9	1.2	1.5	μΑ	
Capacitor discharge current	ICT2		0.10	0.18	0.25	μΑ	
Capacitor charge and discharge current ratio	RCT	RCT = ICT1/ICT2	5	6.5	8	-	
CT charge voltage	VCT1		1.3	1.5	1.7	V	
CT discharge voltage	VCT2		0.3	0.5	0.7	V	
OUT output L saturation voltage	VOL	I <sub>O</sub> = 200 mA		0.25	0.45	V	
OUT output H saturation voltage	VOH	I <sub>O</sub> = 200 mA		0.95	1.2	V	
Input offset voltage	VOFST			3	6	mV	
Hall input sensitivity	VHN	Zero peak value (including offset and hysteresis)		7	15	mV	
Hall bias output L voltage	s output L voltage VHB IHB = 9		1.3	1.5	1.7	V	
RD/FG input pin L voltage	VFG	IRD/FG = 5 mA		0.15	0.3	V	
RD/FG input pin leak current IFGL		VRD/FG = 15 V		1	30	μΑ	
Overheat protection circuit TSD		(*1) Designed target value		180	210	°C	

<sup>(\*1)</sup> The standard is a design target and the measurement with a single unit is not made.

## **Package Dimensions**

unit : mm 3428

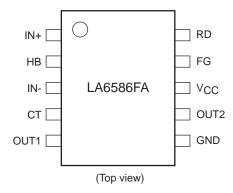




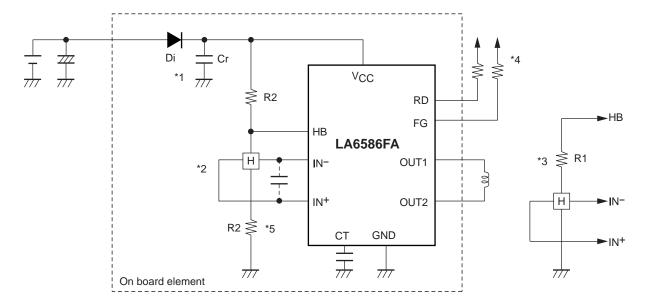
## **Truth Table**

IN-	IN+	CT	OUT1	OUT2	FG	RD	Mode
Н	L	L	Н	L	L	L	During rotation
L	Н		L	Н	Н		
-	-	Н	OFF	OFF	=	Н	During overheat protection

### **Pin Assignment**

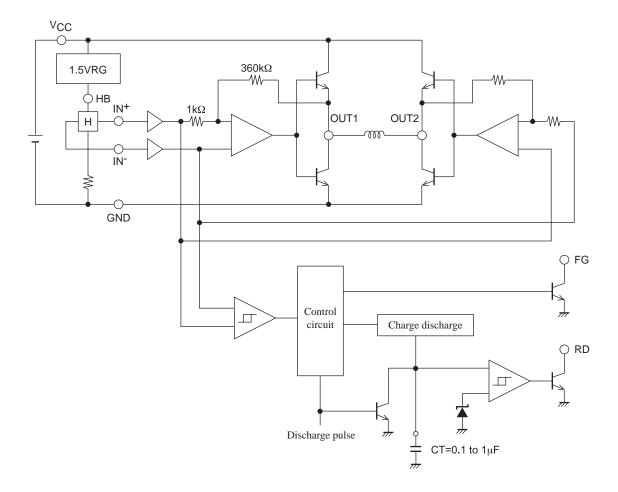


#### **Sample Application Circuit**



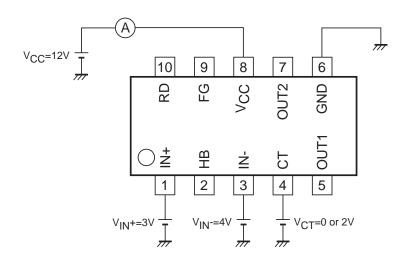
- \*1 To enhance the reliability, it is necessary to insert a capacitor Cr to secure the regenerative current route.
- \*2 To obtain Hall bias from V<sub>CC</sub>, carry out 1/2×V<sub>CC</sub> bias as shown in the figure. Linear driving is made through voltage control of the coil by amplifying the Hall output. When the Hall element output is large, the startup performance and efficiency are improved. Adjustment of the Hall element can reduce the noise further.
- \*3 To obtain Hall bias from the HB pin, carry out constant-voltage bias at about 1.5 V. which enables the Hall element to generate the stable Hall output satisfactory in temperature characteristics. The Hall output amplitude is adjusted with R1. (In the case of  $V_{CC} = 12 \text{ V}$ , \*2 proves advantageous in terms of heat generation in IC.)
- \*4 Keep this open when not used.
- \*5 When the wiring from the Hall output to IC Hall input is long, noise may be carried through the wiring. In this case, insert the capacitor as shown in the figure.

## **Internal Equivalent Circuit**

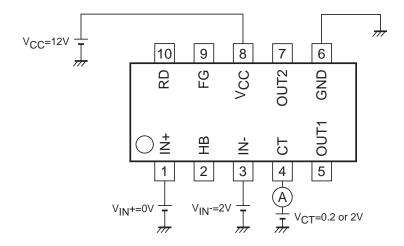


## **Test Circuit Diagram**

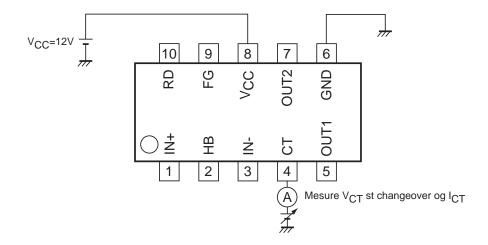
Input current  $(I_{CC})$ 



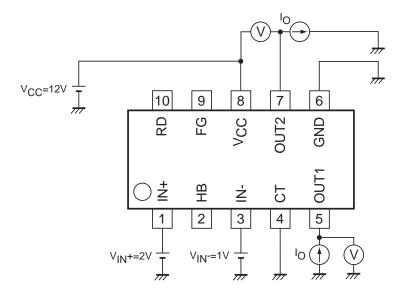
CT pin charge/discharge current (ICT)



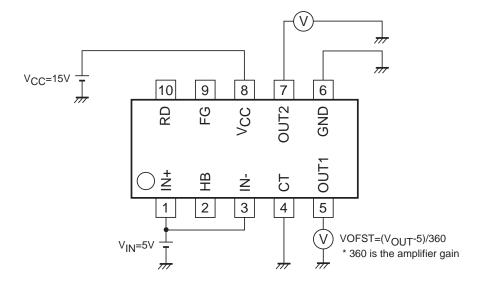
CT pin charge/discharge voltage (VCT)



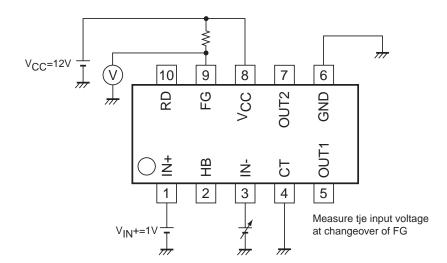
OUT output saturation voltage ( $V_{OL}$ ,  $V_{OH}$ )



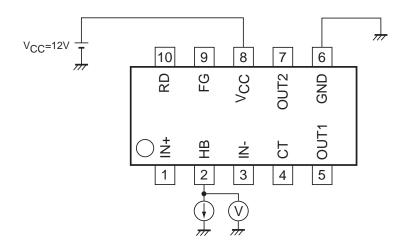
Input offset voltage (VOFST)



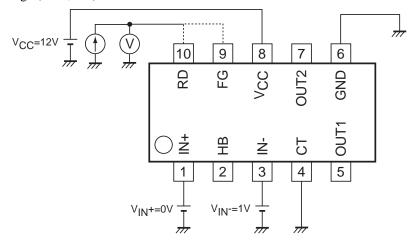
Hall input sensitivity (VHN)



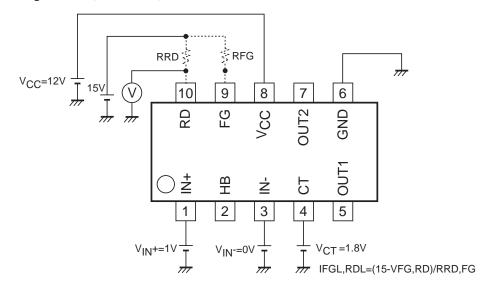
Hall bias output voltage (VHB)



FG, RD input pin L voltage (VFG, RD)



FG, RD input pin leakage current (IFGL, RDL)



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