Monolithic Digital IC 3-phase Sensor Less Motor Driver

Overview

The LB11685AV is a three-phase full-wave current-linear-drive motor driver IC. It adopts a sensor less control system without the use of a Hall Effect device. For quieter operation, the LB11685AV features a current soft switching circuit and be optimal for driving the cooling fan motors used in refrigerators, etc.

Functions

- Three-phase Full-wave Linear Drive (Hall Sensor-less Method)
- Built-in Current Limiter Circuit
- Built-in Three-phase Output Voltage Control Circuit
- Built-in Motor Lock Protection Circuit
- Motor Lock Protection Detection Output
- FG Output Made by Back EMF
- Built-in Thermal Shut Down Circuit
- Beat Lock Prevention Circuit

Specifications

MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum Supply Voltage	V _{CC} max		19	٧
Input Applied Voltage	V _{IN} max		-0.3 to $V_{CC} + 0.3$	V
Maximum Output Current	I _O max (Note 1)		1.2	Α
Allowable Power Dissipation	P _d max	Mounted on a board (Note 2)	1.05	W
Operating Temperature	T _{opr}		-40 to +85	°C
Storage Temperature	T _{stg}		-55 to +150	°C
Junction Temperature	T _j max		150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- 1. The I_O is a peak value of motor-current.
- 2. Specified board: 76.1 mm × 114.3 mm × 1.6 mm, glass epoxy board.

CAUTION: Absolute maximum ratings represent the value which cannot be exceeded for any length of time.

CAUTION: Even when the device is used within the range of absolute

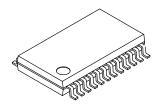
maximum ratings, as a result of continuous usage under high temperature, high current, high voltage, or drastic temperature change, the reliability of the IC may be degraded. Please

contact us for the further details.



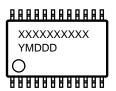
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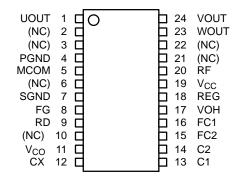
SSOP24J CASE 565AS

MARKING DIAGRAM



XXXXX = Specific Device Code Y = Year M = Month DDD = Additional Traceability Data

PIN ASSIGNMENT



ORDERING INFORMATION

See detailed ordering and shipping information on page 7 of this data sheet.

RECOMMENDED OPERATING CONDITIONS $(T_A = 25^{\circ}C)$

Symbol	Parameter	Conditions	Ratings	Unit
V _{CC}	Recommended Supply Voltage		12.0	V
V _{CC} op	Operating Supply Voltage		4.5 to 18.0	V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{CC}	Supply Current	FC1 = FC2 = 0 V	5	10	20	mA
VREG	Internal Regulate Voltage		3.0	3.3	3.6	V
VOSOUR	Output Voltage (Source)	I _O = 0.8 A (Note 5)		1.3	1.7	V
VOSINK	Output Voltage (Sink)	I _O = 0.8 A (Note 5)		0.5	1.3	V
VOLIM	Current Limiter		0.268	0.300	0.332	V
VINCOM	MCOM Pin Common-input Voltage Range		0		V _{CC} – 2	V
ICOM+	MCOM Pin Source Current for Hysteresis	MCOM = 7 V	30		80	μΑ
ICOM-	MCOM Pin Sink Current for Hysteresis	MCOM = 7 V	30		80	μΑ
RTCOM	MCOM Pin Hysteresis Current Ratio	RTCOM = ICOM+ / ICOM-	0.6		1.4	
I _{VCO}	VCO Input Bias Current	V _{CO} = 2.3 V			0.2	μΑ
f _{VCO} min	VCO Oscillation Minimum Frequency	V _{CO} = 2.1 V, CX = 0.015 μF Design target (Note 4)		930		Hz
f _{VCO} max	VCO Oscillation Maximum Frequency	V _{CO} = 2.7 V, CX = 0.015 μF Design target (Note 4)		8.6		kHz
I _{CX}	CX Charge/Discharge Current	V _{CO} = 2.5 V, CX = 1.6 V	70	100	140	μΑ
ΔVCX	CX Hysteresis Voltage		0.35	0.55	0.75	
IC1(2)+	C1 (C2) Charge Current	V _{CO} = 2.5 V, C1(2) = 1.3 V	12	20	28	μΑ
IC1(2)-	C1 (C2) Discharge Current	V _{CO} = 2.5 V, C1(2) = 1.3 V	12	20	28	μΑ
RTC1(2)	C1 (C2) Charge/Discharge Current Ratio	RTC1(2) = IC1(2)+ / IC1(2)-	0.8	1.0	1.2	
RTCCHG	C1/C2 Charge Current Ratio	RTCCHG = IC1+ / IC2+	0.8	1.0	1.2	
RTCDIS	C1/C2 Discharge Current Ratio	RTCDIS = IC1- / IC2-	0.8	1.0	1.2	
VCW1(2)	C1 (C2) Cramp Voltage Width		1.0	1.3	1.6	V
VFGL	FG Output Low Level Voltage	IFG = 3 mA			0.5	V
VRDL	RD Output Low Level Voltage	IRD = 3 mA			0.5	V
TTSD	Thermal Shut Down Operating Temperature (Note 3)	Junction temperature Design target (Note 4)	150	180		°C
ΔTTSD	Thermal Shut Down Hysteresis Temperature (Note 3)	Junction temperature Design target (Note 4)		15		°C

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. The thermal shut down circuit is built-in for protection from damage of IC. But its operation is out of Topr. Design thermal calculation at normal

- 4. Design target value and no measurement is made.
 5. The I_O is a peak value of motor-current.

operation.

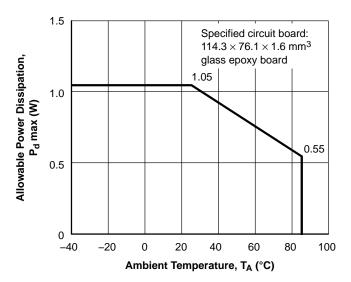


Figure 1. P_d max – T_A

BLOCK DIAGRAM

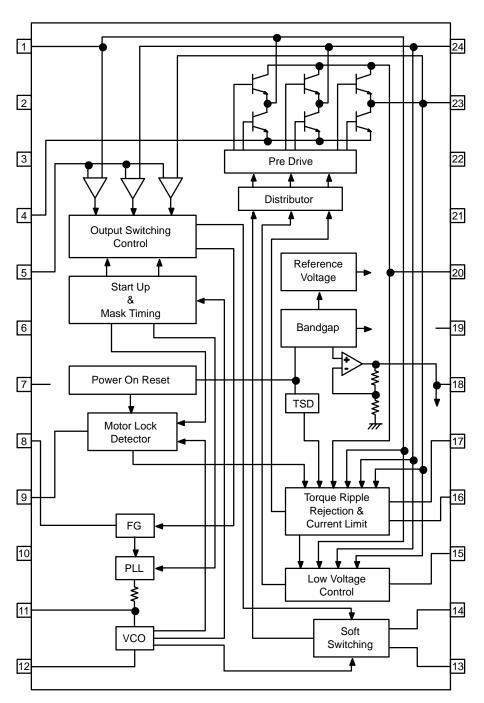


Figure 2. Block Diagram

PIN FUNCTION

PIN FUNCTION

Pin No.	Pin Name	Function	Equivalent Circuit
1 23 24	UOUT WOUT VOUT	Each output pin of three phases.	Pin No. 20
4	PGND	GND pin in the output part. This pin is connected to GND. The SGND pin is also connected to GND	Pin No. 1, 23, 24
20	RF	Pin to detect output current. By connecting a resistor between this pin and V _{CC} , the output current is detected as a voltage. The current limiter is operated by this voltage.	Pin No. 4
5	мсом	Motor coil midpoint input pin. The coil voltage waveform is detected based on this voltage.	SGND SGND SGND VCC Pin No. 5 SGND SGND SGND
7	SGND	Ground pin (except the output part) This pin is connected to GND. The PGND pin is also connected to GND.	
8	FG	FG out made by back EMF pin. It synchronizes FG out with inverted V-phase. When don't use this function, open this pin.	Pin No. 8, 9
9	RD	Motor lock protection detection output pin. Output with L during rotation of motor. Open during lock protection of motor (High-impedance). When don't use this function, open this pin.	SGND Ţʻ SGND
11	VCO	PLL output pin and VCO input pin. To stabilize PLL output, connect a capacitor between this pin and GND.	VREG Vcc Pin No. 11 VREG VREG VREG SGND VREG VREG SGND

PIN FUNCTION (continued)

Pin No.	Pin Name	Function	Equivalent Circuit
12	СХ	VCO oscillation output pin. Operation frequency range and minimum frequency are determined by the capacity of the capacitor connected to this pin.	VREG VCC Pin No. 12 SGND SGND
13 14	C1 C2	Soft switching adjustment pin. The triangular wave from is form formed by connecting a capacitor with this pin. And, the switching of three-phase output is adjusted by the slope.	Pin No. 13, 14 SGND SGND
15	FC2	Frequency characteristic correction pin 2. To suppress the oscillation of control system closed loop of sink-side, connect a capacitor between this pin and GND.	VREG VCC Pin No. 15 SGND SGND
16	FC1	Frequency characteristic correction pin 1. To suppress the oscillation of control system closed loop of source-side, connect a capacitor between this pin and GND.	Pin No. 16 SGND
17	VOH	Three-phase output high level output pin. To stabilize the output voltage of this pin, connect a capacitor between this pin and the V_{CC} pin.	V _{CC} V _{CC} Pin No. 17 SGND
18	VREG	DC voltage (3.3 V) output pin. Connect a capacitor between this pin and GND for stabilization.	Pin No. 18 SGND
19	VCC	Pin to supply power-supply voltage. To curb the influence of ripple and noise. The voltage should be stabilized.	

APPLICATION CIRCUIT EXAMPLE

* Each fixed number in the following Figure 3, is the referential value.

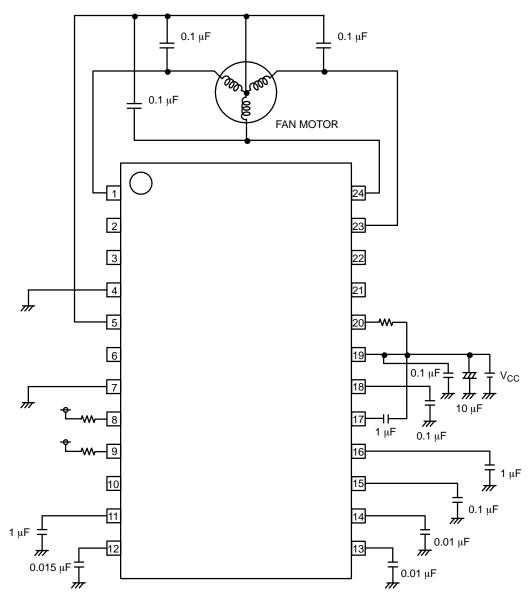


Figure 3. Application Circuit Example

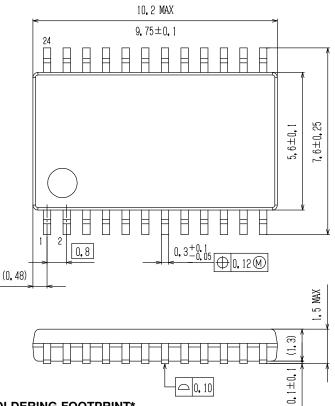
ORDERING INFORMATION

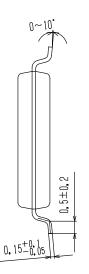
Device	Package	Wire Bond	Shipping [†] (Qty / Packing)
LB11685AV-TLM-H	SSOP24J (275mil) (Pb-Free / Halogen Free)	Au-wire	2000 / Tape & Reel
LB11685AV-W-AH	SSOP24J (275mil) (Pb-Free / Halogen Free)	Cu-wire	2000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

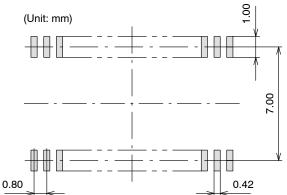


DATE 31 OCT 2013





SOLDERING FOOTPRINT*



GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code

Y = Year

M = Month

DDD = Additional Traceability Data

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTE: The measurements are not to guarantee but for reference only.

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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