# LB1837M

**Monolithic Linear IC** 

# Low-voltage/Low-saturation Bidirectional Constant-Voltage Regulated Motor Driver

#### Overview

The LB1837M is a low-voltage, low-saturation, two-channel motor driver with a bidirectional braking function that provides constant-voltage regulated output for bidirectional operation. The design of the LB1837M is ideal for video equipment, cameras, and other portable equipment.

#### Function

- Wide operating voltage range (3.0 to 9.0 V).
- Low saturation voltage VO (sat) = 0.40 V at IO = 200 mA.
- Consumes almost no current in standby mode  $(0.1 \ \mu A \text{ or less})$ .
- Permits setting of bidirectional constant-voltage regulated value.
- Built-in reference voltage coupled to input.
- Brake function built in.
- Compact MFP14S package.

#### **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

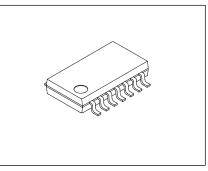
Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	VCC max		10.5	V
Output current	Im max		250	mA
Applied input voltage	VIN		–0.3 to +10	V
Allowable power dissipation	Pd max	With board ( 30 x 30 x 1.5 mm <sub>3</sub> )	800	mW
Operating temperature	Topr		-20 to +80	°C
Storage temperature	Tstg		-40 to +12	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### Allowable Operating Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	Vcc		3.0 to 9.0	V
Input [H] voltage	VIH		3.0 to 9.0	V
Input [L] voltage	VIL		-0.3 to +0.7	V
Control voltage	VC		0.2 to 6.0	V





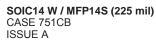
MFP14S(225mil)

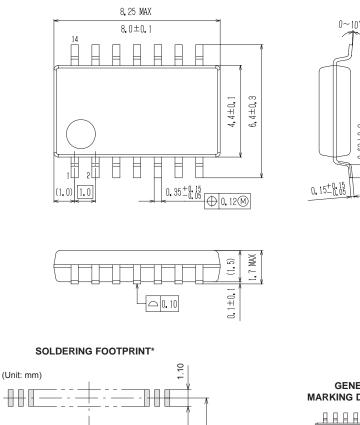
#### Electrical Characteristics at $Ta = 25^{\circ}C$ , $V_{CC} = 6V$

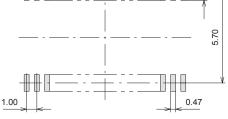
Parameter	Symbol	Conditions	Ratings			1.1
Parameter	Symbol	bol Conditions		typ	max	Unit
Supply current	ICC0	During standby		0.1	10	μA
	ICC1	(For one channel) During bidirectional operation during control, load open		2	3	mA
	ICC2	(For one channel) During bidirectional operation during saturation, load open		3	5	mA
	ICC3	During braking (for one channel)		6.5	9	mA
Output saturation voltage	Vsat1	IO = 100 mA (upper side + lower side)		0.3	0.4	V
	Vsat2	IO = 200 mA (upper side + lower side)		0.4	0.55	V
	Vsat3	IO = 200 mA (lower side)	0.07	0.10	0.15	V
Reference voltage	Vref	lvref = 1 mA	1.85	2.0	2.15	V
Output voltage voltage characteristics	$\frac{\Delta VO}{\Delta VCC}$	VO = 5 V, VCC = 5.5 to 9 V, IO = 100 mA			20	mV
Output voltage current characteristics	ΔVO ΔICC	VO = 5 V, VCC = 6 V, IO = 10 to 100 mA			50	mV
Input current	IIN	VIN = 5 V		90	150	μA
Output voltage	VO	Between OUT and GND 2.5 x			2.7 x VC	V

#### **Package Dimensions**

unit:mm







NOTE: The measurements are not to guarantee but for reference only.

\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC **MARKING DIAGRAM\*** 

0**.**63±0**.**2

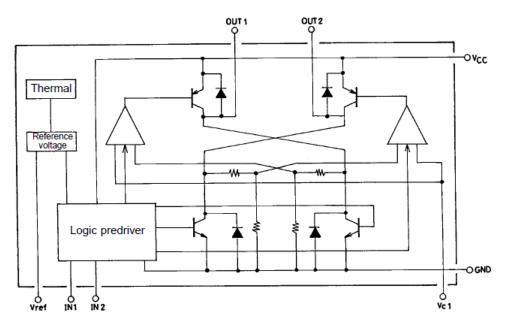


XXXXX = Specific Device Code Y = Year M = Month DDD = Additional Traceability Data

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " =", may or may not be present.

### **Equivalent Circuit Block Diagram**

(For one channel)



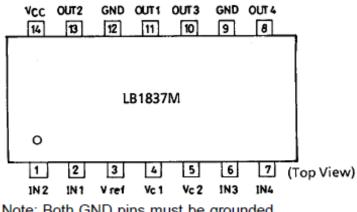
#### **Truth Table**

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Inpi	ut	Output		Mode
IN 1/3	IN 2/4	Out 1/3	Out 2/4	
L	L	OFF	OFF	Standby
Н	L	Н	L	Constant-voltage regulated forward operation
L	Н	L	Н	Constant-voltage regulated reverse operation
Н	Н	L	L	Brake

The constant-voltage regulated output Vo (= voltage between H side output and GND) is controlled by  $2.5 \times VC$ .

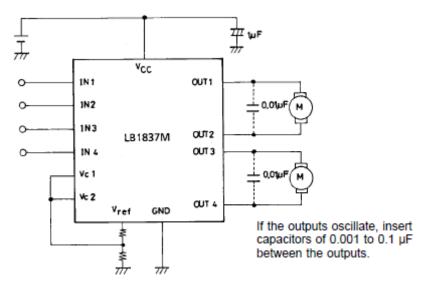
The output is in the saturated state when the VC input range is 0.2 to 6 V and VO  $\ge$  VCC.

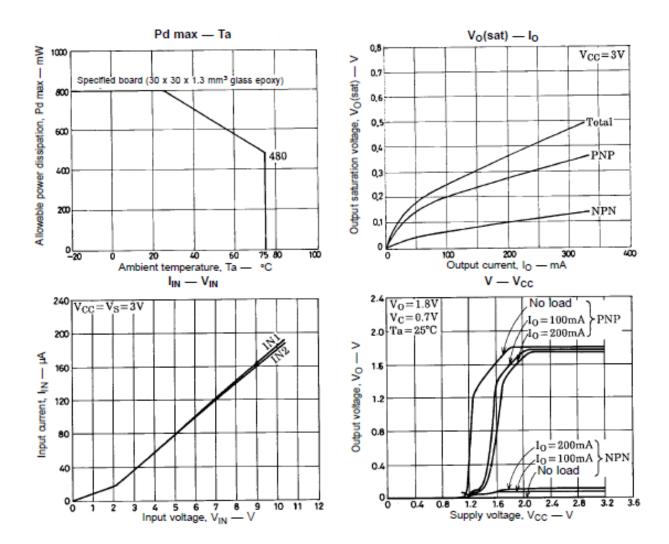
#### **Pin Assignment**



<u>Pin F</u>	Pin Functions						
Pin No.	Symbol	Equivalent Circuit Diagram	Pin Function				
14	VCC		Power supply pin for output and controller.				
9 12	GND		GND pins for output and controller. Both must be grounded.				
1	IN2	Ycc	Input pins that determine the excitation of				
2	IN1	Land Land	the outputs. IN1 and IN2 control outputs OUT1 and				
6	IN3		OUT2; IN3 and IN4 control outputs OUT3				
7	IN4	IN 50KD	and OUT4. When inputs IN1 through IN4 are all low or				
		° † ** • † K	open, the device goes into standby mode				
		<b>★</b> \$80k≏	and current consumption drops to 10 µA or less.				
		rf ≢5k0	L: -0.3 to +0.7 V				
			H: 3.0 to 9.0 V There are no limitations on the magnitude				
		<del>,,,</del>	relationships between the VCC and VIN				
			supply voltages.				
8	OUT4		Output pins. Have built-in spark killer diodes. Braking				
10	OUT3		provides short braking that turns on the				
11	OUT1	Vcc	lower transistor.				
13	OUT2						
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		15k2 15k2					
		╶┤╅┊┊╪┾╴					
		10kg 10kg					
		227					
3	Vref	Vcc	Reference voltage (= 2.0 V).				
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		[↑					
		A ≠ 10k0¥					
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		·					
4	VC1		Input pins that determine the constantvoltage				
5	VC2	Vcc	regulated output level. The constant-voltage regulated output VO				
		┟ <del>┝╶┍</del> ┎ ┟	(= voltage between H side output and				
		U Cutput	GND) is controlled by VO = 2.5 x VC . There are no limitations on the magnitude				
			relationships between the VCC, VC1 and				
		Vc1.2	VC2 supply voltages.				
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#### **Sample Application Circuit**





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