# LB1838M

# Monolithic Digital IC Low-Voltage, Low-Saturation Bidirectional Motor Driver



The LB1838M is a low-saturation two-channel bidirectional motor driver IC for use in low-voltage applications. The LB1838M is a bipolar stepper-motor driver IC that is ideal for use in printers, cameras and other portable devices.

# **Functions**

- Low voltage operation (2.5V min)
- Low saturation voltage (upper transistor + lower transistor residual voltage: 0.40V at 400mA)
- Built-in through-current prevention circuit
- Separate logic power supply and motor power supply
- Built-in spark killer diodes
- Built-in thermal shutdown circuit
- Compact package: MFP14S

#### **Specifications** Absolute Maximum Ratings at $Ta = 25^{\circ}C$

Parameter	Symbol	Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max		-0.3 to +10.5	V
	V <sub>S</sub> max		-0.3 to +10.5	V
Output applied voltage	VOUT		V <sub>S</sub> +V <sub>SF</sub>	V
Input applied voltage	VIN		-0.3 to +10	V
Ground pin flow-out current	IGND	Per channel	1.0	А
Allowable power dissipation	Pd max	Independent IC	550	mW
		Mounted on a specified board *	800	mW
Operating temperature	Topr		-20 to +75	°C
Storage temperature	Tstg		-40 to +125	°C

\* Specified board: 20mm  $\times$  30mm  $\times$  1.6mm, glass epoxy board.

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



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#### Allowable Operating Ranges at $Ta = 25^{\circ}C$

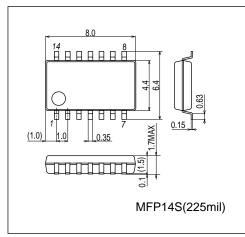
Parameter	Symbol	Conditions	Ratings	Unit
Supply voltage	V <sub>CC</sub>		2.5 to 9.0	VV
	V <sub>S</sub>		1.8 to 9.0	V
Input high-level voltage	VIH		1.8 to 9.0	V
Input Low-level voltage	VIL		-0.3 to +0.7	V

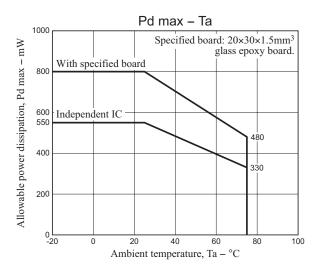
## **Electrical Characteristics** at $Ta = 25^{\circ}C$ , $V_{CC} = 3V$

Parameter	Sumbol	Conditions	Ratings			Unit
Parameter	Symbol Conditions		min	typ	max	Unit
Current drain	I <sub>CC</sub> 0 ENA1,2 = 0V, V <sub>IN</sub> 1 = 3V or 0V			0.1	10	μΑ
	I <sub>CC</sub> 1	ENA1 = 3V, V <sub>IN</sub> 1 = 3V or 0V		12	18	mA
Output saturation voltage	VOUT <sup>1</sup>	ENA = 3V, V <sub>IN</sub> = 3V or 0V, I <sub>OUT</sub> = 200mA		0.2	0.28	V
	V <sub>OUT</sub> 2	ENA = 3V, $V_{IN}$ = 3V or 0V, $I_{OUT}$ = 400mA		0.4	0.6	V
Input current	IIN	$V_{CC} = 6V, V_{IN} = 6V$			200	μΑ
	IENA	$V_{CC} = 6V, ENA = 6V$			200	μΑ
Output sustaining voltage	V <sub>O</sub> (SUS)	I <sub>OUT</sub> = 400mA	9			V
Spark killer diode						
Reverse current	I <sub>S</sub> (leak)	$V_{CC}1, V_S = 7V$			30	μΑ
Forward voltage	V <sub>SF</sub>	I <sub>OUT</sub> = 400mA			1.7	V

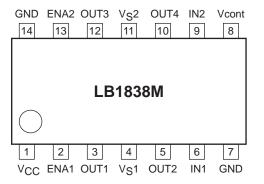
# **Package Dimensions**

unit : mm (typ) 3111A



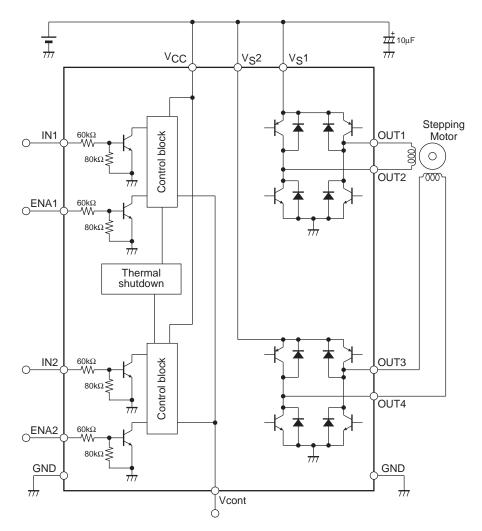


# **Pin Assignment**



Note: Both GND pins should be connected to ground.

### **Block Diagram**

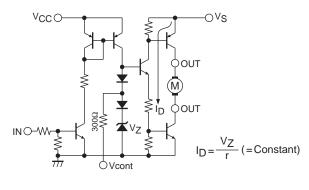


Note: As long as the voltages applied to V<sub>CC</sub>, V<sub>S</sub>1, V<sub>S</sub>2, ENA1, ENA2, IN1, and IN2 are within the limits set by the absolute maximum ratings, there are no restrictions on the relationship of each voltage level in comparison with the others (regarding which is higher or lower). (ex. V<sub>CC</sub> = 3V, V<sub>S</sub>1, 2 = 2V, ENA = IN = 5V)

#### **Truth Table**

IN1,2	ENA1,2	OUT1,3	OUT2,4	Mode
L	Н	Н	L	Forward
Н	н	L	Н	Reverse
L	L	OFF	OFF	Standby
н	L	OFF	OFF	Standby

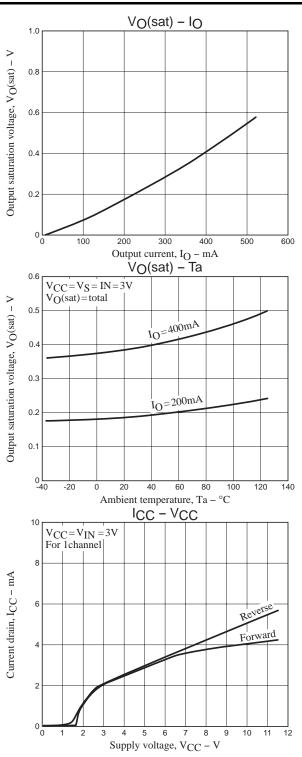
# Vcont pin

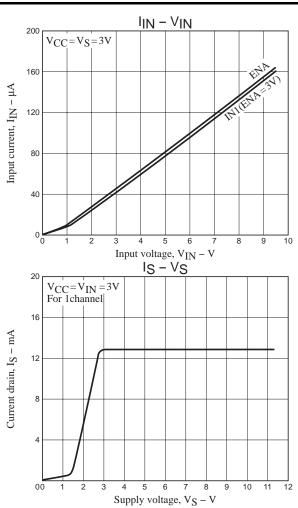


As shown in the left diagram, the Vcont pin outputs the voltage of the band gap Zener  $V_Z$  +V\_F (= 1.93V).

In normal use, this pin is left open.

The drive current  $I_D$  is varied by the Vcont voltage. However, because the band gap Zener is shared, it functions as a bridge.





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