LC898119XC

смоя LSI Optical Image Stabilization (OIS) Controller & Driver





WLCSP25, 2.0x2.0



LC898119XC is a system LSI (WLP type) integrating a digital signal processing function for Optical Image Stabilization (OIS) control and a saturation-driven H bridge driver function.



- Digital signal processing
- Built-in digital servo circuit
- Built-in Gyro filter
- AD converter
 - 12bit
 - Input 2ch
 - Equipped with a sample-hold circuit
- DA converter
 - 8bit
 - Output 2ch
- Built-in Serial I/F circuit (2-wire I²C-Bus)
- Built-in Hall Bias circuit
- Built-in Hall Amp (Gain of Op-amp : ×60, ×90, ×130, ×200)
- Built-in OSC (Oscillator)
- Typ. 36MHz
- Built-in LDO (Low Drop-Out regulator)
- Digital Gyro I/F for the companies (SPI Bus) (Please refer for the details)

- Motor Driver
- Saturation-drive H bridge ×2ch
- IO max : 220mA
- Package
- WLCSP25, 2.00mm × 2.00mm, thickness Max. 0.675mm, with B/C
- Pb-free / Halogen Free
- Power Supply Voltage
- AD/DA/VGA/LDO/OSC : AVDD30 = 2.6V to 3.6V
- Digital I/O : DVDD18 = 1.8V±10%
- Driver

• Core Logic

- : VM = 2.6V to 3.6V : Generation in LDO
 - DVDD12 = typ 1.2V output

* I²C Bus is a trademark of Philips Corporation.

ORDERING INFORMATION

See detailed ordering and shipping information on page 5 of this data sheet.

Block Diagram



Example of wiring diagram (Hall) in LC898119XC

Package Dimensions

unit : mm

WLCSP25, 2.0x2.0 CASE 567HK ISSUE O



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	MILLIMETERS		
DIM	MIN	MAX	
Α		0.675	
A1	0.15	0.25	
b	0.21	0.31	
D	2.00 BSC		
E	2.00 BSC		
е	0.40 BSC		

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*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

Pin Assignment



<typ> I : INPUT, O : OUTPUT, B : BIDIRECTION, P : Power, GND

Ball No	Pin Name	type	Description
A1	DVDD12	Р	LDO Power supply out (Logic Core VDD (typ 1.2V))
A2	IOP0	В	General-purpose IOPORT
A3	IOP1	В	General-purpose IOPORT
A4	I2CCK	I	I2C_IF clock
A5	OUT1	0	Driver Output
B1	AVDD30	Р	Analog Power(2.6V to 3.6V)
B2	OPINMY	1	Hall-Y OpAmp input-
B3	IOP2	В	General-purpose IOPORT / External Clock input (change at Register)
B4	I2CDT	В	I2C_IF Data
B5	OUT2	0	Driver Output
C1	AVSS	Р	Analog GND
C2	OPINMX	I	Hall-X OpAmp input-
C3	DVDD18	Р	IO Power (1.62V to 1.98V)
C4	VM	Р	Driver Power
C5	PGND	Р	Driver GND
D1	OPINPY	1	Hall-Y OpAmp input+
D2	HLXBO	0	Hall-X Bias (Current Drive)
D3	DVSS	Р	Logic GND
D4	DGSSB	В	Digital Gyro IF Chip Select (O)
D5	OUT3	0	Driver Output
E1	OPINPX	I	Hall-X OpAmp input+
E2	HLYBO	0	Hall-X Bias (Current Driver)
E3	DGSCLK	0	Digital Gyro IF clock
E4	DGDATA	В	Digital Gyro IF data
E5	OUT4	0	Driver Output

ORDERING INFORMATION

Device	Package	Shipping (Qty / Packing)
LC898119XC-MH	WLCSP25 2.0x2.0 (Pb-Free / Halogen Free)	4000 / Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D. http://www.onsemi.com/pub_link/Collateral/BRD8011-D.PDF

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