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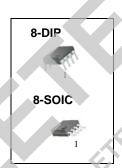
LM2903,LM393/LM393A,LM293A Dual Differential Comparator

Features

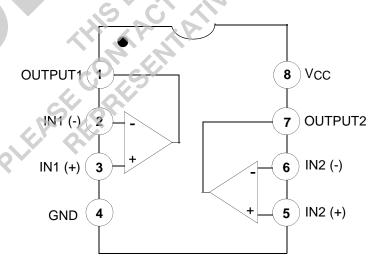
- Single Supply Operation: 2V to 36V
- Dual Supply Operation: ±1V to ±18V
- Allow Comparison of Voltages Near Ground Potential
- Low Current Drain 800µA Typ.
- Compatible with all Forms of Logic
- Low Input Bias Current 25nA Typ.
- Low Input Offset Current ±5nA Typ.
- Low Offset Voltage ±1mV Typ.

Description

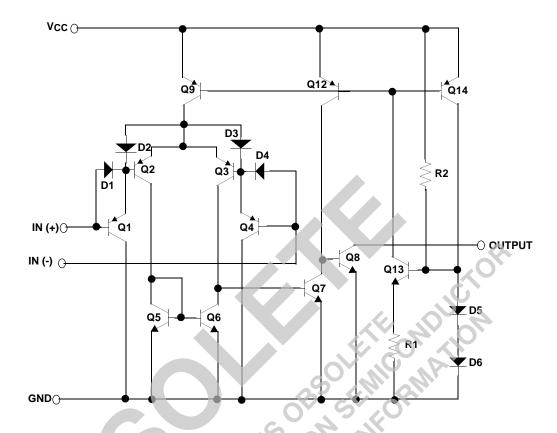
The LM2903, LM393/LM393A, LM293A consist of two independent voltage comparators designed to operate from a single power supply over a wide voltage range.



Internal Block Diagram



Schematic Diagram



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Power Supply Voltage	Vcc	±18 or 36	V
Differential Input Voltage	VI(DIFF)	36	V
Input Voltage	Vı	-0.3 to +36	V
Output Short Circuit to GND	-0	Continuous	-
Power Dissipation, T _a = 25°C 8-DIP 8-SOIC	PD	1040 480	mW
Operating Temperature LM393/LM393A LM2903 LM293A	TOPR	0 ~ +70 -40 ~ +105 -25 ~ +85	°C
Storage Temperature	TSTG	-65 ~ +150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-Ambient Max. 8-DIP 8-SOIC	R _{θja}	120 260	°C/W

Electrical Characteristics

(VCC = 5V, $TA = 25^{\circ}C$, unless otherwise specified)

Devemeter	Cumbal	Conditions		LM293A/LM393A			LM393			l lni4
Parameter	Symbol			Min.	Тур.	Max.	Min.	Тур.	Max.	- Unit
Input Offset Voltage		$V_{O(P)} = 1.4V, R_{S} = 0\Omega$		•	±1	±2	-	±1	±5	mV
		V _{CM} = 0 to 1.5V	-	-	±4.0	-	-	±9.0	IIIV	
Input Offset Current I _{IO}	lio			ı	±5	±50	-	±5	±50	nA
input Onset Ourient	Ü		Note1	ı	-	±150	-	1	±150	ш
Input Bias Current	IBIAS			•	65	250	-	65	250	nA
input Blue Gunerit	IDIAG		Note1	-	2	400	-	-	400	11/ (
Input Common Mode	VI(R)			0	-	VCC -1.5	0	ı	VCC -1.5	V
Voltage Range			Note1	0	-	Vcc-2	0	70%	Vcc-2	
Supply Current	Icc	$RL = \infty$, $VCC = 5$	5V	1	0.6	1	-	0.6	1	mA
Supply Current	ַ	$R_L = \infty$, $V_{CC} = 3$	-	0.8	2.5	;C	0.8	2.5		
Voltage Gain	Gv	VCC =15V, RL ≥ (for large VO(P-F		50	200	· - 3	50	200	-	V/mV
Large Signal Response Time	TLRES	V_I =TTL Logic Swing V_{REF} =1.4V, V_{RL} = 5V, V_{RL} = 5.1k V_{RL}		- 5	350	CO	A	350	-	nS
Response Time	TRES	V _{RL} =5V, R _L =5	.1kΩ	*	1.4	0-77	-	1.4	-	μS
Output Sink Current	ISINK	$V_{I(-)} \ge 1V, V_{I(+)} = V_{O(P)} \le 1.5V$	=0V,	6	18	-	6	18	-	mA
Output Saturation	VSAT	$VI(-) \ge 1 \lor$, $VI(+) = 0 \lor$			160	400	-	160	400	mV
Voltage	VSAI	ISINK = 4mA	Note1	- 2.0	-	700	-	-	700	IIIV
Output Leakage	lo(LKG)	$V_{i(-)} = 0V,$	V _{O(P)} = 5V	<u> </u>	0.1	-	-	0.1	-	nA
Current	IO(LKG)	V _{I(+)} = 1V	VO(P) = 30V	-	-	1.0	-	-	1.0	μΑ
Note1 _M393/LM393A: 0 ≤ T _A _M2903: -40 ≤ T _A ≤ +10 _M293A: -25 ≤ T _A ≤ +8	≤ +70°C 5°C 5°C	ASE CONT.	ENTA							

Electrical Characteristics (Continued)

(VCC = 5V, $TA = 25^{\circ}C$, unless otherwise specified)

Daramatar	Symbol	Symbol Conditions		LM2903			Unit
Parameter	Syllibol			Min.	Тур.	Max.	Ullit
Input Offset Voltage	Vio	$V_{O(P)} = 1.4V, R_S = 0\Omega$			±1	±7	mV
input Onset voltage		V _{CM} = 0 to 1.5V	Note1	-	±9	±15	1110
Input Offset Current	OI			- ±5 ±50		±50	nA
input Onset Ourient			Note1	-	±50	±200	11/1
Input Bias Current	IBIAS			-	65	250	nA
input blas current	IDIAG		Note1	-	-	500	117 \
Input Common Mode Voltage Range	VI(R)			0	-	VCC -1.5	V
Voltage Karige			Note1	0	- (Vcc-2	
Supply Current	Icc	$RL = \infty$, $VCC = 5V$			0.6	1	mA
очрыу очнени	100	$RL = \infty$, $VCC = 30$ \forall		-	1	2.5	ША
Voltage Gain	GV	V _{CC} =15V, R _L ≥15kΩ (for large V _O (P-P)swing)			100	-	V/mV
Large Signal Response Time	TLRES	V _I =TTL Logic Swing VREF =1.4V, VRL = 5V, RL = 5.1kΩ			350	-	nS
Response Time	TRES	$V_{RL} = 5V$, $R_L = 5.1k\Omega$		115.	1.5	-	μS
Output Sink Current	ISINK	$V_{I(-)} \ge 1V, V_{I(+)} = 0V$	V, V _{O(P)} ≤ 1.5V	6	16	-	mA
Output Saturation Voltage	VSAT	$V_{I(-)} \ge 1V, VI(+) = 0V$		-	160	400	mV
		ISINK = 4mA	Note1	-		700	111 V
Output Leakage Current	JO(LKG)	VI(-) = 0V,	V _O (P) = 5V	-	0.1	-	nA
Calput Loakago Carrotti	lO(LNG)	VI(+) = 1V	VO(P) = 30V	-	-	1.0	μΑ
Note1	SER	$V_{I(+)} = 0V,$ $V_{I(+)} = 1V$					

Typical Performance Characteristics

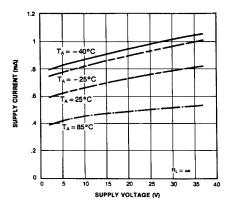


Figure 1. Supply Current vs Supply Voltage

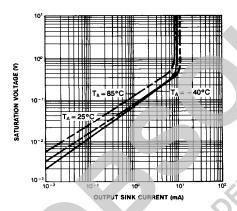


Figure 3. Output Saturation Voltage vs Sink Current

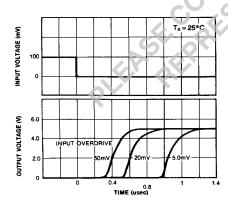


Figure 5. Response Time for Various Input Overdrive-Positive Transition

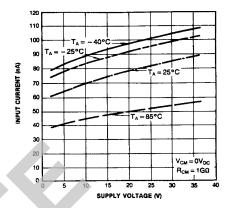


Figure 2. Input Current vs Supply Voltage

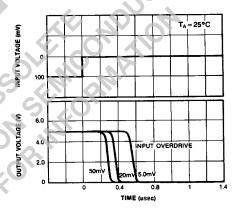


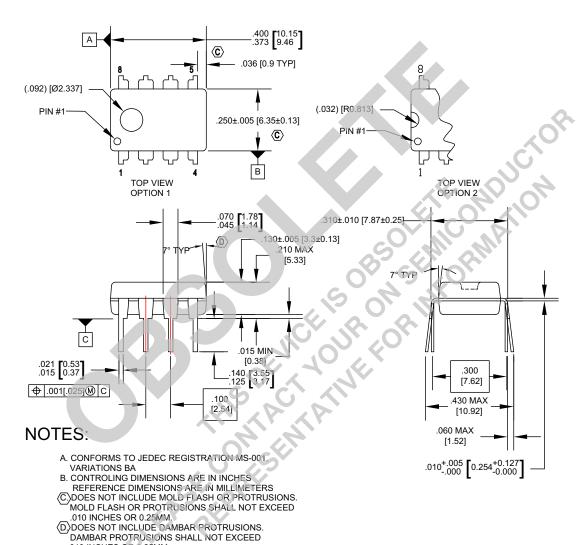
Figure 4. Response Time for Various Input Overdrive-Negative Transition

Mechanical Dimensions

Package

Dimensions in millimeters

8-DIP



.010 INCHES OR 0.25MM.

E. DIMENSIONING AND TOLERANCING

PER ASME Y14.5M-1994.

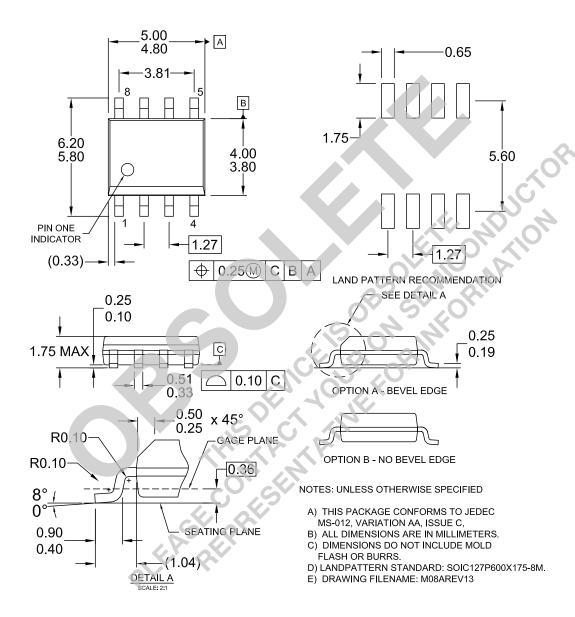
N08EREVG

Mechanical Dimensions (Continued)

Package

Dimensions in millimeters

8-SOIC



Ordering Information

Product Number	Operating Temperature	Package	Packing Method
LM393N		8-DIP	Rail
LM393AN		0-DIF	Rail
LM393M	0 .70°C		Rail
LM393MX	0 ~ +70°C	8-SOIC	Tape & Reel
LM393AM		0-3010	Rail
LM393AMX			Tape & Reel
LM2903N		8-DIP	Rail
LM2903M	-40 ~ +105°C	8-SOIC	Rail
LM2903MX		6-SOIC	Tape & Reel
LM293AN	-25 ~ +85°C	8-DIP	Rail



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