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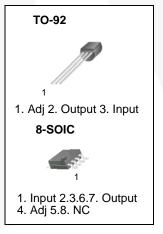
# LM317L 3-Terminal 0.1A Positive Adjustable Regulator

# Features

- Output Current in Excess of 100 mA
- Output Adjustable Between 1.2 V and 37 V
- Internal Thermal Overload Protection
- Internal Short Circuit Current Limiting
- Output Transistor Safe Area Compensation
- Floating Operation for High-Voltage Applications

# Description

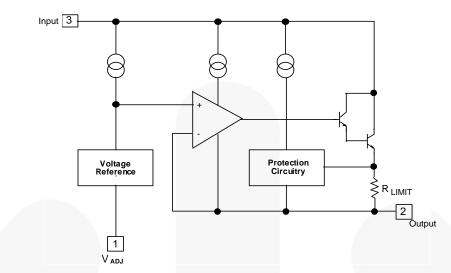
The LM317L is a 3-terminal, adjustable, positive-voltage regulator capable of supplying in excess of 100 mA over an output voltage range of 1.2 V to 37 V. This voltage regulator requires only two external resistors to set the output voltage.



# **Ordering Information**

Part Number	Operating Temperature Range	Top Mark	Package	Packing Method
LM317LM	0°C to +125°C	LM317LM	8-SOIC	Rail
LM317LMX	0°C to +125°C	LM317LM	8-SOIC	Tape and Reel
LM317LZ	0°C to +125°C	LM317LZ	TO-92	Bulk
LM317LZX	0°C to +125°C	LM317LZ	TO-92	Tape and Reel

## **Block Diagram**



### Figure 1. Block Diagram

## **Absolute Maximum Ratings**

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>I</sub> - V <sub>O</sub>	Input-Output Voltage Differential	40	V
PD	Power Dissipation	Internally limited	W
TJ	Operating Junction Temperature Range	0 ~ +125	°C
T <sub>STG</sub>	Storage Temperature Range	-65 ~ +125	°C

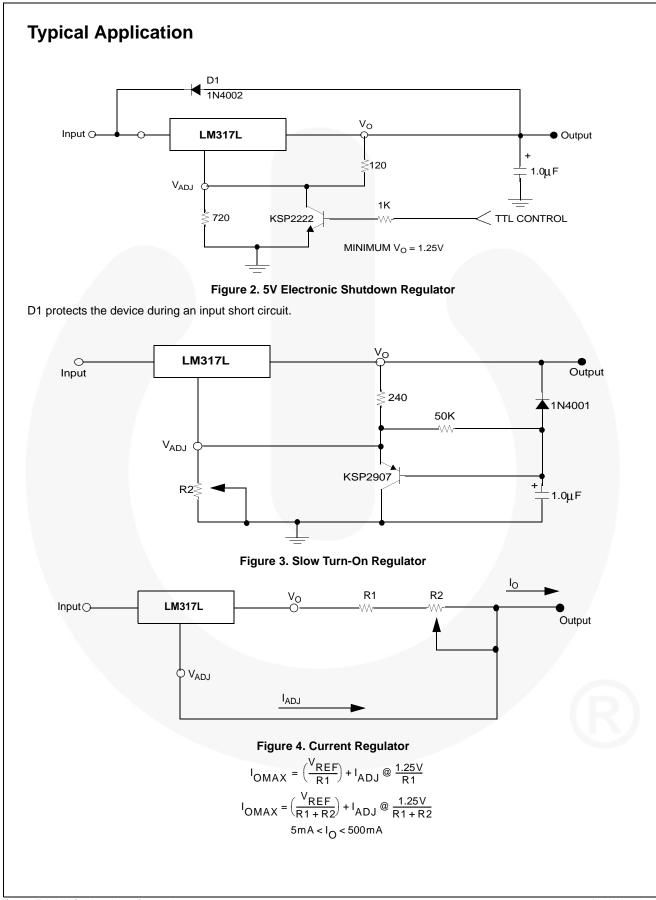
<b>Flectrical</b>	Characteristics
	Characteristics

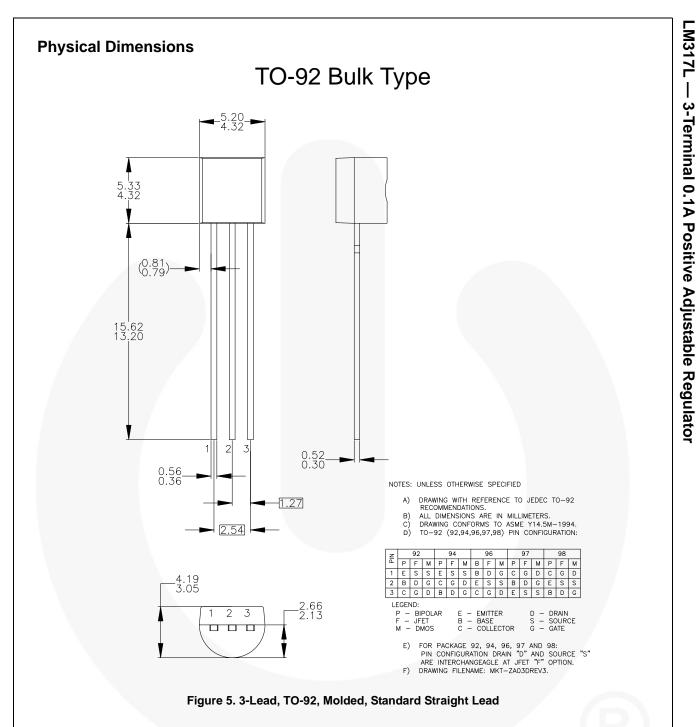
 $V_I - V_O = 5 \text{ V}, I_O = 40 \text{ mA}, 0^{\circ}\text{C} \le T_J \le +125^{\circ}\text{C}, P_{DMAX} = 625 \text{ mW}, \text{ unless otherwise specified}.$ 

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Р	Line Regulation <sup>(1)</sup>	$T_A = +25^{\circ}C, 3 V \le V_I - V_O \le 40 V$		0.01	0.04	% / V
R <sub>LINE</sub>		$3 \text{ V} \le \text{V}_{\text{I}} - \text{V}_{\text{O}} \le 40 \text{ V}$		0.02	0.07	% / V
R <sub>LOAD</sub> L	Load Regulation <sup>(1)</sup>	$\begin{array}{l} T_{A} = +25^{\circ}C, 10 \text{ mA} \leq I_{O} \leq 100 \text{ mA}, \\ V_{O} \leq 5 \text{ V} \end{array}$		5	25	mV
		$T_A = +25^{\circ}C, 10 \text{ mA} \le I_O \le 100 \text{ mA}, V_O \ge 5 \text{ V}$		0.1	0.5	% / V <sub>C</sub>
		$10 \text{ mA} \le I_O \le 100 \text{ mA}, V_O \le 5 \text{ V}$		20	70	mV
		10 mA $\leq$ I <sub>O</sub> $\leq$ 100 mA, V <sub>O</sub> $\geq$ 5 V		0.3	1.5	% / V <sub>0</sub>
I <sub>ADJ</sub>	Adjustment Pin Current			50	100	μΑ
ΔI <sub>ADJ</sub>	Adjustment Pin Current Change	$\begin{array}{l} 3 \ V \leq V_I - V_O \leq 40 \ V, \\ 10 \ mA \leq I_O \leq 100 \ mA, \\ P_D < P_{DMAX} \end{array}$		0.2	5	μΑ
V <sub>REF</sub>	Reference Voltage	$3 V < V_I - V_O < 40 V,$ 10 mA $\leq I_O \leq 100$ mA, P_D $\leq P_{DMAX}$	1.20	1.25	1.30	V
STT	Temperature Stability			0.7		%
I <sub>L(MIN)</sub>	Minimum Load Current to Maintain Regulation	V <sub>I</sub> - V <sub>O</sub> = 40 V		3.5	10	mA
I <sub>O(MAX)</sub>	Maximum Output Current	$V_{I} - V_{O} \le 15 \text{ V}, \text{ P}_{D} < \text{P}_{DMAX}$	100	200		mA
		$T_A = +25^{\circ}C,$ $V_I - V_O \le 40 \text{ V}, \text{ P}_D < \text{P}_{DMAX}$	25	50		mA
e <sub>N</sub>	RMS Noise, % of V <sub>OUT</sub>	$T_A = +25^{\circ}C$ , 10 Hz < f < 10 kHz		0.003		% / V <sub>0</sub>
RR	Ripple Rejection	$V_O = 10V$ , f = 120 Hz, without $C_{ADJ}$		65		dB
		$V_O = 10V$ , f = 120 Hz, $C_{ADJ} = 10 \ \mu F$	66	80		dB
ST	Long-Term Stability	T <sub>J</sub> = +125°C, 1000 Hours		0.3		%

Notes:

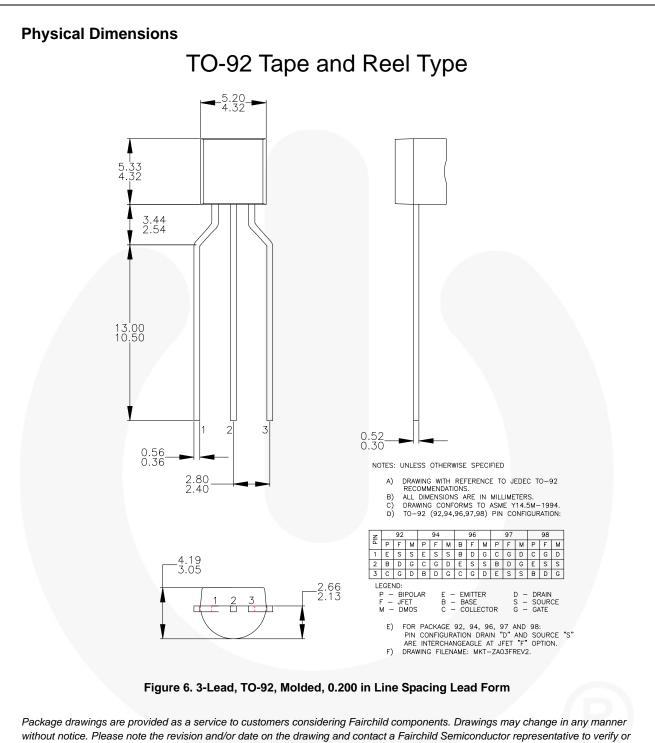
1. Load and Line regulation are specified at constant junction temperature. Change in V<sub>O</sub> due to heating effects must be taken into account separately. Pulse testing with low duty cycle is used.





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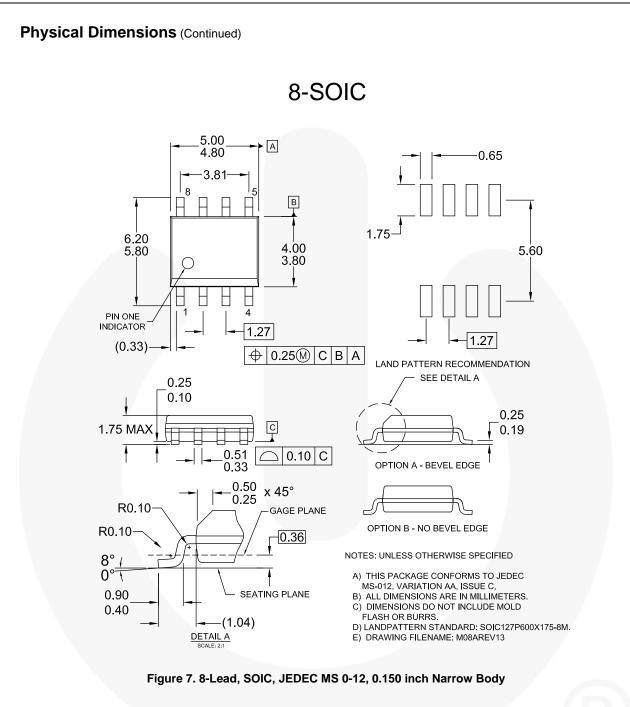


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