# **Precision Micro-Power Shunt Voltage References**

#### Description

LM4040 and LM4041 are precision two-terminal shunt mode voltage references offered in factory programmed reverse breakdown voltages of 1.225 V, 2.500 V, 3.000 V, 3.300 V, 4.096 V, and 5.000 V.

ON Semiconductor's Charge Programmable floating gate technology ensures precise voltage settings offering five grades of initial accuracy; from 0.1% to 2%.

LM4040 and LM4041 operate over a shunt current range of  $60~\mu A$  to 15 mA with low dynamic impedance, and 100 ppm/°C temperature coefficient ensuring stable reverse breakdown voltage accuracy over a wide range of operating conditions.

These shunt regulators do not require an external stabilizing capacitor but are stable with any capacitive load (up to  $1 \mu F$ ).

Offered in space saving SOT-23 and SC-70 packages LM4040 and LM4041 are specified for operation over the full industrial temperature range of  $-40^{\circ}$ C to  $+85^{\circ}$ C.

#### **Features**

- Reverse Breakdown Voltages:
  - ♦ 1.225 V
    - .5 V
  - ♦ 2.500 V

♦ 4.096 V

♦ 3.300 V

♦ 3.000 V

- ♦ 5.000 V
- Accuracy Grades:
  - ♦ A: ±0.1%

◆ D: ±1.0% ◆ E: ±2.0%

- ◆ B: ±0.2% ◆ C: ±0.5%
- Operating Current: 60 µA to 15 mA
- Low Output Noise: 35 μV

(10 Hz to 10 KHz)

- Small Package Size: SOT-23, SC-70
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Typical Applications**

- Mobile Handheld Devices
- Industrial Process Control
- Instrumentation
- Laptop and Desktop PCs
- Automotive
- Energy Management



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SOT-23 3 Lead TB SUFFIX CASE 527AG SC-70 5 Lead SD SUFFIX CASE 419AC

#### **MARKING DIAGRAMS**



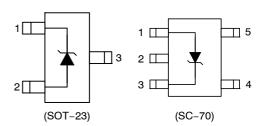


 $\begin{array}{ll} 4x & = Specific \ Device \ Code \\ (4L = LM4040, \ 4M = LM4041) \end{array}$ 

A = Assembly Location Code

Y = Production Year
M = Production Month
= Pb-Free Package

#### PIN CONNECTIONS



#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 11 of this data sheet.

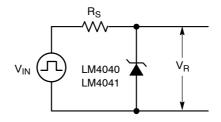


Figure 1. Test Circuit

#### **Table 1. PIN DESCRIPTIONS**

Pir	1		
SOT-23	SC-70	Name	Function
1	3	V+	Positive voltage
2	1	V-	Negative voltage
3	2	NC	This pin must be left floating or connected to V
	4	NIC	No Internal Connection. A voltage or signal applied to this pin will have no effect.
	5	NIC	]

#### **Table 2. ABSOLUTE MAXIMUM RATINGS**

	Parameter	Rating	Unit
Reverse Current		20	mA
Forward Current		10	mA
Junction Temperature		150	°C
Power Dissipation	SOT-23-3	300	mW
Power Dissipation	SC-70-5	240	mW

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### **Table 3. RECOMMENDED OPERATING CONDITIONS**

Parameter	Rating	Unit
I <sub>REVERSE</sub>	0.06 – 15	mA
Ambient Temperature Range	-40 to +85	°C

#### **Table 4. ESD SUSCEPTABILITY**

Symbol	Parameter	Min	Units
ESD	Human Body Model	2000	V
	Machine Model	200	V

#### Table 5. DC ELECTRICAL CHARACTERISTICS

(I  $_{R}$  = 100  $\mu A,\,T_{A}$  =  $-40^{\circ}C$  to +85  $^{\circ}C,$  unless otherwise noted. Typical values are at  $T_{A}$  = +25  $^{\circ}C.)$ 

					Limits			
Symbol	Parameter	Test (	Conditions	Min	Тур	Max	Units	
.225 V								
V <sub>R</sub>	Reverse Breakdown Voltage	T <sub>A</sub> = +25°C	LM4041A (0.1%)	1.2238	1.225	1.2262	٧	
			LM4041B (0.2%)	1.2226	1.225	1.2274	1	
			LM4041C (0.5%)	1.219	1.225	1.231	1	
			LM4041D (1.0%)	1.213	1.225	1.237	1	
			LM4041E (2.0%)	1.200	1.225	1.250	1	
V <sub>R</sub>	Reverse Breakdown Voltage	LM4041A	<u> </u>		±1.2	±9.2	mV	
	Tolerance	LM4041B			±2.4	±10.4		
		LM4041C			±6	±14	1	
		LM4041D			±12	±24	1	
		LM4041E			±25	±36	1	
I <sub>R_MIN</sub>	Minimum Operating Current				45	65	μΑ	
$\Delta V_R/\Delta T$	Reverse Breakdown Voltage	I <sub>R</sub> = 10 mA			±20		ppm/°C	
	Temperature Coefficient	I <sub>R</sub> = 1 mA	LM4041A, B, C		±15	±100	1	
			LM4041D, E		±15	±150	1	
		I <sub>R</sub> = 100 μA			±15		1	
$\Delta V_R/\Delta I_R$	Reverse Breakdown Voltage	I <sub>R MIN</sub> ≤ I <sub>R</sub> ≤	LM4041A, B, C		0.7	2.0	mV	
	Change with Operating Current	1 mA	LM4041D, E		0.7	2.5		
		1 mA ≤ I <sub>R</sub> ≤	LM4041A, B, C		2.5	8		
		15 mA	LM4041D, E		2.5	10	1	
Z <sub>R</sub>	Reverse Dynamic Impedance	I <sub>R</sub> = 1 mA,	LM4041A, B		0.5	1.5	Ω	
		f = 120 Hz,	f = 120 Hz, I <sub>AC</sub> = 0.1 I <sub>R</sub>	LM4041C		0.5	1.5	1
		IAC - 0.1 IR	LM4041D, E		0.5	2.0	1	
e <sub>N</sub>	Wideband Noise	I <sub>R</sub> = 100 μA, 10			200		μV <sub>RMS</sub>	
ΔV <sub>R</sub>	Reverse Breakdown Voltage Long Term Stability	T = 1000 h			120		ppm	
V <sub>HYST</sub>	Thermal Hysteresis (Note 2)	$\Delta T = -40^{\circ} C$ to	+125°C		0.08		%	
2.500 V				1		1		
V <sub>R</sub>	Reverse Breakdown Voltage	T <sub>A</sub> = +25°C	LM4040A (0.1%)	2.498	2.500	2.502	V	
			LM4040B (0.2%)	2.496	2.500	2.504	1	
			LM4040C (0.5%)	2.490	2.500	2.510	1	
			LM4040D (1.0%)	2.475	2.500	2.525	1	
			LM4040E (2.0%)	2.450	2.500	2.550	1	
V <sub>R</sub>	Reverse Breakdown Voltage	LM4040A	<u> </u>		±2	±19	mV	
- •	Tolerance	LM4040B			±4	±21	1	
		LM4040C			±10	±29	1	
		LM4040D			±25	±49	+	
		LM4040E				±74	-	

<sup>1.</sup> Guaranteed by design.

<sup>2.</sup> Thermal hysteresis is defined as the difference in voltage measured at +25°C after cycling to temperature -40°C and the 25°C measurement after cycling to temperature +125°C.

#### **Table 5. DC ELECTRICAL CHARACTERISTICS**

(IR = 100  $\mu$ A, TA = -40°C to +85°C, unless otherwise noted. Typical values are at TA = +25°C.)

					Limits			
Symbol	Parameter	Test 0	Test Conditions		Тур	Max	Units	
2.500 V				•	•			
I <sub>R_MIN</sub>	Minimum Operating Current				45	65	μΑ	
$\Delta V_R/\Delta T$	Reverse Breakdown Voltage	I <sub>R</sub> = 10 mA			±20		ppm/°C	
	Temperature Coefficient	I <sub>R</sub> = 1 mA	LM4040A, B, C		±15	±100		
			LM4040D, E		±15	±150		
		I <sub>R</sub> = 100 μA	•		±15			
$\Delta V_R/\Delta I_R$	Reverse Breakdown Voltage	$I_{R\_MIN} \le I_{R} \le$	LM4040A, B, C		0.3	1.0	mV	
	Change with Operating Current	1 mA	LM4040D, E		0.3	1.2		
		1 mA ≤ I <sub>R</sub> ≤	LM4040A, B, C		2.5	8		
		15 mA	LM4040D, E		2.5	10		
Z <sub>R</sub>	Reverse Dynamic Impedance	I <sub>R</sub> = 1 mA,	LM4040A, B		0.3	0.8	Ω	
		f = 120 Hz, I <sub>AC</sub> = 0.1 I <sub>R</sub>	LM4040C		0.3	0.9		
			LM4040D, E		0.3	1.1	1	
e <sub>N</sub>	Wideband Noise	I <sub>R</sub> = 100 μA, 10	Hz ≤ f ≤ 10 KHz		350		μV <sub>RMS</sub>	
$\Delta V_{R}$	Reverse Breakdown Voltage Long Term Stability	T = 1000 h	T = 1000 h		120		ppm	
V <sub>HYST</sub>	Thermal Hysteresis (Note 2)	$\Delta T = -40^{\circ} C$ to	+125°C		0.08		%	
.000 V		•					•	
V <sub>R</sub>	Reverse Breakdown Voltage	T <sub>A</sub> = +25°C	LM4040A (0.1%)	2.997	3.000	3.003	V	
			LM4040B (0.2%)	2.994	3.000	3.006		
			LM4040C (0.5%)	2.985	3.000	3.015		
			LM4040D (1.0%)	2.970	3.000	3.030		
			LM4040E (2.0%)	2.940	3.000	3.060	1	
V <sub>R</sub>	Reverse Breakdown Voltage	LM4040A	•		±3	±22	mV	
	Tolerance	LM4040B			±6	±26		
		LM4040C			±15	±34		
		LM4040D			±30	±59		
		LM4040E			±60	±89		
I <sub>R_MIN</sub>	Minimum Operating Current				45	65	μА	
$\Delta V_R/\Delta T$	Reverse Breakdown Voltage	I <sub>R</sub> = 10 mA			±20		ppm/°0	
	Temperature Coefficient	I <sub>R</sub> = 1 mA	LM4040A, B, C		±15	±100		
			LM4040D, E		±15	±150	1	
		I <sub>R</sub> = 100 uA	1		±15			
$\Delta V_R / \Delta I_R$	Reverse Breakdown Voltage	$I_{R\_MIN} \le I_{R} \le$	LM4040A, B, C		0.4	1.1	mV	
	Change with Operating Current	1 mA	LM4040D, E		0.4	1.3	1	
		1mA ≤ I <sub>R</sub> ≤	LM4040A, B, C		2.7	9		
		15 mA	LM4040D, E		2.7	11	1	

Guaranteed by design.
 Thermal hysteresis is defined as the difference in voltage measured at +25°C after cycling to temperature -40°C and the 25°C measurement after cycling to temperature +125°C.

**Table 5. DC ELECTRICAL CHARACTERISTICS** (I<sub>R</sub> = 100  $\mu$ A, T<sub>A</sub> = -40°C to +85°C, unless otherwise noted. Typical values are at T<sub>A</sub> = +25°C.)

			Test Conditions		Limits		
Symbol	Parameter	Test (			Тур	Max	Units
3.000 V							
Z <sub>R</sub>	Reverse Dynamic Impedance	I <sub>R</sub> = 1 mA,	LM4040A, B		0.4	0.9	Ω
		f = 120 Hz, I <sub>AC</sub> = 0.1 I <sub>R</sub>	LM4040C		0.4	0.9	
			LM4040D, E		0.4	1.2	
e <sub>N</sub>	Wideband Noise	I <sub>R</sub> = 100 μA, 10	) Hz ≤ f ≤ 10 KHz		350		$\mu V_{RMS}$
$\Delta V_{R}$	Reverse Breakdown Voltage Long Term Stability	T = 1000 h			120		ppm
V <sub>HYST</sub>	Thermal Hysteresis (Note 2)	$\Delta T = -40^{\circ} C$ to	+125°C		0.08		%
3.300 V							
V <sub>R</sub>	Reverse Breakdown Voltage	T <sub>A</sub> = +25°C	LM4040A (0.1%)	3.297	3.300	3.303	V
			LM4040B (0.2%)	3.294	3.300	3.306	
V <sub>R</sub>	Reverse Breakdown Voltage	T <sub>A</sub> = +25°C	LM4040C (0.5%)	3.285	3.300	3.315	V
			LM4040D (1.0%)	3.270	3.300	3.330	
V <sub>R</sub>	Reverse Breakdown Voltage	LM4040A			±3	±22	mV
	Tolerance	LM4040B		±6	±26		
		LM4040C		±15	±34	1	
		LM4040D			±30	±59	1
I <sub>R_MIN</sub>	Minimum Operating Current				45	65	μΑ
$\Delta V_R/\Delta T$	Reverse Breakdown Voltage	I <sub>R</sub> = 10 mA		±20		ppm/°C	
	Temperature Coefficient	I <sub>R</sub> = 1 mA	LM4040A, B, C		±15	±100	
			LM4040D		±15	±150	
		I <sub>R</sub> = 100 μA	1		±15		
$\Delta V_R / \Delta I_R$	Reverse Breakdown Voltage	I <sub>R_MIN</sub> ≤ I <sub>R</sub> ≤	LM4040A, B, C		0.3	1.0	mV
	Change with Operating Current	1 mA	LM4040D		0.3	1.2	
		1 mA ≤ I <sub>R</sub> ≤	LM4040A, B, C		2.5	8	
		15 mA	LM4040D		2.5	10	1
Z <sub>R</sub>	Reverse Dynamic Impedance	I <sub>R</sub> = 1 mA,	LM4040A, B		0.3	0.8	Ω
		f = 120 Hz, I <sub>AC</sub> = 0.1 I <sub>R</sub>	LM4040C		0.3	0.9	1
		7.0	LM4040D		0.3	1.1	1
e <sub>N</sub>	Wideband Noise	I <sub>R</sub> = 100 μA, 10	) Hz ≤ f ≤ 10 KHz		350		μV <sub>RMS</sub>
$\Delta V_{R}$	Reverse Breakdown Voltage Long Term Stability	T = 1000 h			120		ppm
V <sub>HYST</sub>	Thermal Hysteresis (Note 2)	$\Delta T = -40^{\circ}C$ to	+125°C		0.08		%
.096 V	•	•		•			
V <sub>R</sub>	Reverse Breakdown Voltage	T <sub>A</sub> = +25°C	LM4040A (0.1%)	4.092	4.096	4.100	V
			LM4040B (0.2%)	4.088	4.096	4.104	1
			LM4040C (0.5%)	4.080	4.096	4.120	1
			LM4040D (1.0%)	4.055	4.096	4.137	1

Guaranteed by design.
 Thermal hysteresis is defined as the difference in voltage measured at +25°C after cycling to temperature -40°C and the 25°C measurement after cycling to temperature +125°C.

#### **Table 5. DC ELECTRICAL CHARACTERISTICS**

(IR = 100  $\mu$ A, TA = -40°C to +85°C, unless otherwise noted. Typical values are at TA = +25°C.)

Symbol	Parameter	Test 0	Test Conditions		Тур	Max	Units
.096 V		•			•		
V <sub>R</sub>	Reverse Breakdown Voltage	LM4040A			±4	±31	mV
	Tolerance	LM4040B			±8	±35	
		LM4040C			±20	±47	
		LM4040D			±41	±80	
I <sub>R_MIN</sub>	Minimum Operating Current				45	65	μА
$\Delta V_R/\Delta T$	Reverse Breakdown Voltage	I <sub>R</sub> = 10 mA			±30		ppm/°C
	Temperature Coefficient	I <sub>R</sub> = 1 mA	LM4040A, B, C		±20	±100	1
			LM4040D		±20	±150	
		I <sub>R</sub> = 100 μA	•		±15		1
$\Delta V_R / \Delta I_R$	Reverse Breakdown Voltage	$I_{R\_MIN} \le I_{R} \le$	LM4040A, B, C		0.5	1.2	mV
	Change with Operating Current	1 mA	LM4040D		0.5	1.5	
		1 mA ≤ I <sub>R</sub> ≤	LM4040A, B, C		3.0	10	
		15 mA	LM4040D		3.0	13	
Z <sub>R</sub>	Reverse Dynamic Impedance	I <sub>R</sub> = 1 mA,	LM4040A, B		0.5	1.0	Ω
		f = 120 Hz, I <sub>AC</sub> = 0.1 I <sub>R</sub>	LM4040C		0.5	1.0	
			LM4040D		0.5	1.3	
e <sub>N</sub>	Wideband Noise	I <sub>R</sub> = 100 μA, 10	Hz ≤ f ≤ 10 KHz		800		μV <sub>RMS</sub>
$\Delta V_{R}$	Reverse Breakdown Voltage Long Term Stability	T = 1000 h			120		ppm
V <sub>HYST</sub>	Thermal Hysteresis (Note 2)	$\Delta T = -40^{\circ} C$ to	+125°C		0.08		%
.000 V		•					
V <sub>R</sub>	Reverse Breakdown Voltage	T <sub>A</sub> = +25°C	LM4040A (0.1%)	4.995	5.000	5.005	V
			LM4040B (0.2%)	4.990	5.000	5.010	
			LM4040C (0.5%)	4.975	5.000	5.025	
			LM4040D (1.0%)	4.950	5.000	5.050	1
V <sub>R</sub>	Reverse Breakdown Voltage	LM4040A	•		±5	±38	mV
	Tolerance	LM4040B			±10	±43	1
		LM4040C			±25	±58	
		LM4040D			±50	±99	
I <sub>R_MIN</sub>	Minimum Operating Current				45	65	μΑ
$\Delta V_R/\Delta T$	Reverse Breakdown Voltage	I <sub>R</sub> = 10 mA			±30		ppm/°C
	Temperature Coefficient	I <sub>R</sub> = 1 mA	LM4040A, B, C		±20	±100	
			LM4040D		±20	±150	
		I <sub>R</sub> = 100 μA			±15		1

<sup>1.</sup> Guaranteed by design.

<sup>2.</sup> Thermal hysteresis is defined as the difference in voltage measured at +25°C after cycling to temperature -40°C and the 25°C measurement after cycling to temperature +125°C.

#### Table 5. DC ELECTRICAL CHARACTERISTICS

(IR = 100  $\mu$ A, TA = -40°C to +85°C, unless otherwise noted. Typical values are at TA = +25°C.)

				Limits			
Symbol	Parameter	Test (	Test Conditions			Max	Units
5.000 V							
$\Delta V_R/\Delta I_R$	Reverse Breakdown Voltage	I <sub>R_MIN</sub> ≤ I <sub>R</sub> ≤	LM4040A, B, C		0.5	1.4	mV
	Change with Operating Current	1 mA	LM4040D		05	1.8	
		1 mA ≤ I <sub>R</sub> ≤	LM4040A, B, C		3.5	12	
		15 mA	LM4040D		3.5	15	
Z <sub>R</sub>	Reverse Dynamic Impedance	I <sub>R</sub> = 1 mA,	LM4040A, B		0.5	1.1	Ω
		f = 120 Hz, I <sub>AC</sub> = 0.1 I <sub>R</sub>	LM4040C		0.5	1.1	
			LM4040D		0.5	1.5	
e <sub>N</sub>	Wideband Noise	I <sub>R</sub> = 100 μA, 10	Hz ≤ f ≤ 10 KHz		800		$\mu V_{RMS}$
$\Delta V_{R}$	Reverse Breakdown Voltage Long Term Stability	T = 1000 h			120		ppm
V <sub>HYST</sub>	Thermal Hysteresis (Note 2)	$\Delta T = -40^{\circ} C$ to	+125°C		0.08		%

Guaranteed by design.
 Thermal hysteresis is defined as the difference in voltage measured at +25°C after cycling to temperature -40°C and the 25°C measurement after cycling to temperature +125°C.

#### TYPICAL PERFORMANCE CHARACTERISTICS

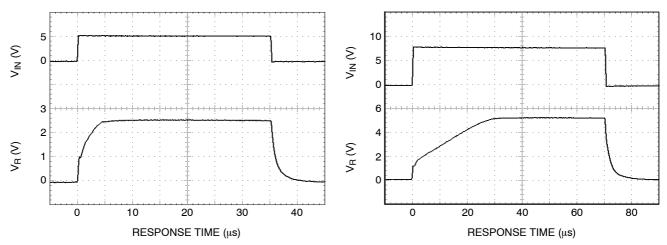


Figure 2. LM4040  $- 2.5 \text{ V } (R_S = 30 \text{ k})$ 

Figure 3. LM4040 – 5 V ( $R_S = 30 \text{ k}$ )

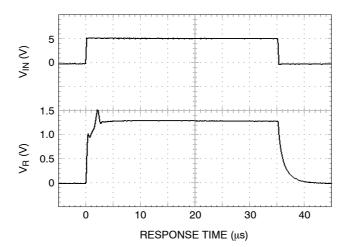


Figure 4. LM4041  $- 1.225 \text{ V } (R_S = 30 \text{ k})$ 

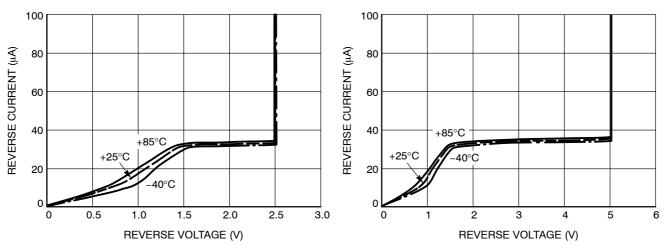


Figure 5. Reverse Characteristics (LM4040 – 2.5 V)

Figure 6. Reverse Characteristics (LM4040 – 5 V)

#### TYPICAL PERFORMANCE CHARACTERISTICS

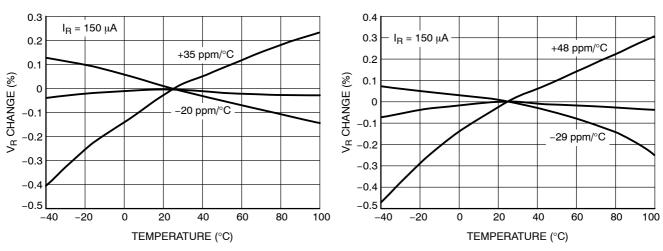


Figure 7. Temperature Drift - LM4040

Figure 8. Temperature Drift - LM4041

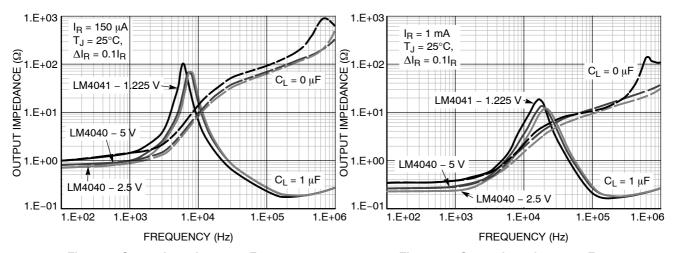


Figure 9. Output Impedance vs. Frequency

Figure 10. Output Impedance vs. Frequency

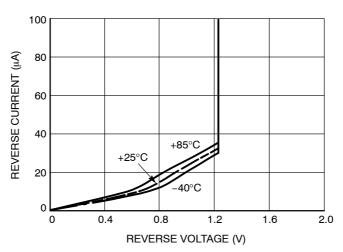


Figure 11. Reverse Characteristics - LM4041

#### **Device Description**

The LM404x shunt references use ON Semiconductor's floating gate (EEPROM) technology to produce a capacitor which stores an accurate and stable voltage that is used as the reference voltage for a control amplifier and shunt N-channel FET.

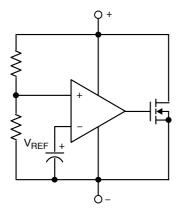


Figure 12. Functional Block Diagram

The device operates like a zener diode; maintaining a fixed voltage across its output terminals when biased with 60  $\mu$ A to 15 mA of reverse current. The LM404x will also act like a silicon diode when forward biased with currents up to 10 mA.

#### **Applications Information**

The LM404x's internal pass transistor maintains a constant output voltage by sinking the necessary amount of current across a source resistor. The source resistance (RS) is set by the load current range ( $I_{LOAD}$ ), supply voltage (VS) variations, LM404x's terminal voltage (VR), and desired quiescent current.

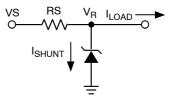


Figure 13. Typical Operating Circuit

To select a value of RS, set VS at its minimum value and  $I_{\rm LOAD}$  at its maximum. Be sure to maintain a minimum operating current of 60  $\mu A$  through LM404x at all times, as LM404x uses this current to power its internal circuitry. The RS value should be large enough to keep  $I_{\rm SHUNT}$  less than 15 mA for proper regulation when VS is maximum and  $I_{\rm LOAD}$  is at a minimum. Therefore, the value of RS is bounded by the following equation:

$$\frac{\left(V_{S(min)} - V_{R}\right)}{\left(60 \mu A + I_{LOAD(max)}\right)} > RS$$

and

$$RS > \frac{\left(V_{S(max)} - V_{R}\right)}{\left(15 \text{ mA} + I_{LOAD(min)}\right)}$$

Choosing a larger resistance minimizes the power dissipated in the circuit by reducing the shunt current.

#### **Output Capacitance**

The LM404x does not require an external capacitor for frequency stability and is stable for any output capacitance.

#### **Effect of Temperature**

LM404x has an output voltage temperature coefficient of typically  $\pm 15$  to  $\pm 30$  ppm/°C meaning the LM404x's output voltage will change by  $50-100~\mu\text{V/°C}$  for a 3.300 V regulator. The polarity of this temperature induced voltage shift can vary from device to device, some moving in the positive direction and others in the negative direction.

**Table 6. ORDERING INFORMATION** 

Part Number	Specific Device Marking	Voltage	Accuracy	Max Drift	Temperature Range	Package (Note 3)	
LM4041 <b>A</b> TB-122GT3*			±0.1%				
LM4041 <b>B</b> TB-122GT3	1		±0.2%	100 ppm/°C			
LM4041 <b>C</b> TB-122GT3	4M	1.225 V	±0.5%				
LM4041 <b>D</b> TB-122GT3			±1.0%,			1	
LM4041 <b>E</b> TB-122GT3	1		±2.0%,	150 ppm/°C			
LM4040 <b>A</b> TB-250GT3*			±0.1%				
LM4040 <b>B</b> TB-250GT3	1		±0.2%	100 ppm/°C			
LM4040 <b>C</b> TB-250GT3	1	2.500 V	±0.5%				
LM4040 <b>D</b> TB-250GT3	1		±1.0%,	/2.0			
LM4040 <b>E</b> TB-250GT3*	1		±2.0%,	150 ppm/°C			
LM4040 <b>A</b> TB-300GT3*			±0.1%		1		
LM4040 <b>B</b> TB-300GT3	1		±0.2%	100 ppm/°C			
LM4040 <b>C</b> TB-300GT3	1	3.000 V	±0.5%				
LM4040 <b>D</b> TB-300GT3	1		±1.0%,	150 /00			
LM4040 <b>E</b> TB-300GT3*			±2.0%,	150 ppm/°C	-40°C to 85°C	40°C to 05°C	SOT 00 0
LM4040 <b>A</b> TB-330GT3*			±0.1%			SOT-23-3	
LM4040 <b>B</b> TB-330GT3			±0.2%	100 ppm/°C			
LM4040 <b>C</b> TB-330GT3	4L	3.300 V	±0.5%	1			
LM4040 <b>D</b> TB-330GT3			±1.0%,	150	]		
LM4040 <b>E</b> TB-330GT3*			±2.0%,	150 ppm/°C			
LM4040 <b>A</b> TB-409GT3*			±0.1%				
LM4040 <b>B</b> TB-409GT3	1		±0.2%	100 ppm/°C			
LM4040 <b>C</b> TB-409GT3	1	4.096 V	±0.5%				
LM4040 <b>D</b> TB-409GT3	1		±1.0%,				
LM4040 <b>E</b> TB-409GT3*	1		±2.0%,	150 ppm/°C			
LM4040 <b>A</b> TB-500GT3	1		±0.1%				
LM4040 <b>B</b> TB-500GT3	1		±0.2%	100 ppm/°C			
LM4040 <b>C</b> TB-500GT3	1	5.000 V	±0.5%	1			
LM4040 <b>D</b> TB-500GT3	1		±1.0%,	150			
LM4040 <b>E</b> TB-500GT3*	1		±2.0%,	150 ppm/°C			

#### \*Consult Sales.

- Tape & Reel, 3,000 Units / Reel
   All packages are RoHS-compliant (Lead-free, Halogen-free).
   The standard lead finish is NiPdAu.
- 6. For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
- For detailed information and a breakdown of device nomenclature and numbering systems, please see the ON Semiconductor Device Nomenclature document, TND310/D, available at <a href="https://www.onsemi.com">www.onsemi.com</a>

**Table 6. ORDERING INFORMATION** 

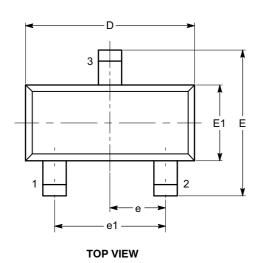
Part Number	Specific Device Marking	Voltage	Accuracy	Max Drift	Temperature Range	Package (Note 3)	
LM4041 <b>A</b> SD-122GT3*			±0.1%		ge	- , ,	
LM4041 <b>B</b> SD-122GT3	_		±0.2%	100 ppm/°C			
LM4041 <b>C</b> SD-122GT3	4M	1.225 V	±0.5%				
LM4041 <b>D</b> SD-122GT3	1		±1.0%,		-		
LM4041 <b>E</b> SD-122GT3*			±2.0%,	150 ppm/°C			
LM4040 <b>A</b> SD-250GT3*			±0.1%		-		
LM4040 <b>B</b> SD-250GT3	_		±0.2%	100 ppm/°C			
LM4040 <b>C</b> SD-250GT3	-	2.500 V	±0.5%				
LM4040 <b>D</b> SD-250GT3			±1.0%,		1		
LM4040 <b>E</b> SD-250GT3*			±2.0%,	150 ppm/°C			
LM4040 <b>A</b> SD-300GT3*			±0.1%			<b>-</b>	
LM4040 <b>B</b> SD-300GT3			±0.2%	100 ppm/°C			
LM4040 <b>C</b> SD-300GT3		3.000 V	±0.5%		– –40°C to 85°C		
LM4040 <b>D</b> SD-300GT3			±1.0%,	"-			
LM4040 <b>E</b> SD-300GT3*			±2.0%,	150 ppm/°C		00.70.5	
LM4040 <b>A</b> SD-330GT3*			±0.1%			SC-70-5	
LM4040 <b>B</b> SD-330GT3			±0.2%	100 ppm/°C			
LM4040 <b>C</b> SD-330GT3	4L	3.300 V	±0.5%				
LM4040 <b>D</b> SD-330GT3			±1.0%,	150 (00	-		
LM4040 <b>E</b> SD-330GT3*			±2.0%,	150 ppm/°C			
LM4040 <b>A</b> SD-409GT3*			±0.1%				
LM4040 <b>B</b> SD-409GT3			±0.2%	100 ppm/°C			
LM4040 <b>C</b> SD-409GT3		4.096 V	±0.5%	1			
LM4040 <b>D</b> SD-409GT3			±1.0%,	150			
LM4040 <b>E</b> SD-409GT3*			±2.0%,	150 ppm/°C			
LM4040 <b>A</b> SD-500GT3*			±0.1%		-		
LM4040 <b>B</b> SD-500GT3			±0.2%	100 ppm/°C	<u>-</u>		
LM4040 <b>C</b> SD-500GT3		5.000 V	±0.5%				
LM4040 <b>D</b> SD-500GT3			±1.0%,	150			
LM4040 <b>E</b> SD-500GT3*			±2.0%,	150 ppm/°C			

#### \*Consult Sales.

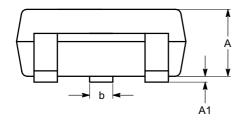
- Tape & Reel, 3,000 Units / Reel
   All packages are RoHS-compliant (Lead-free, Halogen-free).
- 5. The standard lead finish is NiPdAu.
- 6. For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
- 7. For detailed information and a breakdown of device nomenclature and numbering systems, please see the ON Semiconductor Device Nomenclature document, TND310/D, available at www.onsemi.com

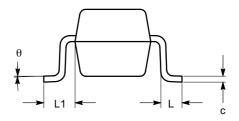
#### **PACKAGE DIMENSIONS**

SOT-23, 3 Lead CASE 527AG ISSUE O



SYMBOL	MIN	NOM	MAX
Α	0.89		1.12
A1	0.013		0.10
b	0.37		0.50
С	0.085		0.18
D	2.80		3.04
Е	2.10		2.64
E1	1.20		1.40
е		0.95 BSC	
e1		1.90 BSC	
L		0.40 REF	
L1		0.54 REF	
θ	0°		8°





**END VIEW** 

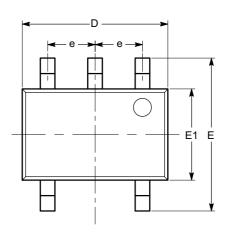
SIDE VIEW

#### Notes:

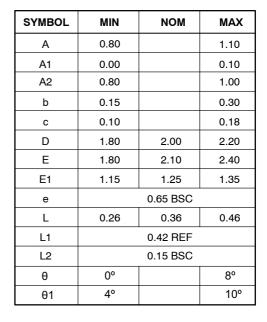
- (1) All dimensions are in millimeters. Angles in degrees.(2) Complies with JEDEC TO-236.

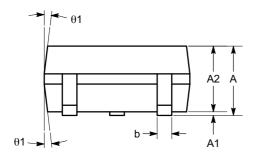
#### PACKAGE DIMENSIONS

#### SC-88A (SC-70 5 Lead), 1.25x2 CASE 419AC ISSUE A

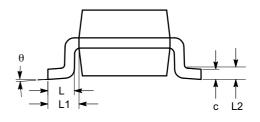


**TOP VIEW** 





SIDE VIEW



**END VIEW** 

#### Notes:

- (1) All dimensions are in millimeters. Angles in degrees.
- (2) Complies with JEDEC MO-203.

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LT1460KCS3-3.3#TRM LT66660KCDC-10#TRMPBF LTC6652BHLS8-5#PBF LTC6652AHLS8-4.096#PBF LTC6655BHLS8-4.096#PBF

LT6660HCDC-5#TRMPBF LM336Z-2.5#PBF LT1021BMH-10 SC431ILPRAG TLVH431MIL3T MAX6023EBT21+T AP432AQG-7

ADR4540CRZ LM4040B25QFTA TS3325AQPR REF102AU/2K5 TL4050B25QDBZR TL4051C12QDCKR TL431ACZ KA431SLMF2TF

KA431SMF2TF KA431SMFTF LM385BXZ/NOPB LM4040QCEM3-3.0/NOPB LM4041C12ILPR LM4050AEM3X-5.0/NOPB

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