Ordering number : ENA1133A

# **LV8075LP**

#### **Bi-CMOS LSI**

# **Constant-voltage Control 1-channel Forward/Reverse Motor Driver**



http://onsemi.com

#### **Overview**

LV8075LP is a constant voltage control 1-channel forward/reverse motor driver IC.

#### **Features**

- ullet Constant voltage control forward/reverse H-bridge Parallel input-Analog value must be entered for constant voltage reference input  $V(OUT) = V(VC) \times 2.0$
- Built-in thermal protection circuit and under-voltage detection protection circuit

#### **Specifications**

**Absolute Maximum Ratings** at Ta = 25°C, SGND = PGND = 0V

Parameter	Symbol	Conditions	Ratings	Unit
Maximum control power supply voltage	V <sub>CC</sub> max		6	V
Maximum load power supply voltage	VM max		6	V
Maximum control pin voltage	V <sub>C</sub> max		6	V
Maximum output current	I <sub>O</sub> max	OUT1, 2	0.5	Α
VREF maximum current	IREF max	VREF	1	mA
Allowable power dissipation	Pd max	Mounted on a circuit board*	700	mW
Operating temperature	Topr		-30 to +85	°C
Storage temperature	Tstg		-40 to +150	°C

 $<sup>^{\</sup>star}$  Specified circuit board : 40.0×50.0×0.8mm  $^{3}$  : glass epoxy four-layer board

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### Allowable Operating Range at Ta = 25°C, SGND = PGND = 0V

Parameter	Symbol	Conditions	Ratings	Unit
Control power-supply voltage	VCC		2.5 to 5.5	V
Load power-supply voltage	VM		2.5 to 5.5	V
Output control input voltage	Vcont	VC pin	0 to V <sub>CC</sub> -1	٧
Input pin "H" voltage	V <sub>IN</sub> H	IN1, 2,EN pin	$V_{CC} \times 0.6$ to $V_{CC}$ +0.3	V
Input pin "L" voltage	V <sub>IN</sub> L	IN1, 2,EN pin	-0.1 to V <sub>CC</sub> × 0.2	V

#### **Electrical Characteristics** at Ta = 25°C, $V_{CC} = VM = 3.0V$ , PGND = SGND = 0V, unless otherwise specified.

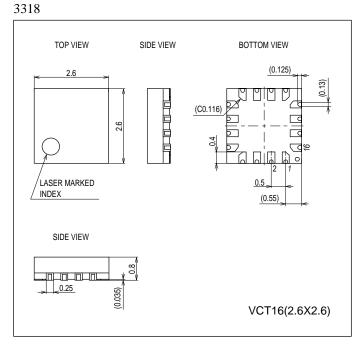
Parameter	Cumbal	O - m - diski - m -	Ratings			l lmit
Parameter	Symbol	Conditions	min	typ	max	Unit
Standby currfent consumption 1	Icco	EN, IN1, 2 = H/L/L or EN = L			1	μΑ
Standby current consumption 1	I <sub>MO</sub>	EN, IN1, 2 = H/L/L or EN = L			1	μΑ
Operating current consumption	V <sub>CC</sub> 1	EN = H, IN1 or IN2 = H		0.5	1.0	mA
H-level input current	I <sub>IN</sub> H	200kΩ pull-down, V <sub>IN</sub> = 3V	10	15	20	μΑ
L-level input current	I <sub>IN</sub> L	V <sub>IN</sub> = 0V		0	1	μΑ
Reference voltage output	VREF	IREF = 500μF	1.4	1.5	1.6	V
Output on-resistance	Ron1	Total of top and bottom		1.75	2.5	Ω
Constant-voltage control output voltage	VOUT	VC = 1.0V	1.94	2.0	2.06	V
Under-voltage detection operating voltage	V <sub>CS</sub>	V <sub>CC</sub> Voltage	2.1	2.2	2.35	V
Thermal protection temperature	TSD	Design guarantee value* 150		180	210	°C
Output rise time	Tr	(Note)		1.6	3.0	μs
Output fall time	Tf	(Note)		0.2	1.0	μs

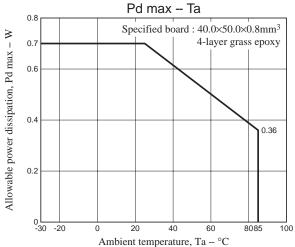
<sup>\*</sup> Design guarantee value and no measurement is made.

Note : Specify rising control start time  $\rightarrow$  90% of OUT output voltage, and falling control start time  $\rightarrow$  10% of OUT output voltage.

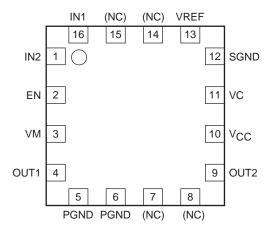
# **Package Dimensions**

unit: mm (typ)



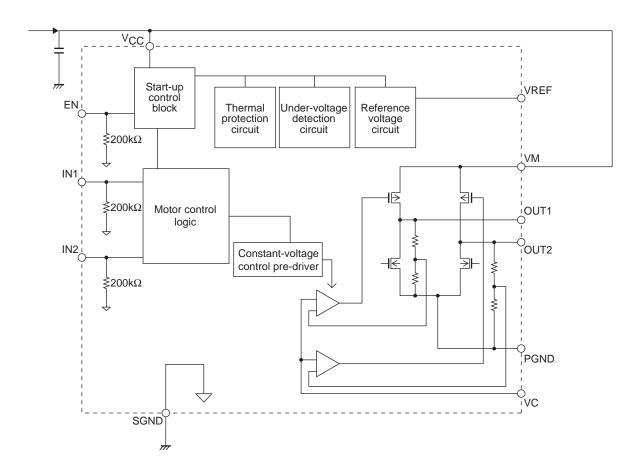


### **Pin Assignment**



Top view

# **Block Diagram**



**Truth Table** 

Constant voltage output H-bridge

EN	IN1	IN2	OUT1	OUT2	Mode
Н	Н	Н	L	L	Brake
	Н	L	Н	L	Forward evolution
	L	Н	L	Н	Reverse rotation
	L	L	off	off	Stand by
L	-	-	off	off	Stand by

<sup>&</sup>quot;-" entries indicate don't care state, "off" indicates output off state, insert  $20k\Omega$  impedance across PGND.

Constant voltage output value : V (OUT) = V (VC)×2.0

#### LV8075LP

#### **Pin Functions**

Pin No.	Pin name	Description
10	Vcc	Power supply pin for control
5, 6	PGND	Power ground pins for IC
12	SGND	IC system ground
3	VM	Power supply pin for constant voltage output H-bridge
2	EN	IC enable pin. Power-saving mode is established when L-level is applied. Pulled-down with 200kΩ
16, 1	IN1, 2	Input pins for manipulating constant-current output H-bridge (OUT1, 2). Pulled-down with 200kΩ
4, 9	OUT1, 2	Constant voltage H-bridge output pins
13	VREF	Reference voltage output, outputs 1.5V
11	VC	Analog voltage input pin for constant voltage setting.  Must be short-circuited to V <sub>CC</sub> pin when using saturation control.

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