General Purpose Transistor NPN Silicon

Features

- Moisture Sensitivity Level: 1
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	40	Vdc
Collector-Base Voltage	V _{CBO}	75	Vdc
Emitter-Base Voltage	V _{EBO}	6.0	Vdc
Collector Current – Continuous	Ι _C	600	mAdc
Electrostatic Discharge	ESD	HBM Clas MM Class	

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Package Dissipation (Note 1), T _A = 25°C	P _D	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	833	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

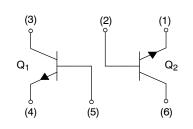
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.



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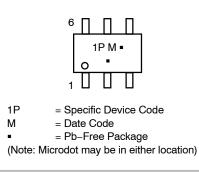
http://onsemi.com





SC-88/SC70-6/SOT-363 CASE 419B STYLE 1

MARKING DIAGRAM



ORDERING INFORMATION

Device	Package	Shipping [†]
MBT2222ADW1T1G	SOT-363 (Pb-Free)	3000 / Tape & Reel
NSVBT2222ADW1T1G	SOT–363 (Pb–Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_A = 25° C unless otherwise noted)

Charact	Symbol	Min	Мах	Unit	
OFF CHARACTERISTICS					
Collector-Emitter Breakdown Voltage	$(I_{\rm C} = 10 \text{ mAdc}, I_{\rm B} = 0)$	V _{(BR)CEO}	40	-	Vdc
Collector-Base Breakdown Voltage	$(I_{C} = 10 \ \mu Adc, \ I_{E} = 0)$	V _{(BR)CBO}	75	-	Vdc
Emitter-Base Breakdown Voltage,	$(I_E = 10 \ \mu Adc, \ I_C = 0)$	V _{(BR)EBO}	6.0	-	Vdc
Collector Cutoff Current	(V _{CE} = 60 Vdc, V _{EB(off)} = 3.0 Vdc)	ICEX	-	10	nAdc
Collector Cutoff Current	$(V_{CB} = 60 \text{ Vdc}, I_E = 0)$ $(V_{CB} = 60 \text{ Vdc}, I_E = 0, T_A = 125^{\circ}\text{C})$	I _{CBO}		0.01 10	μAdc
Emitter Cutoff Current	$(V_{EB} = 3.0 \text{ Vdc}, I_C = 0)$	I _{EBO}	-	100	nAdc
Base Cutoff Current	$(V_{CE} = 60 \text{ Vdc}, V_{EB(off)} = 3.0 \text{ Vdc})$	I _{BL}	-	20	nAdc
ON CHARACTERISTICS					
DC Current Gain		h _{FE}	05		-

Be carron clain		''FE			
	(I _C = 0.1 mAdc, V _{CE} = 10 Vdc)	_	35	-	
	(I _C = 1.0 mAdc, V _{CE} = 10 Vdc)		50	-	
	(I _C = 10 mAdc, V _{CE} = 10 Vdc)		75	-	
(I _C = 1	0 mAdc, V _{CE} = 10 Vdc, T _A = -55°C)		35	-	
	= 150 mAdc, V _{CE} = 10 Vdc) (Note 2)		100	300	
(I _C =	= 150 mAdc, V _{CE} = 1.0 Vdc) (Note 2)		50	-	
(I _C :	= 500 mAdc, V _{CE} = 10 Vdc) (Note 2)		40	-	
Collector-Emitter Saturation Voltage (Note 2)		V _{CE(sat)}			Vdc
5 ()	(I _C = 150 mAdc, I _B = 15 mAdc)	OE(Sul)	-	0.3	
	$(I_C = 500 \text{ mAdc}, I_B = 50 \text{ mAdc})$		-	1.0	
Read Emitter Seturation Voltage (Note 2)		M			Vdc
Base – Emitter Saturation Voltage (Note 2)	(I _C = 150 mAdc, I _B = 15 mAdc)	V _{BE(sat)}	0.6	1.2	vuc
	$(I_{\rm C} = 500 \text{ mAdc}, I_{\rm B} = 50 \text{ mAdc})$ $(I_{\rm C} = 500 \text{ mAdc}, I_{\rm B} = 50 \text{ mAdc})$		0.0	2.0	
	$(I_{\rm C} = 500 \text{mAdc}, I_{\rm B} = 50 \text{mAdc})$		_	2.0	

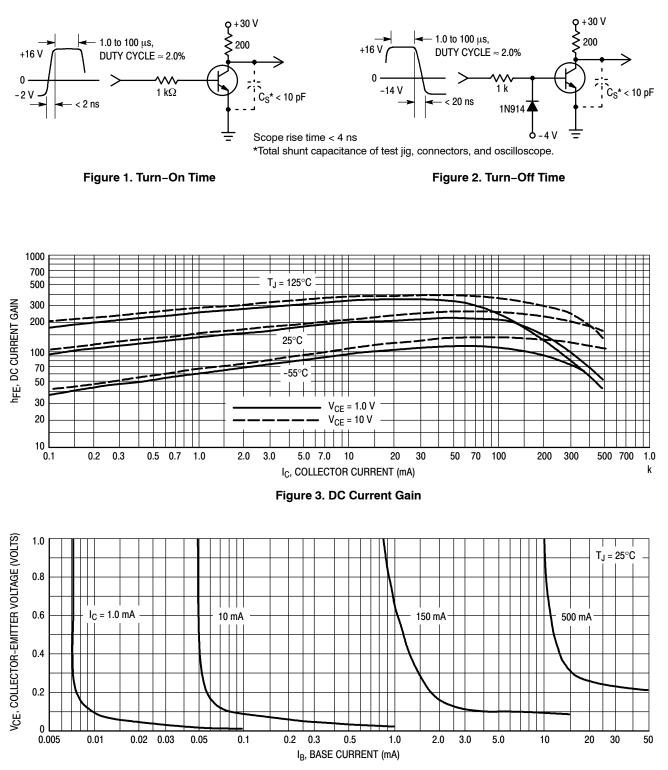
SMALL-SIGNAL CHARACTERISTICS

Current-Gain – Bandwidth Product (Note 3)		f _T	300	-	MHz
	(I _C = 20 mAdc, V _{CE} = 20 Vdc, f = 100 MHz)				
Output Capacitance	(V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)	C _{obo}	-	8.0	pF
Input Capacitance	$(V_{EB} = 0.5 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz})$	C _{ibo}	-	25	pF
Input Impedance	$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$ $(I_{C} = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{ie}	2.0 0.25	8.0 1.25	kΩ
Voltage Feedback Ratio	$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$ $(I_{C} = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{re}		8.0 4.0	X 10 ⁻⁴
Small-Signal Current Gain	$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$ $(I_{C} = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{fe}	50 75	300 375	_
Output Admittance	$(I_{C} = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$ $(I_{C} = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{oe}	5.0 25	35 200	μmhos
Collector Base Time Constant	$(I_E = 20 \text{ mAdc}, V_{CB} = 20 \text{ Vdc}, f = 31.8 \text{ MHz})$	rb, C _c	-	150	ps
Noise Figure	I_C = 100 μ Adc, V _{CE} = 10 Vdc, R _S = 1.0 kΩ, f = 1.0 kHz)	NF	-	4.0	dB
SWITCHING CHARACTERIS	rics		•		•

Delay Time	(V _{CC} = 30 Vdc, V _{BE(off)} = -0.5 Vdc,	t _d	-	10	20
Rise Time	$I_{\rm C} = 150 \text{ mAdc}, I_{\rm B1} = 15 \text{ mAdc})$	t _r	-	25	ns
Storage Time	(V _{CC} = 30 Vdc, I _C = 150 mAdc,	ts	-	225	
Fall Time	$I_{B1} = I_{B2} = 15 \text{ mAdc}$	t _f	-	60	ns

2. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%. 3. f_T is defined as the frequency at which |h_{fe}| extrapolates to unity.

SWITCHING TIME EQUIVALENT TEST CIRCUITS





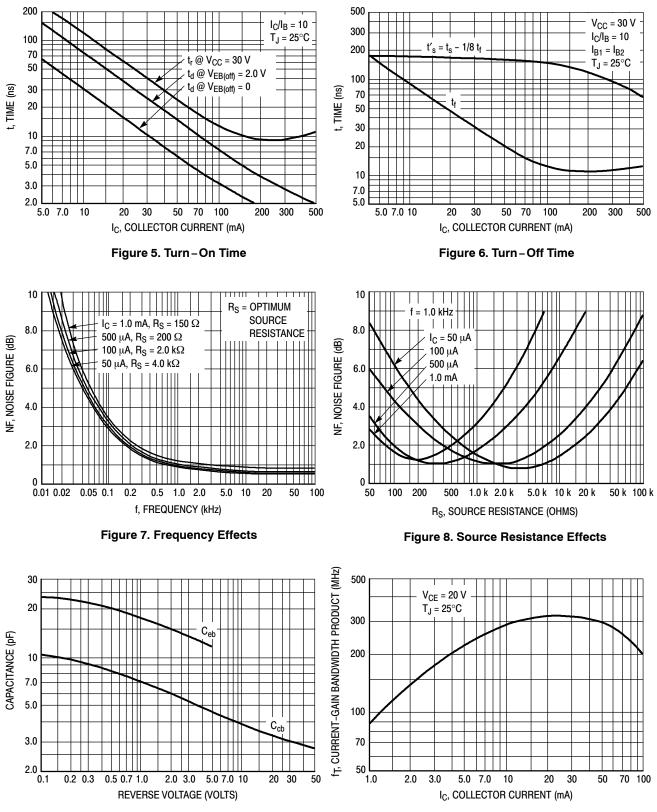


Figure 9. Capacitances

Figure 10. Current–Gain Bandwidth Product

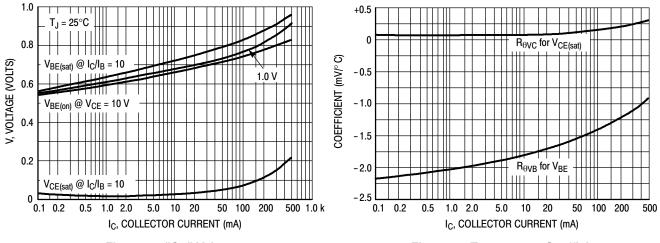


Figure 11. "On" Voltages

Figure 12. Temperature Coefficients

0.043

0.004





- XXX = Specific Device Code

(Note: Microdot may be in either location)

*Date Code orientation and/or position may vary depending upon manufacturing location.

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering

details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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SC-88/SC70-6/SOT-363 CASE 419B-02 ISSUE Y

DATE 11 DEC 2012

STYLE 1: PIN 1. EMITTER 2 2. BASE 2 3. COLLECTOR 1 4. EMITTER 1 5. BASE 1 6. COLLECTOR 2	STYLE 2: CANCELLED	STYLE 3: CANCELLED	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. COLLECTOR 4. EMITTER 5. BASE 6. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. COLLECTOR 4. EMITTER 5. BASE 6. CATHODE	STYLE 6: PIN 1. ANODE 2 2. N/C 3. CATHODE 1 4. ANODE 1 5. N/C 6. CATHODE 2
STYLE 7: PIN 1. SOURCE 2 2. DRAIN 2 3. GATE 1 4. SOURCE 1 5. DRAIN 1 6. GATE 2	STYLE 8: CANCELLED	STYLE 9: PIN 1. EMITTER 2 2. EMITTER 1 3. COLLECTOR 1 4. BASE 1 5. BASE 2 6. COLLECTOR 2	STYLE 10: PIN 1. SOURCE 2 2. SOURCE 1 3. GATE 1 4. DRAIN 1 5. DRAIN 2 6. GATE 2	STYLE 11: PIN 1. CATHODE 2 2. CATHODE 2 3. ANODE 1 4. CATHODE 1 5. CATHODE 1 6. ANODE 2	STYLE 12: PIN 1. ANODE 2 2. ANODE 2 3. CATHODE 1 4. ANODE 1 5. ANODE 1 6. CATHODE 2
STYLE 13:	STYLE 14:	STYLE 15:	STYLE 16:	STYLE 17:	STYLE 18:
PIN 1. ANODE	PIN 1. VREF	PIN 1. ANODE 1	PIN 1. BASE 1	PIN 1. BASE 1	PIN 1. VIN1
2. N/C	2. GND	2. ANODE 2	2. EMITTER 2	2. EMITTER 1	2. VCC
3. COLLECTOR	3. GND	3. ANODE 3	3. COLLECTOR 2	3. COLLECTOR 2	3. VOUT2
4. EMITTER	4. IOUT	4. CATHODE 3	4. BASE 2	4. BASE 2	4. VIN2
5. BASE	5. VEN	5. CATHODE 2	5. EMITTER 1	5. EMITTER 2	5. GND
6. CATHODE	6. VCC	6. CATHODE 1	6. COLLECTOR 1	6. COLLECTOR 1	6. VOUT1
STYLE 19:	STYLE 20:	STYLE 21:	STYLE 22:	STYLE 23:	STYLE 24:
PIN 1. I OUT	PIN 1. COLLECTOR	PIN 1. ANODE 1	PIN 1. D1 (i)	PIN 1. Vn	PIN 1. CATHODE
2. GND	2. COLLECTOR	2. N/C	2. GND	2. CH1	2. ANODE
3. GND	3. BASE	3. ANODE 2	3. D2 (i)	3. Vp	3. CATHODE
4. V CC	4. EMITTER	4. CATHODE 2	4. D2 (c)	4. N/C	4. CATHODE
5. V EN	5. COLLECTOR	5. N/C	5. VBUS	5. CH2	5. CATHODE
6. V REF	6. COLLECTOR	6. CATHODE 1	6. D1 (c)	6. N/C	6. CATHODE
STYLE 25:	STYLE 26:	STYLE 27:	STYLE 28:	STYLE 29:	STYLE 30:
PIN 1. BASE 1	PIN 1. SOURCE 1	PIN 1. BASE 2	PIN 1. DRAIN	PIN 1. ANODE	PIN 1. SOURCE 1
2. CATHODE	2. GATE 1	2. BASE 1	2. DRAIN	2. ANODE	2. DRAIN 2
3. COLLECTOR 2	3. DRAIN 2	3. COLLECTOR 1	3. GATE	3. COLLECTOR	3. DRAIN 2
4. BASE 2	4. SOURCE 2	4. EMITTER 1	4. SOURCE	4. EMITTER	4. SOURCE 2
5. EMITTER	5. GATE 2	5. EMITTER 2	5. DRAIN	5. BASE/ANODE	5. GATE 1
6. COLLECTOR 1	6. DRAIN 1	6. COLLECTOR 2	6. DRAIN	6. CATHODE	6. DRAIN 1

Note: Please refer to datasheet for style callout. If style type is not called out in the datasheet refer to the device datasheet pinout or pin assignment.

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