### Thyristor Datasheet

PO



## **Additional Information**







Samples

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Accessories

### **Functional Diagram**



## Description

Designed for overvoltage protection in crowbar circuits.

### **Features**

- Glass-Passivated Junctions for Greater Parameter Stability and Reliability
- Center-Gate Geometry for Uniform Current Spreading Enabling High Discharge Current
- Small Rugged, Thermowatt Package Constructed for Low Thermal Resistance and Maximum Power Dissipation and Durability
- High Capacitor Discharge Current, 300 Amps
- Pb-Free Package is Available



**Pin Out** 

<b>Maximum Ratings</b> $(I_{J} = 25  {}^{\circ}\text{C} \text{ unless otherwise noted})$						
Rating		Symbol	Value	Unit		
Peak Repetitive Off-State Voltage (Note 1) (- 40 to 125°C, Gate Open)	MCR68-2	V <sub>drm</sub> , V <sub>rrm</sub>	50	V		
On-State RMS Current (180° Conduction Angles; $T_c = 85^{\circ}C$ )		I <sub>T (RMS)</sub>	12	А		
Peak Discharge Current (Note 2)		I <sub>TM</sub>	300	А		
Average On-State Current (180° Conduction Angles; T <sub>c</sub> = 85°C)	I <sub>T(AV)</sub>	8.0	А			
Peak Non-Repetitive Surge Current (1/2 Cycle, Sine Wave 60 Hz, $T_{J}$ = 125°C)	I <sub>tsm</sub>	100	А			
Circuit Fusing Consideration (t = $8.3 \text{ ms}$ )	l²t	40	A <sup>2</sup> sec			
Forward Peak Gate Current (Pulse Width $\leq$ 1.0 $\mu sec,T_{c}\text{=}$ 80°C)	I <sub>GM</sub>	2.0	А			
Forward Peak Gate Power (Pulse Width $\leq$ 1.0 $\mu sec, T_{c} = 85^{\circ}C)$	P <sub>GM</sub>	20	W			
Forward Average Gate Power (t = 8.3 ms, TC = $85^{\circ}$ C)	P <sub>G(AV)</sub>	0.5	W			
Operating Junction Temperature Range	TJ	-40 to +125	°C			
Storage Temperature Range	T <sub>stg</sub>	-40 to +150	°C			
MountingTorque	_	8.0	in. lb.			

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits

Naminal ranges are interviewed values beyond which device damage can become values interviewed are interviewed and which the values interviewed are interviewed and the values of the values interviewed are interviewed and the values interviewed are exceeded.
 V<sub>prote</sub> and V<sub>stant</sub> for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.
 Ratings apply for tw = 1 ms. See Figure 1 for I<sub>m</sub> capability for various duration of an exponentially decaying current waveform, tw is defined as 5 time constants of an exponentially decaying current pulse.

### **Thermal Characteristics**

Rating		Symbol	Value	Unit
Thermal Resistance,	Junction-to-Case (AC) Junction-to-Ambient	R <sub>ejc</sub> R <sub>eja</sub>	2.0 60	°C/W
Maximum Lead Temperature for Soldering Pu 10 seconds	TL	260	°C	

### **Electrical Characteristics - OFF** ( $T_{,i} = 25 \text{ °C}$ unless otherwise noted)

Characteristic		Symbol	Min	Тур	Мах	Unit
Peak Repetitive Forward or Reverse Blocking Current	T, = 25 °C	I <sub>DBM</sub> ,	-	-	0.01	<u>م</u>
$(V_{D} = V_{DRM} = V_{RRM}; \text{ Gate Open})$	T_ = 125 °C	I	-	-	2.0	ШA

### **Electrical Characteristics - ON** ( $T_{J} = 25 \text{ °C}$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Мах	Unit
Peak Forward On-State Voltage $(I_{TM} = 24 \text{ A})$ (Note 3) $(I_{TM} = 300 \text{ A}, tw = 1 \text{ ms})$ (Note 4)	$V_{\rm TM}$	- -	- 6.0	2.2	V
Gate Trigger Current (Continuous dc) ( $V_D = 12 V$ ; $R_L = 100 \Omega$ )	I <sub>gt</sub>	2.0	7.0	30	mA
Gate Trigger Voltage (Continuous dc) (V $_{\rm D}$ = 12 V; R $_{\rm L}$ = 100 $\Omega$ )	V <sub>gt</sub>	-	0.65	1.5	V
Gate Non-Trigger Voltage (V <sub>D</sub> =12 V <sub>dc'</sub> R <sub>I</sub> =100 Ω, T <sub>J</sub> =125 °C)		0.2	0.40	-	V
Holding Current ( $V_{D} = 12 V$ , Initiating Current = 200 mA, Gate Open))		3.0	15	50	mA
Latch Current ( $V_D = 12 \text{ V}, I_G = 150 \text{ mA}$ )	I <sub>L</sub>	-	-	60	mA
Gate Controlled Turn-On Time (Note 5) ( $V_{D}$ = Rated $V_{DRM}$ , $I_{G}$ = 150 mA) ( $I_{TM}$ = 24 A Peak)		-	1.0	-	μs

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### **Dynamic Characteristics**

Characteristic	Symbol	Min	Тур	Мах	Unit
Critical Rate of Rise of Off–State Voltage ( $V_D = Rated V_{DRM}$ , Exponential Waveform, Gate Open, $T_J = 125^{\circ}C$ )	dv/dt	10	-	-	V/µs
Critical Rate of Rise of On–State Current $I_{g} = 150 \text{ A} \qquad T_{J} = 125^{\circ}\text{C}$	di/dt	-	-	75	A/µs

Pulse duration ≤ 300 μs, duty cycle ≤ 2%.
 Ratings apply for tw = 1 ms. See Figure 1 for I<sub>IM</sub> capability for various durations of an exponentially decaying current waveform. tw is defined as 5 time constants of an exponentially decaying current pulse.
 The gate controlled turn-on time in a crowbar circuit will be influenced by the circuit inductance.

### **Voltage Current Characteristic of SCR**

Symbol	Parameter
V <sub>drm</sub>	Peak Repetitive Forward Off State Voltage
I <sub>DRM</sub>	Peak Forward Blocking Current
V <sub>RRM</sub>	Peak Repetitive Reverse Off State Voltage
I <sub>RRM</sub>	Peak Reverse Blocking Current
V <sub>TM</sub>	Maximum On State Voltage
I <sub>H</sub>	Holding Current





### Figure 3. **Current Derating**





Peak Capacitor Discharge Current Derating



### Figure 4. Maximum Power Dissipation





**Figure 5.** Thermal Response

**Figure 6.** Gate Trigger Current



Figure 8. Holding Current



**Figure 7.** Gate Trigger Voltage



### Thyristor Datasheet

# MCR68–2 Silicon Controlled Rectifiers

### Dimensions

s

R



### Part Marking System



	Inches		Millimeters		
Dim	Min	Max	Min	Max	
А	0.590	0.620	14.99	15.75	
В	0.380	0.420	9.65	10.67	
С	0.178	0.188	4.52	4.78	
D	0.025	0.035	0.64	0.89	
F	0.142	0.147	3.61	3.73	
G	0.095	0.105	2.41	2.67	
Н	0.110	0.130	2.79	3.30	
J	0.018	0.024	0.46	0.61	
К	0.540	0.575	13.72	14.61	
L	0.060	0.075	1.52	1.91	
Ν	0.195	0.205	4.95	5.21	
Q	0.105	0.115	2.67	2.92	
R	0.085	0.095	2.16	2.41	
S	0.045	0.060	1.14	1.52	
Т	0.235	0.255	5.97	6.47	
U	0.000	0.050	0.00	1.27	
V	0.045		1.15		
Z		0.080		2.04	

Pin Assignment			
1	Cathode		
2	Anode		
3	Gate		
4	Anode		

### **Ordering Information**

Device	Package	Shipping
MCR68-2	TO-220AB	1000 Upita / Pay
MCR68–2G	(Pb-Free)	1000 Units / Box

1. Dimensioning and tolerancing per ansi y14.5m, 1982.

2. Controlling dimension: inch.

3. Dimension z defines a zone where all body and lead irregularities are allowed.

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