Complementary Silicon Plastic Power Transistors

These devices are designed for use as high-frequency drivers in audio amplifiers.

Features

- High Current Gain Bandwidth Product
- TO-220 Compact Package
- These Devices are Pb-Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage MJE15028G, MJE15029G MJE15030G, MJE15031G	V _{CEO}	120 150	Vdc
Collector-Base Voltage MJE15028G, MJE15029G MJE15030G, MJE15031G	V _{CB}	120 150	Vdc
Emitter-Base Voltage	V _{EB}	5.0	Vdc
Collector Current – Continuous	Ι _C	8.0	Adc
Collector Current – Peak	I _{CM}	16	Adc
Base Current	I _B	2.0	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	50 0.40	W W/°C
Total Device Dissipation @ $T_A = 25^{\circ}C$ Derate above 25°C	P _D	2.0 0.016	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

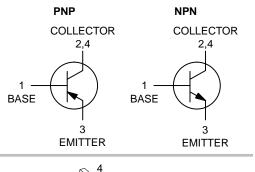
Characteristics	Symbol	Max	Unit	
Thermal Resistance, Junction-to-Case	R_{\thetaJC}	2.5	°C/W	
Thermal Resistance, Junction-to-Ambient	R_{\thetaJA}	62.5	°C/W	



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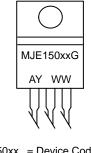
http://onsemi.com

8 AMPERE POWER TRANSISTORS COMPLEMENTARY SILICON 120–150 VOLTS, 50 WATTS





MARKING DIAGRAM



MJE150xx = Device Code x = 28, 29, 30, or 31

= Assembly Location

= Year

A Y

WW

= Work Week

G = Pb–Free Package

ORDERING INFORMATION

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

See detailed ordering and shipping information in the package dimensions section on page 5 of this data sheet.

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS			•	•
Collector–Emitter Sustaining Voltage (Note 1) ($I_C = 10 \text{ mAdc}, I_B = 0$) MJE15028, MJE15029 MJE15030, MJE15031	V _{CEO(sus)}	120 150		Vdc mAdc μAdc μAdc
Collector Cutoff Current $(V_{CE} = 120 \text{ Vdc}, I_B = 0)$ MJE15028, MJE15029 $(V_{CE} = 150 \text{ Vdc}, I_B = 0)$ MJE15030, MJE15031	ICEO	-	0.1	
Collector Cutoff Current $(V_{CB} = 120 \text{ Vdc}, I_E = 0)$ MJE15028, MJE15029 $(V_{CB} = 150 \text{ Vdc}, I_E = 0)$ MJE15030, MJE15031	I _{CBO}	-	10 10	
Emitter Cutoff Current ($V_{BE} = 5.0 \text{ Vdc}, I_C = 0$)	I _{EBO}	_	10	
ON CHARACTERISTICS (Note 1)				
	h _{FE}	40 40 40 20		-
DC Current Gain Linearity (V _{CE} From 2.0 V to 20 V, I _C From 0.1 A to 3 A) (NPN to PNP)	h _{FE}	Typ 2 3		
Collector–Emitter Saturation Voltage ($I_C = 1.0 \text{ Adc}, I_B = 0.1 \text{ Adc}$)	V _{CE(sat)}	_	0.5	Vdc
Base–Emitter On Voltage (I _C = 1.0 Adc, V _{CE} = 2.0 Vdc)	V _{BE(on)}	_	1.0	Vdc
DYNAMIC CHARACTERISTICS				•
			1	

Current Gain – Bandwidth Product (Note 2)	f _T			MHz	Ī
$(I_C = 500 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f_{test} = 10 \text{ MHz})$		30	-		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions. 1. Pulse Test: Pulse Width \leq 300 µs, Duty Cycle \leq 2.0%. 2. $f_T = |h_{fe}| \bullet f_{test}$.

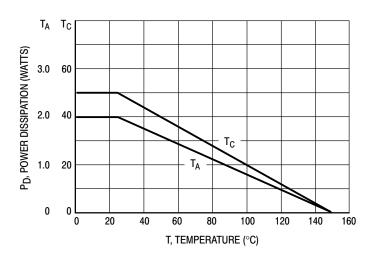


Figure 1. Power Derating

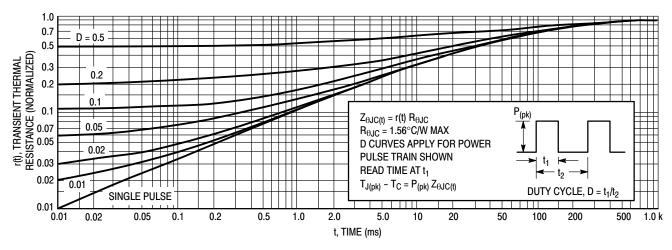


Figure 2. Thermal Response

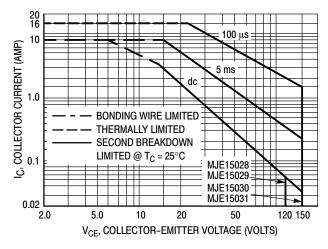
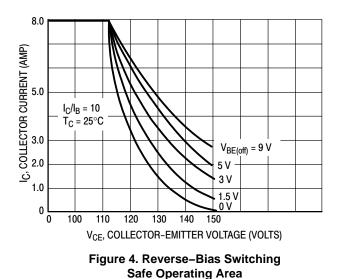


Figure 3. Forward Bias Safe Operating Area



There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate I_C-V_{CE} limits of the transistor that must be observed for reliable operation, i.e., the transistor must not be subjected to greater dissipation then the curves indicate.

The data of Figures 3 and 4 is based on $T_{J(pk)} = 150^{\circ}$ C; T_{C} is variable depending on conditions. Second breakdown pulse limits are valid for duty cycles to 10% provided $T_{J(pk)} < 150^{\circ}$ C. $T_{J(pk)}$ may be calculated from the data in Figure 2. At high case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown.

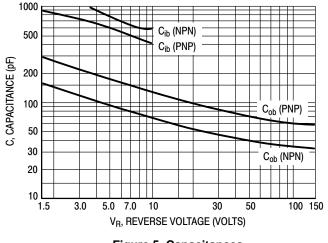
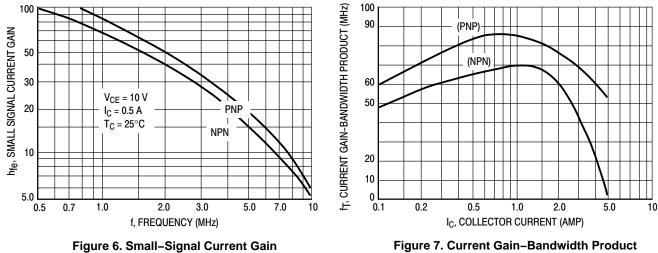
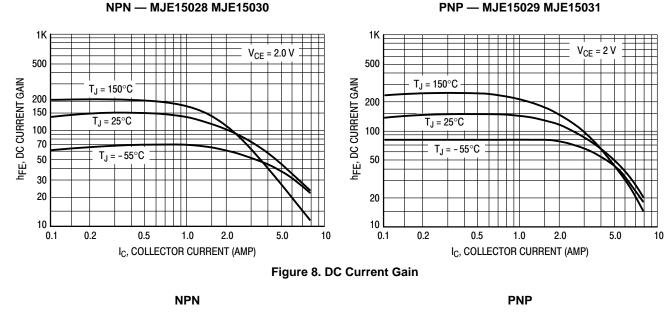


Figure 5. Capacitances





gure 7. Current Gain-Balluwiuth Plouu

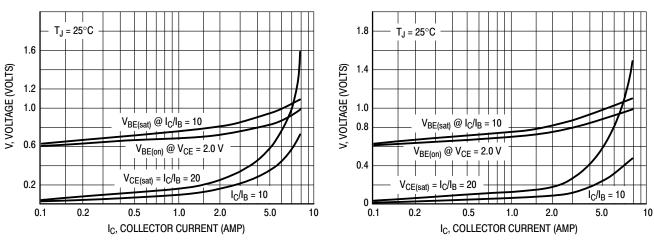
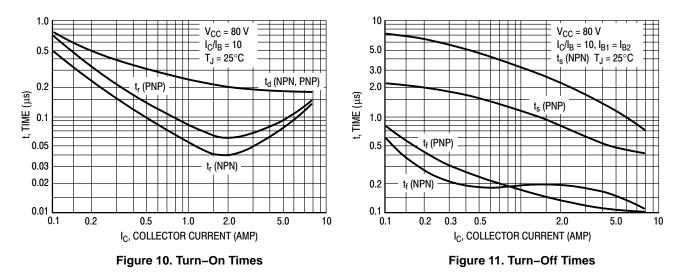


Figure 9. "On" Voltage



ORDERING INFORMATION

Device	Package	Shipping
MJE15028G	TO-220 (Pb-Free)	50 Units / Rail
MJE15029G	TO-220 (Pb-Free)	50 Units / Rail
MJE15030G	TO-220 (Pb-Free)	50 Units / Rail
MJE15031G	TO-220 (Pb-Free)	50 Units / Rail

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		TO-220 CASE 221A ISSUE AK						DATE	13 JAN 2022
SCALE 1:1			1. C 2. C 3. C	CONTR DIMEN LEAD	ROLLING DI ISION Z DEI D IRREGULA	MENSION FINES A ZO ARITIES AR	ONE WHERE AL E ALLOWED.		
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				ым 🛛	MIN.	MAX.	MIN.	MAX.	
	2 3			A	0.570	0.620	14.48	15.75	
				в	0.380	0.415	9.66	10.53	
н —	₩₩			с	0.160	0.190	4.07	4.83	
	7 \7	H I		D	0.025	0.038	0.64	0.96	
z_				F	0.142	0.161	3.60	4.09	
<u> </u>	I K			G	0.095	0.105	2.42	2.66	
				н	0.110	0.161	2.80	4.10	
	Щ Щ <u> </u>	Ü I		J	0.014	0.024	0.36	0.61	
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V — + I I-	►- ``.			L	0.045	0.060	1.15	1.52	
G 	. <mark> </mark> J [−]			N	0.190	0.210	4.83	5.33	
· · · ·	- → D			Q	0.100	0.120	2.54	3.04	
	N 🖛			R	0.080	0.110	2.04	2.79	
				s	0.045	0.055	1.15	1.41	
				т	0.235	0.255	5.97	6.47	
				U	0.000	0.050	0.00	1.27	
				V	0.045		1.15		
				Z		0.080		2.04	
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 TO-220
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