# MMBD6100LT1G

# Monolithic Dual Switching Diode

#### Features

• These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

## **MAXIMUM RATINGS (EACH DIODE)**

Rating	Symbol	Value	Unit
Reverse Voltage	V <sub>R</sub>	70	Vdc
Forward Current	١ <sub>F</sub>	200	mAdc
Peak Forward Surge Current	I <sub>FM(surge)</sub>	500	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR–5 Board (Note 1) $T_A = 25^{\circ}C$ Derate above 25°C	P <sub>D</sub>	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\thetaJA}$	556	°C/W
Total Device Dissipation Alumina Substrate (Note 2) $T_A = 25^{\circ}C$ Derate above 25°C	P <sub>D</sub>	300 2.4	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\thetaJA}$	417	°C/W
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.

2. Alumina = 0.4  $\times$  0.3  $\times$  0.024 in. 99.5% alumina.

Characteristic

ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Symbol Min Max Unit

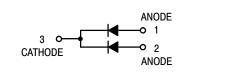
	•,•			•
OFF CHARACTERISTICS				
Reverse Breakdown Voltage (I <sub>(BR)</sub> = 100 μAdc)	V <sub>(BR)</sub>	70	1	Vdc
Reverse Voltage Leakage Current $(V_R = 50 \text{ Vdc})$ (For each individual diode while the second diode is unbiased)	I <sub>R</sub>	-	0.1	μAdc
Forward Voltage (I <sub>F</sub> = 1.0 mAdc) (I <sub>F</sub> = 100 mAdc)	VF	0.55 0.8	0.7 1.1	Vdc
Reverse Recovery Time ( $I_F = I_R = 10$ mAdc, $I_{R(REC)} = 1.0$ mAdc) (Figure 1)	t <sub>rr</sub>	-	4.0	ns
Capacitance (V <sub>R</sub> = 0 V)	С	_	2.5	pF

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



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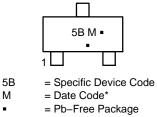
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CASE 318 STYLE 9

### MARKING DIAGRAM



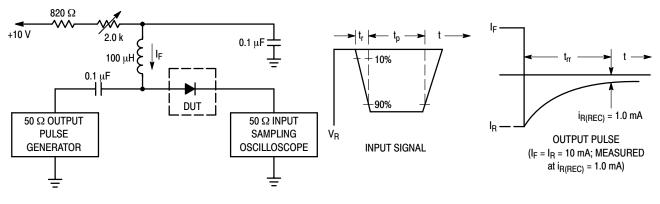
(Note: Microdot may be in either location) \*Date Code orientation and/or overbar may vary depending upon manufacturing location.

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MMBD6100LT1G	SOT-23 (Pb-Free)	3000/Tape & Reel
MMBD6100LT3G	SOT–23 (Pb–Free)	10,000/Tape & Reel

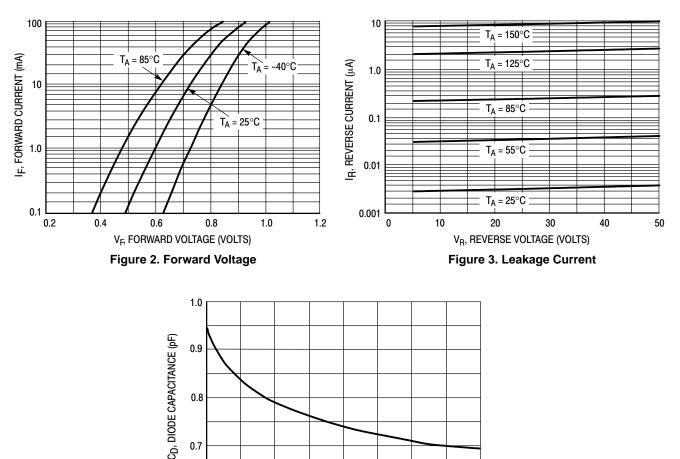
+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MMBD6100LT1G

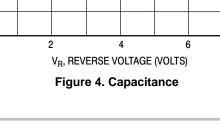


Notes: 1. A 2.0 k $\Omega$  variable resistor adjusted for a Forward Current (I<sub>F</sub>) of 10 mA. 2. Input pulse is adjusted so I<sub>R(peak)</sub> is equal to 10 mA. 3. t<sub>p</sub> » t<sub>rr</sub>

Figure 1. Recovery Time Equivalent Test Circuit



## CURVES APPLICABLE TO EACH CATHODE



8

0.6

0





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