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Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (_), the underscore (_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at www.onsemi.com. Please email any questions regarding the system integration to Fairchild guestions@onsemi.com.

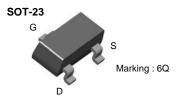
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July 2011

MMBFJ305 N-Channel RF Amplifier

Features

- This device is designed primarily for electronic switching applications such as low On Resistance analog switching.
- Sourced from process 50.



Note: Drain & Source are interchangeable.

Absolute Maximum Ratings* T_A = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V_{DG}	Drain-Gate Voltage	30	V
V _{GS}	Gate-Source Voltage	-30	V
I_GF	Forward Gate Current	10	mA
T _{J,} T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics* T_A = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
P _D	Total Device Dissipation	225	mW
	Derate above 25°C	1.8	mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	556	°C/W

^{*} Device mounted on FR-4 PCB 1.6" x 1.6" x 0.06".

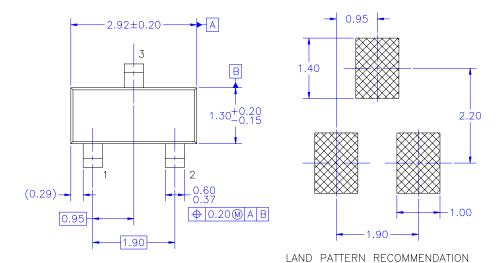
Electrical Characteristics T_A=25°C unless otherwise noted

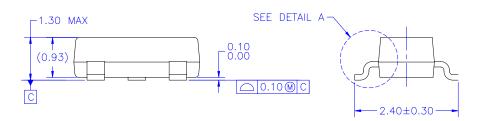
Symbol	Parameter	Conditions	Min.	Max.	Units			
Off Charact	Off Characteristics							
V _{(BR)GSS}	Gate-Source Breakdown Voltage	$I_G = -1.0 \mu A, V_{DS} = 0$	-30		V			
I _{GSS}	Gate Reverse Current	$V_{GS} = -20V, V_{DS} = 0$		-100	pА			
V _{GS} (off)	Gate-Source Cutoff Voltage	$V_{DS} = 15V, I_{D} = 1.0nA$	-0.5	-3.0	V			
On Characteristics								
I _{DSS}	Zero-Gate Voltage Drain Current*	$V_{DS} = 15V, V_{GS} = 0$	1.0	8.0	mA			
Small Signal Characteristics								
gfs	Forward Transfer Conductance	$V_{DS} = 15V, V_{GS} = 0, f = 1.0kHz$	3000		μmhos			
9oss	Output Conductance	$V_{DS} = 15V, V_{GS} = 0, f = 1.0kHz$		50	μmhos			

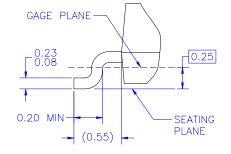
^{*} Pulse Test: Pulse Width $\leq 300 \mu s$, Duty Cycle $\leq 2.0\%$

Physical Dimensions

SOT-23







DETAIL A

- NOTES: UNLESS OTHERWISE SPECIFIED
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 - DRAWING FILE NAME: MAO3DREV9

Dimensions in Millimeters





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Definition of Terms

Definition of Terms				
Datasheet Identification	Product Status	Definition		
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.		
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.		
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