

# **General Purpose Transistor**

## **PNP Silicon**

# MMBT2907AWT1G, NSVMMBT2907AWT1G

These transistors are designed for general purpose amplifier applications. They are housed in the SC-70/SOT-323 package which is designed for low power surface mount applications.

## **Features**

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### **MAXIMUM RATINGS**

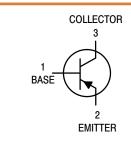
Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V <sub>CEO</sub>	-60	Vdc
Collector - Base Voltage	V <sub>CBO</sub>	-60	Vdc
Emitter – Base Voltage	V <sub>EBO</sub>	-5.0	Vdc
Collector Current - Continuous	I <sub>C</sub>	-600	mAdc

## THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) T <sub>A</sub> = 25°C	P <sub>D</sub>	150	mW
Thermal Resistance Junction-to-Ambient	$R_{\theta JA}$	833	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	−55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1.  $FR-5 = 1.0 \times 0.75 \times 0.062$  in.





SC -70/SOT- 323 CASE 419 - 04 STYLE 3

### **MARKING DIAGRAM**



20 = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

\*Date Code orientation may vary depending upon manufacturing location.

### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MMBT2907AWT1G	SC-70 (Pb-Free)	3000 Tape & Reel
NSVMMBT2907AWT1G	SC-70 (Pb-Free)	3000 Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MMBT2907AWT1G, NSVMMBT2907AWT1G

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic			Min	Max	Unit
OFF CHARACTERISTICS		· ·			I
Collector – Emitter Breakdown Voltage (Note $(I_C = -10 \text{ mAdc}, I_B = 0)$	2)	V <sub>(BR)CEO</sub>	-60	_	Vdc
Collector – Base Breakdown Voltage (I <sub>C</sub> = –10 μAdc, I <sub>E</sub> = 0)		V <sub>(BR)CBO</sub>	-60	_	Vdc
Emitter – Base Breakdown Voltage ( $I_E = -10 \mu Adc$ , $I_C = 0$ )		V <sub>(BR)EBO</sub>	-5.0	-	Vdc
Base Cutoff Current (V <sub>CE</sub> = -30 Vdc, V <sub>EB(off)</sub> = -0.5 Vdc)		I <sub>BL</sub>	-	-50	nAdc
Collector Cutoff Current (V <sub>CE</sub> = -30 Vdc, V <sub>EB(off)</sub> = -0.5 Vdc)		I <sub>CEX</sub>	ı	-50	nAdc
ON CHARACTERISTICS(3)		•		•	•
DC Current Gain (Note 2) $ \begin{aligned} &(I_C = -0.1 \text{ mAdc, } V_{CE} = -10 \text{ Vdc)} \\ &(I_C = -1.0 \text{ mAdc, } V_{CE} = -10 \text{ Vdc)} \\ &(I_C = -1.0 \text{ mAdc, } V_{CE} = -10 \text{ Vdc)} \\ &(I_C = -150 \text{ mAdc, } V_{CE} = -10 \text{ Vdc)} \\ &(I_C = -500 \text{ mAdc, } V_{CE} = -10 \text{ Vdc)} \\ &(I_C = -500 \text{ mAdc, } V_{CE} = -10 \text{ Vdc)} \end{aligned} $	H <sub>FE</sub>	75 100 100 100 50	- - - 340 -	-	
Collector – Emitter Saturation Voltage (Note 2 ( $I_C$ = -150 mAdc, $I_B$ = -15 mAdc) ( $I_C$ = -500 mAdc, $I_B$ = -50 mAdc)	V <sub>CE(sat)</sub>	- -	-0.4 -1.6	Vdc	
Base – Emitter Saturation Voltage (Note 2) ( $I_C$ = -150 mAdc, $I_B$ = -15 mAdc) ( $I_C$ = -500 mAdc, $I_B$ = -50 mAdc)	V <sub>BE(sat)</sub>	- -	-1.3 -2.6	Vdc	
SMALL-SIGNAL CHARACTERISTICS		•		•	
Current – Gain – Bandwidth Product (I <sub>C</sub> = –50 mAdc, V <sub>CE</sub> = 20 Vdc, f = 100 MHz)		f <sub>T</sub>	200	_	MHz
Output Capacitance (V <sub>CB</sub> = -10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>obo</sub>	-	8.0	pF	
Input Capacitance (V <sub>EB</sub> = -2.0 Vdc, I <sub>C</sub> = 0, f = 1.0 MHz)	C <sub>ibo</sub>	ı	30	pF	
SWITCHING CHARACTERISTICS					
Turn-On Time		t <sub>on</sub>	-	45	
Delay Time	$(V_{CC} = -30 \text{ Vdc}, I_{C} = -150 \text{ mAdc}, I_{B1} = -15 \text{ mAdc})$	t <sub>d</sub>	-	10	
Rise Time		t <sub>r</sub>	-	40	no
Storage Time		t <sub>s</sub>	_	80	ns
Fall Time	$(V_{CC} = -6.0 \text{ Vdc}, I_{C} = -150 \text{ mAdc}, I_{B_1} = I_{B_2} = 15 \text{ mAdc})$	t <sub>f</sub>	-	30	
Turn-Off Time	J. 32 ,	t <sub>off</sub>	-	100	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: Pulse Width  $\leq 300 \,\mu s$ , Duty Cycle  $\leq 2.0\%$ .

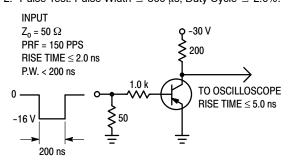


Figure 1. Delay and Rise Time Test Circuit

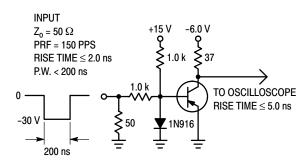


Figure 2. Storage and Fall Time Test Circuit

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## **TYPICAL CHARACTERISTICS**

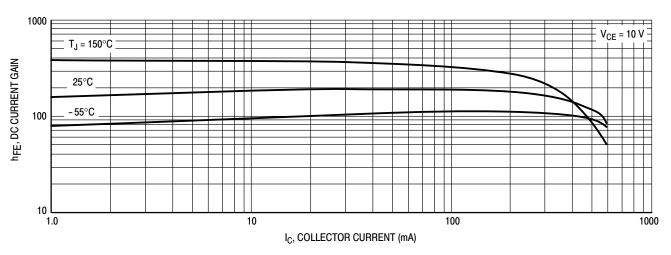


Figure 3. DC Current Gain

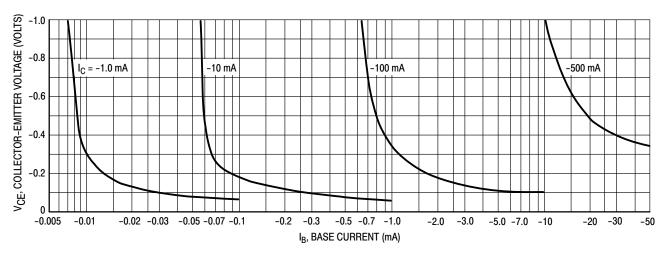


Figure 4. Collector Saturation Region

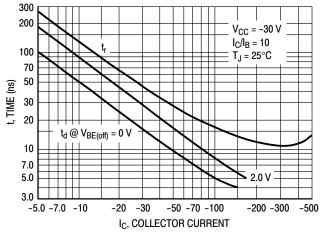


Figure 5. Turn-On Time

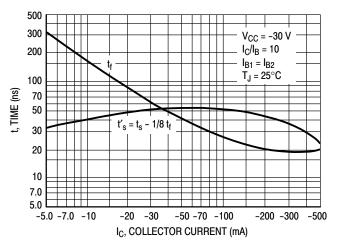


Figure 6. Turn-Off Time

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# TYPICAL SMALL-SIGNAL Characteristics NOISE FIGURE

 $V_{CE}$  = 10 Vdc,  $T_A$  = 25°C

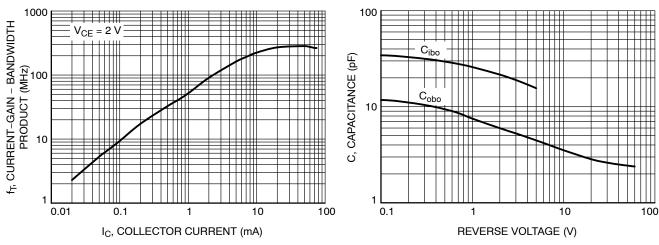


Figure 7. Current-Gain - Bandwidth Product

Figure 8. Capacitances

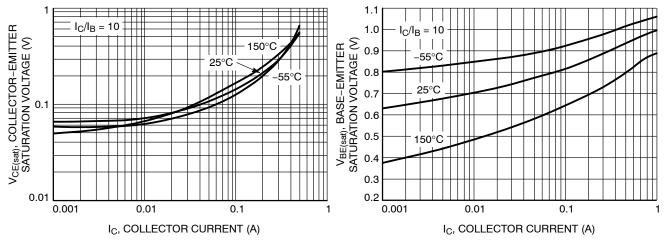


Figure 9. Collector Emitter Saturation Voltage vs. Collector Current

Figure 10. Base Emitter Saturation Voltage vs.
Collector Current

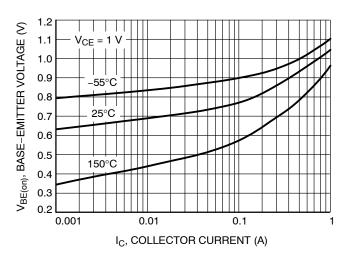


Figure 11. Base Emitter Voltage vs. Collector
Current





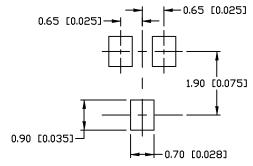
SC-70 (SOT-323) CASE 419 ISSUE P

**DATE 07 OCT 2021** 

#### NOTES:

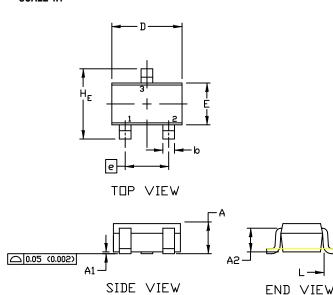
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH

	MILLIMETERS			INCHES		
DIM	MIN.	N□M.	MAX.	MIN.	N□M.	MAX.
Α	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2		0.70 REF			0.028 BS	C
b	0.30	0.35	0.40	0.012	0.014	0.016
С	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
Ε	1.15	1.24	1.35	0.045	0.049	0.053
e	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC				0.026 BS	C
L	0.20	0.38	0.56	0.008	0.015	0.022
HE	2.00	2.10	2.40	0.079	0.083	0.095
				`		



For additional information on our Pb-Free strategy and soldering details, please download the IIN Semiconductor Soldering and Mounting Techniques Reference Manual, SILDERRM/D.

SOLDERING FOOTPRINT



# GENERIC MARKING DIAGRAM



XX = Specific Device Code

M = Date Code

■ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1: CANCELLED	STYLE 2: PIN 1. ANODE 2. N.C. 3. CATHODE	STYLE 3: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 5: PIN 1. ANODE 2. ANODE 3. CATHODE	
STYLE 6:	STYLE 7:	STYLE 8:	STYLE 9:	STYLE 10:	STYLE 11:
PIN 1. EMITTER	PIN 1. BASE	PIN 1. GATE	PIN 1. ANODE	PIN 1. CATHODE	PIN 1. CATHODE
2. BASE	2. EMITTER	2. SOURCE	2. CATHODE	2. ANODE	2. CATHODE
3. COLLECTOR	3. COLLECTOR	3. DRAIN	3. CATHODE-ANODE	3. ANODE-CATHODE	3. CATHODE

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