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March 2014



# MMBT3640 PNP Switching Amplifier

## Description

This device is designed for very high-speed saturated switching at collector currents to 100 mA. Sourced from process 65.



### **Ordering Information**

Part Number	Marking	Package	Packing Method
MMBT3640	2J	SOT-23 3L	Tape and Reel

## Absolute Maximum Ratings<sup>(1),(2)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V <sub>CEO</sub>	Collector-Emitter Voltage	-12	V
V <sub>CBO</sub>	Collector-Base Voltage	-12	V
V <sub>EBO</sub>	Emitter-Base Voltage	-4	V
I <sub>C</sub>	Collector Current - Continuous	-200	mA
T <sub>J</sub> , T <sub>STG</sub>	Junction and Storage Temperature Range	-55 to +150	°C

### Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

## Thermal Characteristics<sup>(3)</sup>

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
р	Total Device Dissipation	225	mW
PD	Derate Above T <sub>A</sub> = 25°C	1.8	mW/°C
$R_{\thetaJA}$	Thermal Resistance, Junction to Ambient	556	°C/W

### Note:

3. Device mounted on FR-4 PCB 1.6 inch X 1.6 inch X 0.06 inch.

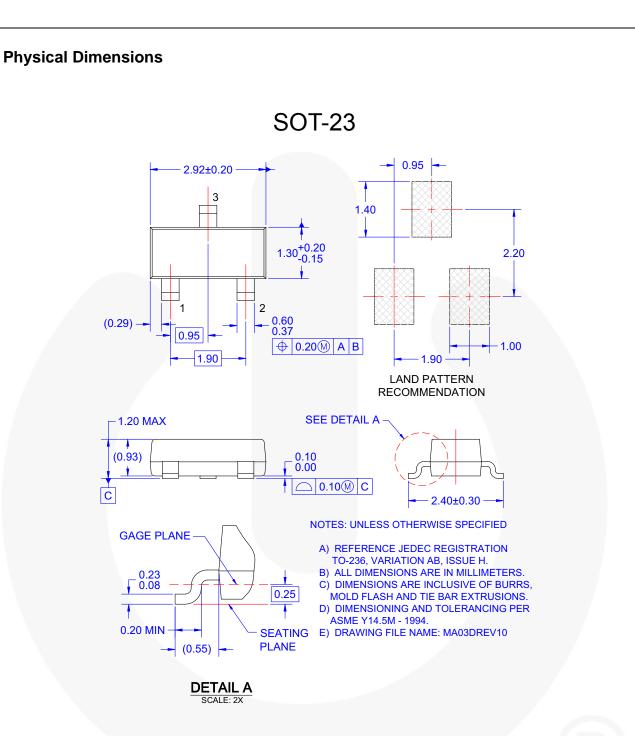
# **Electrical Characteristics**

Values are at  $T_A = 25^{\circ}C$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage <sup>(4)</sup>	I <sub>C</sub> = -10 mA, I <sub>B</sub> = 0	-12		V
V <sub>(BR)CES</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -100 μA, V <sub>BE</sub> = 0	-12		V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -100 μA, I <sub>E</sub> = 0	-12		V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -100 μA, I <sub>C</sub> = 0	-4.0		V
I <sub>CES</sub>	Collector Cut-Off Current	V <sub>CE</sub> = -6.0 V, V <sub>BE</sub> = 0		-0.01	μΑ
		$V_{CE} = -6.0 \text{ V}, V_{BE} = 0,$ $T_A = 65^{\circ}\text{C}$		-1.00	
Ι <sub>Β</sub>	Base Current	V <sub>CE</sub> = -6.0 V, V <sub>BE</sub> = 0		-10	nA
h	DC Current Gain <sup>(4)</sup>	I <sub>C</sub> = -10 mA, V <sub>CE</sub> = -0.3 V	30	120	
h <sub>FE</sub>		I <sub>C</sub> = -50 mA, V <sub>CE</sub> = -1.0 V	20		
		I <sub>C</sub> = -10 mA, I <sub>B</sub> = -0.5 mA		-0.30	
	Collector Emitter Saturation	I <sub>C</sub> = -10 mA, I <sub>B</sub> = -1.0 mA		-0.20	
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage <sup>(4)</sup>	I <sub>C</sub> = -50 mA, I <sub>B</sub> = -5.0 mA		-0.60	V
		$I_{\rm C}$ = -10 mA, $I_{\rm B}$ = -1.0 mA, $T_{\rm A}$ = 65°C		-0.25	
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage <sup>(4)</sup>	I <sub>C</sub> = -10 mA, I <sub>B</sub> = -0.5 mA	-0.75	-0.95	v
		I <sub>C</sub> = -10 mA, I <sub>B</sub> = -1.0 mA	-0.80	-1.00	
		I <sub>C</sub> = -50 mA, I <sub>B</sub> = -5.0 mA		-1.50	
f <sub>T</sub>	Current Gain - Bandwidth Product	I <sub>C</sub> = -10 mA, V <sub>CE</sub> = -5.0 V, f = 100 MHz	500		MHz
C <sub>ob</sub>	Output Capacitance	$V_{CB}$ = -5.0 V, I <sub>E</sub> = 0, f = 1.0 MHz		3.5	pF
C <sub>ib</sub>	Input Capacitance	$V_{EB} = -0.5 \text{ V}, \text{ I}_{C} = 0,$ f = 1.0 MHz		3.5	pF
t <sub>d</sub>	Delay Time	V <sub>CC</sub> = -6 V, V <sub>BE(off)</sub> = -1.9 V,		10	ns
t <sub>r</sub>	Rise Time	I <sub>C</sub> = -50 mA, I <sub>B1</sub> = -5.0 mA		30	ns
t <sub>s</sub>	Storage Time	V <sub>CC</sub> = -6 V, I <sub>C</sub> = -50 mA,		20	ns
t <sub>f</sub>	Fall Time	I <sub>B1</sub> = I <sub>B2</sub> = -5.0 mA		12	ns
t <sub>on</sub>		$V_{CC}$ = -6 V, $V_{BE(off)}$ = -1.9 V, I <sub>C</sub> = -50 mA, I <sub>B1</sub> = -5.0 mA		25	– ns
	Turn-On Time	$V_{CC}$ = -1.5 V, I <sub>C</sub> = -10 mA, I <sub>B1</sub> = I <sub>B2</sub> = -0.5 mA		60	
t <sub>off</sub>	Turn Off Time	$V_{CC}$ = -6 V, $V_{BE(off)}$ = -1.9 V, I <sub>C</sub> = -50 mA, I <sub>B1</sub> = -5.0 mA		35	ns
	Turn-Off Time	V <sub>CC</sub> = -1.5 V, I <sub>C</sub> = -10 mA, I <sub>B1</sub> = I <sub>B2</sub> = -0.5 mA		75	

### Note:

4. Pulse test: pulse width  $\leq$  300 µs, duty cycle  $\leq$  2.0%.



### Figure1. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE (ACTIVE)

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