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PNP Switching Transistor

The MMBT4403M3T5G device is a spin-off of our popular SOT-23 three-leaded device. It is designed for general purpose switching applications and is housed in the SOT-723 surface mount package. This device is ideal for low-power surface mount applications where board space is at a premium.

Features

- Reduces Board Space
- This is a Halide-Free Device
- This is a Pb-Free Device

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V_{CEO}	-40	Vdc
Collector - Base Voltage	V _{CBO}	-40	Vdc
Emitter – Base Voltage	V _{EBO}	-5.0	Vdc
Collector Current - Continuous	Ic	-600	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) T _A = 25°C Derate above 25°C	P _D	265 2.1	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{ heta JA}$	470	°C/W
Total Device Dissipation Alumina Substrate, (Note 2) T _A = 25°C Derate above 25°C	P _D	640 5.1	mW mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	195	°C/W
Junction and Storage Temperature	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

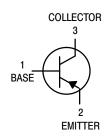
1

- 1. FR-5 = $1.0 \times 0.75 \times 0.062$ in.
- 2. Alumina = 0.4 \times 0.3 \times 0.024 in. 99.5% alumina.



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MARKING DIAGRAM



SOT-723 CASE 631AA STYLE 1



AG M = Specific Device Code

M = Date Code

ORDERING INFORMATION

Device	Package	Shipping [†]		
MMBT4403M3T5G	SOT-723 (Pb-Free)	8000/Tape & Reel		

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic			Min	Max	Unit
OFF CHARACTERISTICS		•			
Collector - Emitter Breakdown Voltage	(Note 3) $(I_C = -1.0 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	-40	_	Vdc
Collector - Base Breakdown Voltage	$(I_C = -0.1 \text{ mAdc}, I_E = 0)$	V _{(BR)CBO}	-40	-	Vdc
Emitter - Base Breakdown Voltage	$(I_E = -0.1 \text{ mAdc}, I_C = 0)$	V _{(BR)EBO}	-5.0	-	Vdc
Base Cutoff Current	$(V_{CE} = -35 \text{ Vdc}, V_{EB} = -0.4 \text{ Vdc})$	I _{BEV}	-	-0.1	μAdc
Collector Cutoff Current	$(V_{CE} = -35 \text{ Vdc}, V_{EB} = -0.4 \text{ Vdc})$	I _{CEX}	-	-0.1	μAdc
ON CHARACTERISTICS		•		•	•
DC Current Gain (Note 3) (Note 3)	$ \begin{array}{l} (I_{C}=-0.1 \text{ mAdc}, V_{CE}=-1.0 \text{ Vdc}) \\ (I_{C}=-1.0 \text{ mAdc}, V_{CE}=-1.0 \text{ Vdc}) \\ (I_{C}=-10 \text{ mAdc}, V_{CE}=-1.0 \text{ Vdc}) \\ (I_{C}=-150 \text{ mAdc}, V_{CE}=-2.0 \text{ Vdc}) \\ (I_{C}=-500 \text{ mAdc}, V_{CE}=-2.0 \text{ Vdc}) \end{array} $	h _{FE}	30 60 100 100 20	- - - 300 -	-
Collector - Emitter Saturation Voltage	V _{CE(sat)}	_ _	-0.4 -0.75	Vdc	
Base - Emitter Saturation Voltage (Not	V _{BE(sat)}	-0.75 -	-0.95 -1.3	Vdc	
SMALL-SIGNAL CHARACTERISTIC	s				
Current - Gain - Bandwidth Product	nt – Gain – Bandwidth Product (I _C = –20 mAdc, V _{CE} = –10 Vdc, f = 100 MHz)		200	-	MHz
Collector-Base Capacitance	$(V_{CB} = -10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C _{cb}	-	8.5	pF
Emitter-Base Capacitance	$(V_{BE} = -0.5 \text{ Vdc}, I_{C} = 0, f = 1.0 \text{ MHz})$	C _{eb}	-	30	pF
Input Impedance	$(I_C = -1.0 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{ie}	1.5	15	kΩ
Voltage Feedback Ratio	$(I_C = -1.0 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{re}	0.1	8.0	X 10 ⁻⁴
Small-Signal Current Gain	$(I_C = -1.0 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}, f = 1.0 \text{ kHz})$	h _{fe}	60	500	-
Output Admittance	$(I_C = -1.0 \text{ mAdc}, V_{CE} = -10 \text{ Vdc}, f = 1.0 \text{ kHz})$		1.0	100	μMhos
SWITCHING CHARACTERISTICS					
Delay Time	$(V_{CC} = -30 \text{ Vdc}, V_{EB} = -2.0 \text{ Vdc},$	t _d	-	15	
Rise Time	I _C = -150 mAdc, I _{B1} = -15 mAdc)	t _r	-	20	ns
Storage Time	(V _{CC} = -30 Vdc, I _C = -150 mAdc,	t _s	-	225	no
Fall Time	$I_{B1} = I_{B2} = -15 \text{ mAdc}$	t _f	-	30	ns

^{3.} Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.

SWITCHING TIME EQUIVALENT TEST CIRCUIT

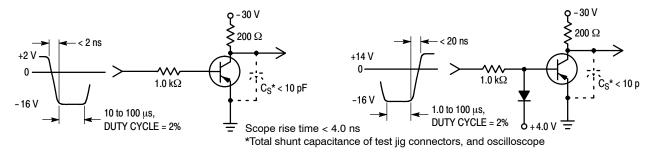


Figure 1. Turn-On Time

Figure 2. Turn-Off Time

STATIC CHARACTERISTICS

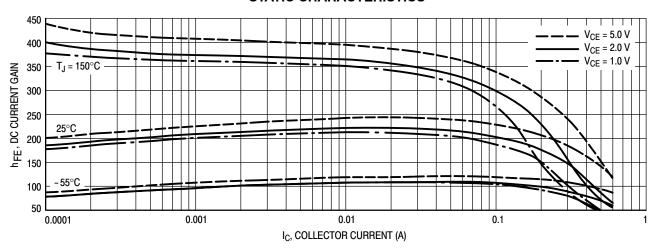


Figure 3. DC Current Gain

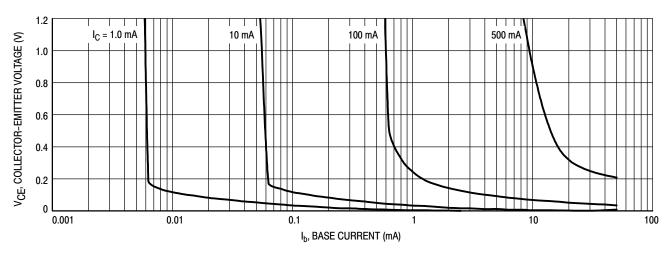


Figure 4. Collector Saturation Region

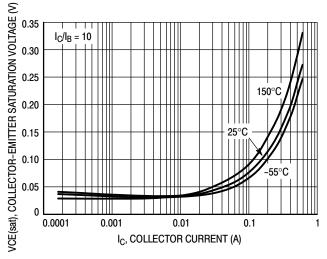


Figure 5. Collector-Emitter Saturation Voltage vs. Collector Current

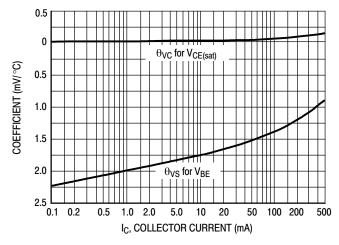


Figure 6. Temperature Coefficients

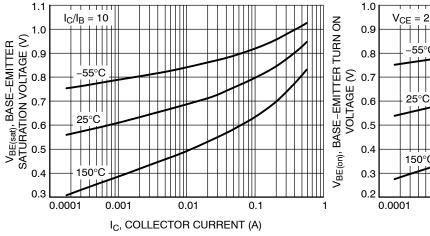


Figure 7. Base-Emitter Saturation Voltage vs. Collector Current

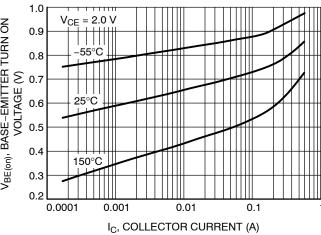


Figure 8. Base-Emitter Turn On Voltage vs.
Collector Current

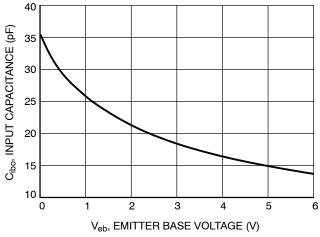


Figure 9. Input Capacitance vs. Emitter Base Voltage

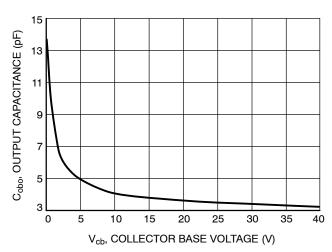
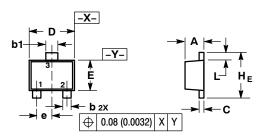


Figure 10. Output Capacitance vs. Collector Base Voltage

PACKAGE DIMENSIONS

SOT-723 CASE 631AA-01 ISSUE C



STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR

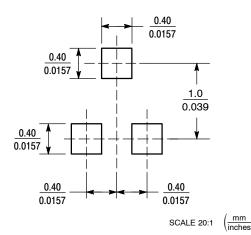
NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: MILLIMETERS.
 MAXIMUM LEAD THICKNESS INCLUDES LEAD
- FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

 DIMENSIONS D AND E DO NOT INCLUDE MOLD
- FLASH, PROTRUSIONS OR GATE BURRS.

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.45	0.50	0.55	0.018	0.020	0.022	
b	0.15	0.21	0.27	0.0059	0.0083	0.0106	
b1	0.25	0.31	0.37	0.010	0.012	0.015	
С	0.07	0.12	0.17	0.0028	0.0047	0.0067	
D	1.15	1.20	1.25	0.045	0.047	0.049	
E	0.75	0.80	0.85	0.03	0.032	0.034	
е	0.40 BSC			C	.016 BS	С	
ΗE	1.15	1.20	1.25	0.045	0.047	0.049	
L	0.15	0.20	0.25	0.0059	0.0079	0.0098	

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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