NPN - MPS6601; PNP - MPS6651, MPS6652

MPS6652 is a Preferred Device

Amplifier Transistors

Features

- Voltage and Current are Negative for PNP Transistors
- Pb–Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage MPS6601/6651 MPS6652	V _{CEO}	25 40	Vdc
Collector – Base Voltage MPS6601/6651 MPS6652	V _{CBO}	25 30	Vdc
Emitter-Base Voltage	V _{EBO}	4.0	Vdc
Collector Current – Continuous	۱ _C	1000	mAdc
Total Device Dissipation @ $T_A = 25^{\circ}C$ Derate above 25°C	P _D	625 5.0	W mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	PD	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient (Note 1)	R_{\thetaJA}	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

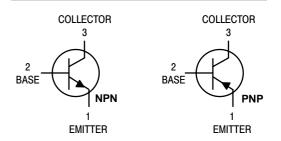
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

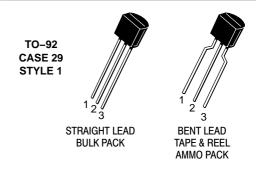
1. $R_{\theta,IA}$ is measured with the device soldered into a typical printed circuit board.

ON

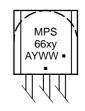
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MARKING DIAGRAM



MPS66xy	= Device Code
-	x = 0 or 5
	y = 1 or 2
А	= Assembly Location
Υ	= Year
WW	= Work Week
•	= Pb–Free Package
oto: Microdo	t may be in either least

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Preferred devices are recommended choices for future use

and best overall value.

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

Semiconductor Components Industries. LLC. 2007

NPN - MPS6601; PNP - MPS6651, MPS6652

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

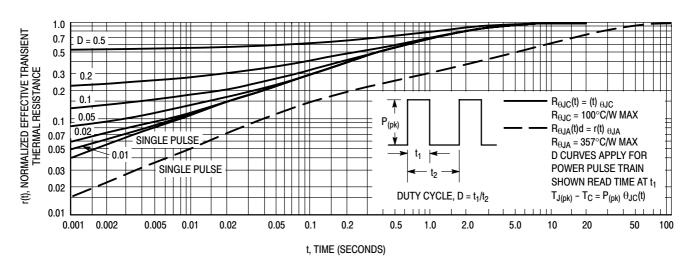
Chara	acteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage ($I_C = 1.0 \text{ mAdc}, I_B = 0$)	MPS6601/6651 MPS6652	V _{(BR)CEO}	25 40		Vdc
Collector – Base Breakdown Voltage ($I_C = 100 \ \mu Adc, I_E = 0$)	MPS6601/6651 MPS6652	V _{(BR)CBO}	25 40		Vdc
Emitter – Base Breakdown Voltage $(I_E = 10 \ \mu Adc, I_C = 0)$		V _{(BR)EBO}	4.0	-	Vdc
$ Collector Cutoff Current \\ (V_{CE} = 25 \text{ Vdc}, \text{ I}_{B} = 0) \\ (V_{CE} = 30 \text{ Vdc}, \text{ I}_{B} = 0) $	MPS6601/6651 MPS6652	I _{CES}		0.1 0.1	μAdc
$ Collector Cutoff Current \\ (V_{CB} = 25 Vdc, I_E = 0) \\ (V_{CB} = 30 Vdc, I_E = 0) $	MPS6601/6651 MPS6652	I _{СВО}		0.1 0.1	μAdc
ON CHARACTERISTICS					
		h _{FE}	50 50 30		_
Collector – Emitter Saturation Voltage $(I_C = 1000 \text{ mAdc}, I_B = 100 \text{ mAdc})$		V _{CE(sat)}	-	0.6	Vdc
Base–Emitter On Voltage (I _C = 500 mAdc, V _{CE} = 1.0 Vdc)		V _{BE(on)}	-	1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS	; ;		•	•	
$\begin{array}{l} Current-Gain - Bandwidth \ Product \\ (I_C = 50 \ mAdc, \ V_{CE} = 10 \ Vdc, \ f = 1 \end{array}$	00 MHz)	f _T	100	_	MHz
Output Capacitance $(V_{CB} = 10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$)	C _{obo}	_	30	pF
SWITCHING CHARACTERISTICS					
Delay Time		t _d	-	25	ns
Rise Time	$(V_{CC} = 40 \text{ Vdc}, I_C = 500 \text{ mAdc}, I_{B1} = 50 \text{ mAdc},$	t _r	-	30	ns
Storage Time	$t_p \ge 300 \text{ ns Duty Cycle})$	t _s	-	250	ns
Fall Time		t _f	_	50	ns

ORDERING INFORMATION

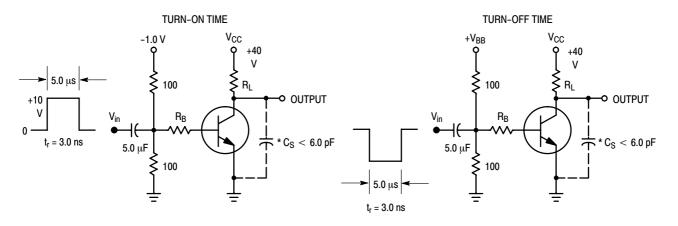
Device	Package	Shipping [†]
MPS6601RLRAG	TO-92 (TO-226) (Pb-Free)	2000 Units / Tape & Reel
MPS6651G	TO-92 (TO-226) (Pb-Free)	
MPS6652	TO-92 (TO-226)	5000 Units / Bulk
MPS6652G	TO-92 (TO-226) (Pb-Free)	
MPS6652RLRAG	TO-92 (TO-226) (Pb-Free)	2000 Units / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

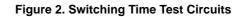
NPN – MPS6601; PNP – MPS6651, MPS6652

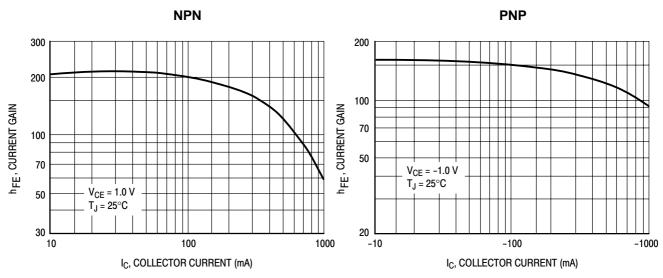






*Total Shunt Capacitance of Test Jig and Connectors For PNP Test Circuits, Reverse All Voltage Polarities





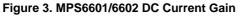


Figure 4. MPS6651/6652 DC Current Gain

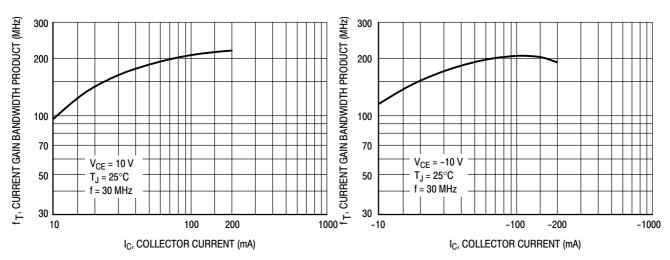
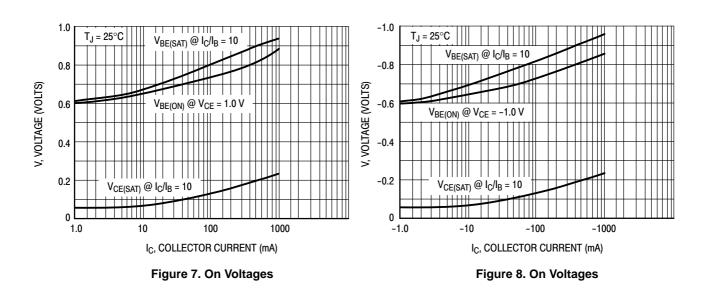
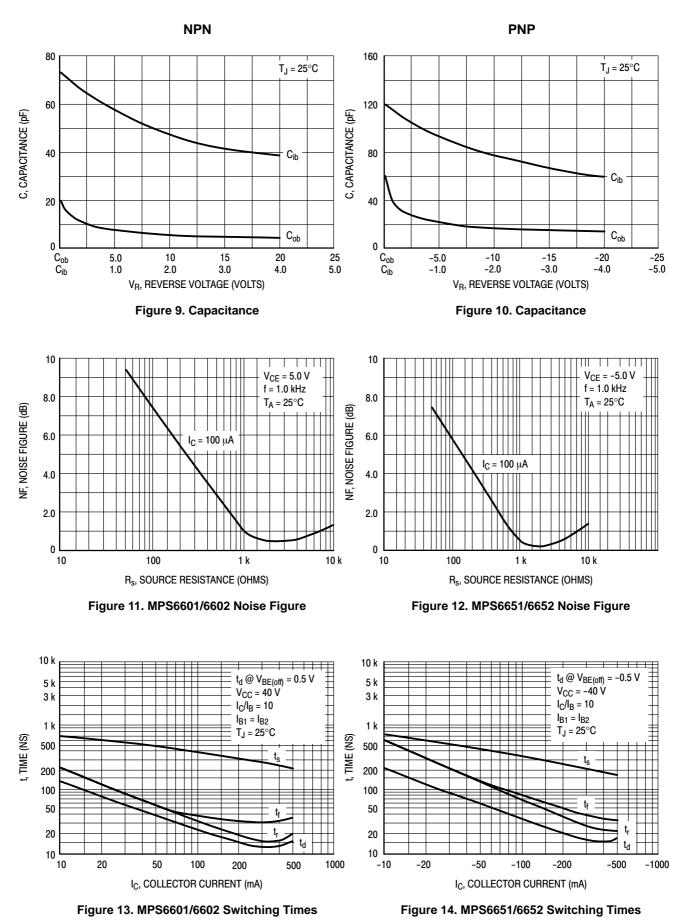
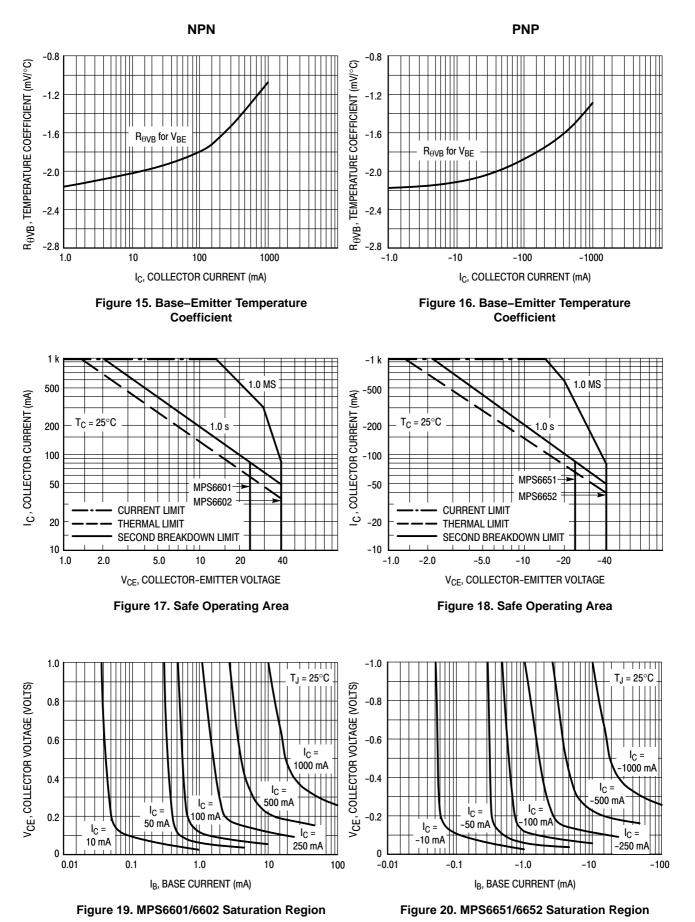


Figure 5. Current Gain Bandwidth Product

Figure 6. Current Gain Bandwidth Product

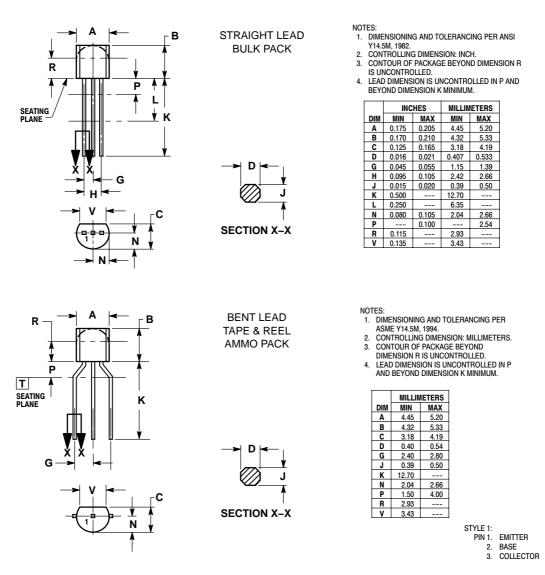






PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-11 ISSUE AM



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