TinyLogic HS 2-Input Exclusive-OR Gate

NC7S86

Description

The NC7S86 is a single 2–Input high performance CMOS Exclusive–OR Gate. Advanced Silicon Gate CMOS fabrication assures high speed and low power circuit operation over a broad V_{CC} range. ESD protection diodes inherently guard both inputs and output with respect to the V_{CC} and GND rails. Inputs are well buffered from the output to assure high noise immunity and reduced sensitivity to input edge rate.

Features

- Space Saving SC-74A and SC-88A 5-Lead Package
- Ultra Small MicroPakTM Leadless Package
- High Speed: $t_{PD} = 4.5$ ns Typ
- Low Quiescent Power: $I_{CC} < 1 \mu A$
- Balanced Output Drive: 2 mA I_{OL}, -2 mA I_{OH}
- Broad V_{CC} Operating Range: 2 V 6 V
- Balanced Propagation Delays
- Specified for 3 V Operation
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

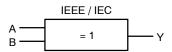
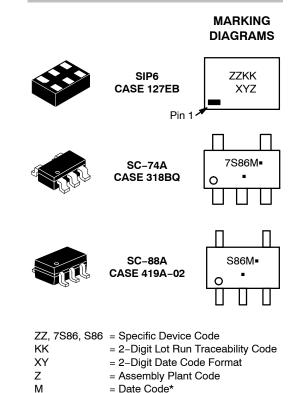


Figure 1. Logic Symbol



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*Date Code orientation and/or position may vary depending upon manufacturing location.

ORDERING INFORMATION

See detailed ordering, marking and shipping information in the package dimensions section on page 5 of this data sheet.

Pin Configurations

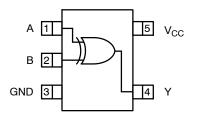


Figure 2. SC-88A and SC-74A (Top View)

PIN DESCRIPTIONS

Pin Name	Description
A, B	Input
Y	Output
NC	No Connect

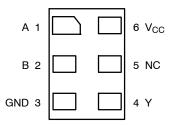


Figure 3. MicroPak (Top Through View)

FUNCTION TABLE (Y = $A \oplus B$)

Inp	Output	
А	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

H = HIGH Logic Level L = LOW Logic Level

ABSOLUTE MAXIMUM RATINGS

Symbol	Param	leter	Min	Max	Unit
V _{CC}	Supply Voltage		-0.5	6.5	V
I _{IK}	DC Input Diode Current	V _{IN} < 0 V	-	-20	mA
		$V_{IN} > V_{CC}$	-	+20	
V _{IN}	DC Input Voltage		-0.5	V _{CC} + 0.5	V
I _{OK}	DC Output Diode Current	V _{OUT} < 0 V	-	-20	mA
	V _{OUT} > V _{CC}		-	+20	
V _{OUT}	DC Output Voltage		-0.5	V _{CC} + 0.5	V
I _{OUT}	DC Output Source or Sink Current		-	±12.5	mA
$I_{CC} \text{ or } I_{GND}$	DC V _{CC} or Ground Current per Output Pin		-	±25	mA
T _{STG}	Storage Temperature		-65	+150	°C
TJ	Junction Temperature		-	+150	°C
ΤL	Lead Temperature (Soldering, 10 Seconds)		-	+260	°C
PD	Power Dissipation in Still Air	SC-74A	-	390	mW
		SC-88A	-	332	1
		MicroPak-6	_	812	1

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{CC}	Supply Voltage		2.0	6.0	V
V _{IN}	Input Voltage		0	V _{CC}	V
V _{OUT}	Output Voltage		0	V _{CC}	V
T _A	Operating Temperature		-40	+85	°C
t _r , t _f Input Rise and Fall Times	V _{CC} at 2.0 V	0	20	ns/V	
		V _{CC} at 3.0 V	0	20	
		V _{CC} at 4.5 V	0	10	
		V _{CC} at 6.0 V	0	5	
θ_{JA}	Thermal Resistance	SC-74A	-	320	°C/W
		SC-88A	-	377	
		MicroPak-6	-	154	

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability. 1. Unused inputs must be held HIGH or LOW. They may not float.

DC ELECTICAL CHARACTERISTICS

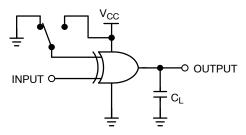
				-	Γ _A = +25°C	;	T _A = -40	to +85°C	
Symbol	Parameter	V _{CC} (V)	Conditions	Min	Тур	Max	Min	Max	Unit
V _{IH}	HIGH Level Input Voltage	2.0 3.0 – 6.0		1.50 0.7 V _{CC}	-		1.50 0.7 V _{CC}	-	V
V _{IL}	LOW Level Input Voltage	2.0 3.0 – 6.0				0.50 0.3 V _{CC}		0.50 0.3 V _{CC}	V
V _{OH}	HIGH Level Output Voltage	2.0 3.0 4.5 6.0		1.90 2.90 4.40 5.90	2.0 3.0 4.5 6.0	- - - -	1.90 2.90 4.40 5.90	- - - -	V
		3.0 4.5 6.0	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $I_{OH} = -1.3 \text{ mA}$ $I_{OH} = -2.0 \text{ mA}$ $I_{OH} = -2.6 \text{ mA}$	2.68 4.18 5.68	2.85 4.35 5.85		2.63 4.13 5.63		V
V _{OL}	LOW Level Output Voltage	2.0 3.0 4.5 6.0	$ I_{OL} = 20 \ \mu A \\ V_{IN} = V_{IH} \ or \ V_{IL} $	- - - -	0.0 0.0 0.0 0.0	0.10 0.10 0.10 0.10 0.10	- - - -	0.10 0.10 0.10 0.10	V
		3.0 4.5 6.0	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $I_{OL} = 1.3 \text{ mA}$ $I_{OL} = 2.0 \text{ mA}$ $I_{OL} = 2.6 \text{ mA}$	- - -	0.1 0.1 0.1	0.26 0.26 0.26	- - -	0.33 0.33 0.33	V
I _{IN}	Input Leakage Current	6.0	$V_{IN} = V_{CC}, \text{ GND}$	-	-	±0.1	-	±1.0	μA
I _{CC}	Quiescent Supply Current	6.0	$V_{IN} = V_{CC}, \text{ GND}$	-	-	1.0	-	10.0	μA

AC ELECTRICAL CHARACTERISTICS

					Γ _A = +25°C	;	T _A = -40	to +85°C	
Symbol	Parameter	V _{CC} (V)	Conditions	Min	Тур	Max	Min	Max	Unit
t _{PLH} ,	Propagation Delay (Figure 4, 6)	5.0	C _L = 15 pF	-	4.5	17	-	-	ns
^t PHL		2.0 3.0 4.5 6.0	C _L = 50 pF	- - -	22 12 8.5 7	100 27 20 17	- - - -	125 35 25 21	ns
t _{TLH} ,	Output Transition Time	5.0	C _L = 15 pF	-	3	8	-	-	ns
t _{THL}	(Figure 4, 6)	2.0 3.0 4.5 6.0	C _L = 50 pF	- - - -	25 16 11 9	125 35 25 21	- - - -	155 45 31 26	ns
C _{IN}	Input Capacitance	Open		-	2	10	-	10	pF
C _{PD}	Power Dissipation Capacitance (Figure 5)	5.0	(Note 2)	-	8	-	_	-	pF

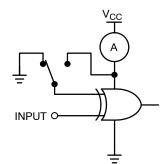
2. C_{PD} is defined as the value of the internal equivalent capacitance which is derived from dynamic operating current consumption (I_{CCD}) at no output loading and operating at 50% duty cycle. C_{PD} is related to I_{CCD} dynamic operating current by the expression: I_{CCD} = (C_{PD}) (V_{CC}) (f_{IN}) + (I_{CC}static).

AC Loading and Waveforms



 C_L includes load and stray capacitance Input PRR = 1.0 MHz; t_W = 500 ns





Input = AC Waveform; PRR = Variable; Duty Cycle = 50%.

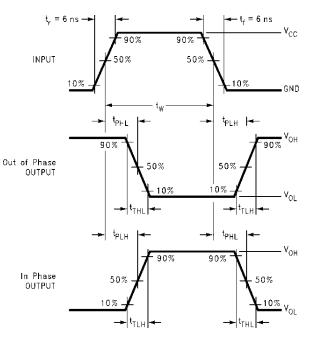




Figure 6. AC Waveforms

ORDERING INFORMATION

Order Number	Top Mark	Package Description	Shipping [†]
NC7S86M5X	7S86	SC-74A	3000 / Tape & Reel
NC7S86P5X	S86	SC-88A	3000 / Tape & Reel
NC7S86L6X	ZZ	SIP6, MicroPak	5000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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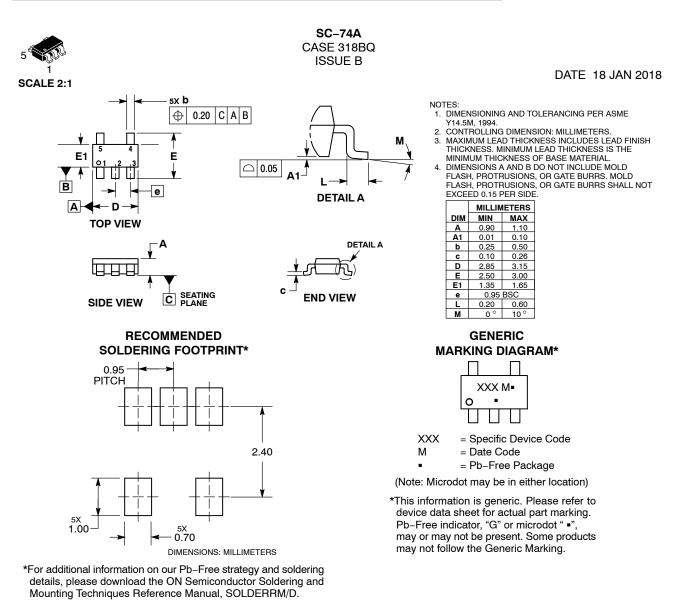


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