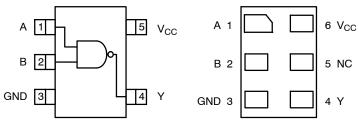
## TinyLogic ULP-A NAND Gate

# NC7SP00

The NC7SP00 is a single 2-input NAND gate in tiny footprint packages. The device is designed to operate for  $V_{CC}$  = 0.9 V to 3.6 V.

## Features

- Designed for 0.9 V to 3.6 V  $V_{CC}$  Operation
- 2.4 ns t<sub>PD</sub> at 3.3 V (Typ)
- Inputs/Outputs Over-Voltage Tolerant up to 3.6 V
- I<sub>OFF</sub> Supports Partial Power Down Protection
- Source/Sink 2.6 mA at 3.3 V
- Available in SC–88A and MicroPak<sup>™</sup> Packages
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant



SC-88A

MicroPak

Figure 1. Pinout Diagrams (Top Views)

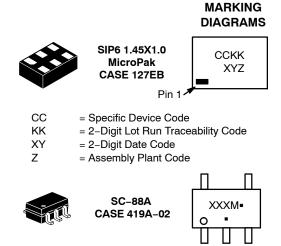


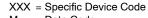
Figure 2. Logic Symbol

### **PIN ASSIGNMENT**

Pin	SC88A	MicroPak
1	А	A
2	В	В
3	GND	GND
4	Y	Y
5	V <sub>CC</sub>	N.C.
6	-	V <sub>CC</sub>

N.C. = No Connect





- M = Date Code
- = Pb-Free Package

## **ORDERING INFORMATION**

See detailed ordering, marking and shipping information on page 6 of this data sheet.

## FUNCTION TABLE

Ing	Output	
А	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

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## MAXIMUM RATINGS

Symbol	Characteristi	cs	Value	Unit
V <sub>CC</sub>	DC Supply Voltage		-0.5 to +4.3	V
V <sub>IN</sub>	DC Input Voltage		-0.5 to +4.3	V
V <sub>OUT</sub>	DC Output Voltage	Active–Mode (High or Low State) Tri–State Mode (Note 1) Power–Down Mode (V <sub>CC</sub> = 0 V)	-0.5 to V <sub>CC</sub> + 0.5 -0.5 to +4.3 -0.5 to +4.3	V
I <sub>IK</sub>	DC Input Diode Current	V <sub>IN</sub> < GND	-50	mA
Ι <sub>ΟΚ</sub>	DC Output Diode Current	V <sub>OUT</sub> < GND	-50	mA
IOUT	DC Output Source/Sink Current		±50	mA
I <sub>CC</sub> or I <sub>GND</sub>	DC Supply Current per Supply Pin or Ground	Pin	±50	mA
T <sub>STG</sub>	Storage Temperature Range		-65 to +150	°C
ΤL	Lead Temperature, 1 mm from Case for 10 Se	econds	260	°C
TJ	Junction Temperature Under Bias		+150	°C
$\theta_{JA}$	Thermal Resistance (Note 2)	SC-88A MicroPak	377 154	°C/W
P <sub>D</sub>	Power Dissipation in Still Air	SC-88A MicroPak	332 812	mW
MSL	Moisture Sensitivity		Level 1	-
F <sub>R</sub>	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	-
$V_{\text{ESD}}$	ESD Withstand Voltage (Note 3)	Human Body Model Charged Device Model	2000 1000	V
I <sub>Latchup</sub>	Latchup Performance (Note 4)		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

 Applicable to devices with outputs that may be tri-stated.
 Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow per JESD51-7.
 HBM tested to EIA / JESD22-A114-A. CDM tested to JESD22-C101-A. JEDEC recommends that ESD qualification to EIA/JESD22-A115A (Machine Model) be discontinued.

4. Tested to EIA/JÉSD78 Class II.

#### **RECOMMENDED OPERATING CONDITIONS**

Symbol	Parameter		Min	Max	Unit
V <sub>CC</sub>	Positive DC Supply Voltage		0.9	3.6	V
V <sub>IN</sub>	DC Input Voltage		0	3.6	V
V <sub>OUT</sub>	DC Output Voltage	Active–Mode (High or Low State) Tri–State Mode (Note 1) Power–Down Mode (V <sub>CC</sub> = 0 V)	0 0 0	V <sub>CC</sub> 3.6 3.6	
T <sub>A</sub>	Operating Temperature Range		-40	+85	°C
t <sub>r</sub> , t <sub>f</sub>	Input Transition Rise and Fall Time	$V_{CC}=3.3~V\pm0.3~V$	0	10	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

## DC ELECTRICAL CHARACTERISTICS

				T	A = 25°	с	T <sub>A</sub> = -40°0	C to +85°C	
Symbol	Parameter	Condition	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit
V <sub>IH</sub>	High-Level Input		0.9	-	0.5	-	-	-	V
	Voltage		1.1 to 1.3	$0.65 \times V_{CC}$	-	-	0.65 x V <sub>CC</sub>	-	
			1.4 to 1.6	0.65 x V <sub>CC</sub>	-	_	0.65 x V <sub>CC</sub>	-	
		1.65 to 1.95	$0.65 \times V_{CC}$	-	-	0.65 x V <sub>CC</sub>	-		
			2.3 to 2.7	1.6	-	_	1.6	-	
			3.0 to 3.6	2.1	-	-	2.1	-	
VIL	Low-Level Input		0.9	-	0.5	-	-	-	V
	Voltage		1.1 to 1.3	-	-	$0.35 \times V_{CC}$	-	$0.35 \times V_{CC}$	
			1.4 to 1.6	-	-	$0.35 \times V_{CC}$	-	$0.35 \times V_{CC}$	
			1.65 to 1.95	-	-	$0.35 \times V_{CC}$	-	$0.35 \times V_{CC}$	
			2.3 to 2.7	-	-	0.7	-	0.7	
			3.0 to 3.6	-	-	0.9	-	0.9	
V <sub>OH</sub>	High-Level Output	$V_{IN} = V_{IH} \text{ or } V_{IL}$							V
	Voltage	I <sub>OH</sub> = -20 μA	0.9	-	V <sub>CC</sub> - 0.1	-	_	-	
			1.1 to 1.3	V <sub>CC</sub> – 0.1	-	-	V <sub>CC</sub> – 0.1	-	
			1.4 to 1.6	V <sub>CC</sub> - 0.1	-	-	V <sub>CC</sub> – 0.1	-	
			1.65 to 1.95	V <sub>CC</sub> - 0.1	-	-	V <sub>CC</sub> – 0.1	-	
			2.3 to 2.7	V <sub>CC</sub> – 0.1	-	-	V <sub>CC</sub> - 0.1	-	
			3.0 to 3.6	V <sub>CC</sub> – 0.1	-	-	V <sub>CC</sub> - 0.1	-	
		I <sub>OH</sub> = -0.5 mA	1.1 to 1.3	$0.75 \times V_{CC}$	-	-	0.70 x V <sub>CC</sub>	-	
		I <sub>OH</sub> = -1 mA	1.4 to 1.6	1.07	-	-	0.99	-	
		I <sub>OH</sub> = -1.5 mA	1.65 to 1.95	1.24	-	-	1.22	-	
		I <sub>OH</sub> = -2.1 mA	2.3 to 2.7	1.95	-	-	1.87	-	
		I <sub>OH</sub> = -2.6 mA	3.0 to 3.6	2.61	-	-	2.55	-	
V <sub>OL</sub>	Low-Level Output	$V_{IN} = V_{IH} \text{ or } V_{IL}$							V
	Voltage	I <sub>OL</sub> = 20 μA	0.9	-	0.1	-	-	-	
			1.1 to 1.3	-	-	0.1	-	0.1	
			1.4 to 1.6	-	-	0.1	-	0.1	
			1.65 to 1.95	-	-	0.1	-	0.1	
			2.3 to 2.7	-	-	0.1	-	0.1	
			3.0 to 3.6	-	-	0.1	-	0.1	
		I <sub>OL</sub> = 0.5 mA	1.1 to 1.3	-	-	$0.3 \times V_{CC}$	-	$0.3 \times V_{CC}$	
		I <sub>OL</sub> = 1 mA	1.4 to 1.6	-	-	0.31	-	0.37	
		I <sub>OL</sub> = 1.5 mA	1.65 to 1.95	-	-	0.31	-	0.35	
		I <sub>OL</sub> = 2.1 mA	2.3 to 2.7	_	_	0.31	-	0.33	
		I <sub>OL</sub> = 2.6 mA	3.0 to 3.6	-	-	0.31	-	0.33	
I <sub>IN</sub>	Input Leakage Current	V <sub>IN</sub> = 0 V to 3.6 V	0.9 to 3.6	-	-	±0.1	-	±0.5	μA
I <sub>OFF</sub>	Power Off Leakage Current		0	-	-	0.5	-	0.5	μA
I <sub>CC</sub>	Quiescent Supply Current	$V_{IN} = V_{CC}$ or GND	0.9 to 3.6	-	-	0.9	-	0.9	μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

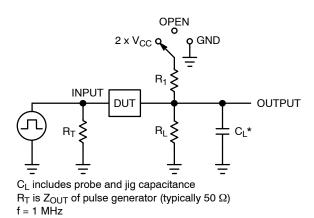
#### AC ELECTRICAL CHARACTERISTICS

				٦	Γ <sub>A</sub> = 25°0	2	T <sub>A</sub> = -40°C	C to +85°C		
Symbol	Parameter	Condition	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Мах	Unit	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay, (A or B) to Y (Figures 3 and 4)		$R_L = 1 M\Omega$ , $C_L = 10 pF$	0.9	-	39.6	-	-	-	ns
			1.10 to 1.30	-	10.1	23.3	-	31.0		
			1.40 to 1.60	-	5.5	12.0	-	14.0		
			1.65 to 1.95	-	4.5	10.0	-	12.0		
			2.3 to 2.7	-	3.8	7.0	-	8.0		
			3.0 to 3.6	-	2.4	6.0	-	7.0		
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay,	B) to Y	0.9	-	41.5	-	-	-	ns	
	(A or B) to Y (Figures 3 and 4)		1.10 to 1.30	-	10.9	24.5	-	34.0		
			1.40 to 1.60	-	6.1	13.0	-	16.0		
			1.65 to 1.95	-	4.7	10.0	-	12.0		
			2.3 to 2.7	-	3.8	7.0	-	8.0		
			3.0 to 3.6	-	2.6	6.0	-	7.0		
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay,	$R_L$ = 1 M $\Omega$ , $C_L$ = 30 pF	0.9	-	47.3	-	-	-	ns	
	(A or B) to Y (Figures 3 and 4)		1.10 to 1.30	-	13.6	28.3	-	43.0		
			1.40 to 1.60	-	7.7	16.0	-	18.0		
			1.65 to 1.95	-	5.6	12.0	-	14.0		
			2.3 to 2.7	-	4.0	9.0	-	10.0		
			3.0 to 3.6	-	3.1	7.0	-	9.0		

#### **CAPACITIVE CHARACTERISTICS**

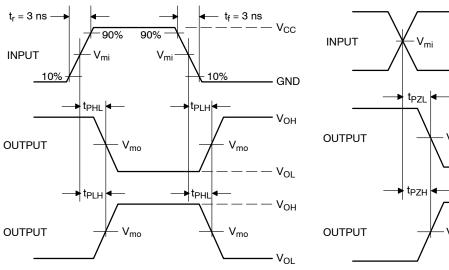
Symbol	Parameter	Parameter Test Condition		Unit
C <sub>IN</sub>	Input Capacitance	V <sub>CC</sub> = 0 V	2.0	pF
C <sub>OUT</sub>	Output Capacitance	V <sub>CC</sub> = 0 V	4.0	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 5)	f = 10 MHz, V_{CC} = 0.9 to 3.6 V, V <sub>IN</sub> = 0 V or V <sub>CC</sub>	6.0	pF

5.  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation  $I_{CC(OPR)} = C_{PD} \bullet V_{CC} \bullet f_{in} + I_{CC}$ .  $C_{PD}$  is used to determine the no–load dynamic power consumption:  $P_D = C_{PD} \bullet V_{CC}^2 \bullet f_{in} + I_{CC} \bullet V_{CC}$ .



Test	Switch Position
t <sub>PLH</sub> / t <sub>PHL</sub>	Open
t <sub>PLZ</sub> / t <sub>PZL</sub>	2 x V <sub>CC</sub>
t <sub>PHZ</sub> / t <sub>PZH</sub>	GND

Figure 3. Test Circuit



INPUT	V <sub>mi</sub>	V <sub>cc</sub>	
	_/ \		
		-V <sub>CI</sub>	С
OUTPUT	V <sub>mo</sub>	V <sub>OL</sub> +V <sub>Y</sub> V <sub>OL</sub> +V <sub>Y</sub>	
		t <sub>PHZ</sub> V <sub>OH</sub>	
		~0 V	,

V <sub>CC</sub> , V	V <sub>mi</sub> , V	V <sub>mo</sub> , V	V <sub>Y</sub> , V
0.9	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.1
1.1 to 1.3	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.1
1.4 to 1.6	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.1
1.65 to 1.95	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.15
2.3 to 2.7	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.15
3.0 to 3.6	1.5	1.5	0.3

Figure 4. Switching Waveforms

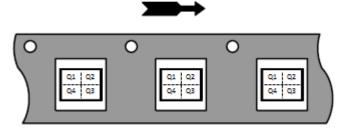
### **ORDERING INFORMATION**

Device	Package	Marking	Pin 1 Orientation (See below)	Shipping <sup>†</sup>
NC7SP00P5X	SC-88A	P00	Q4	3000 / Tape & Reel
NC7SP00L6X	MicroPak	J4	Q4	5000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

## Pin 1 Orientation in Tape and Reel

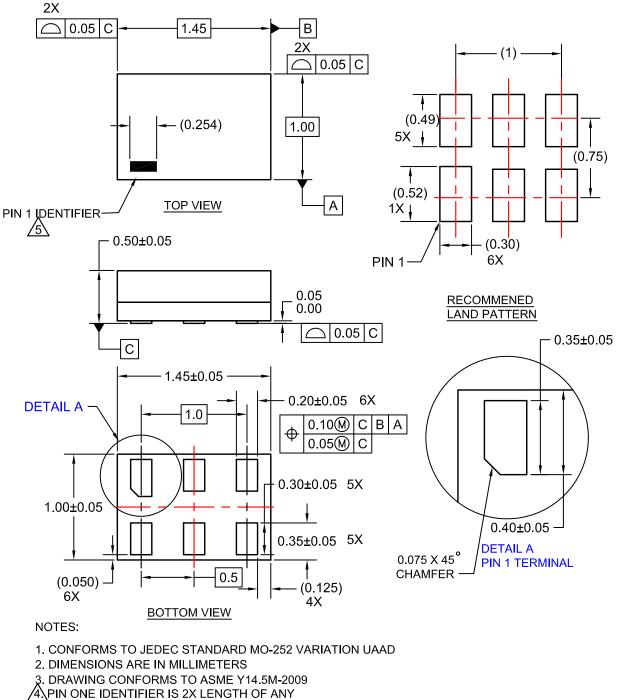




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#### PACKAGE DIMENSIONS

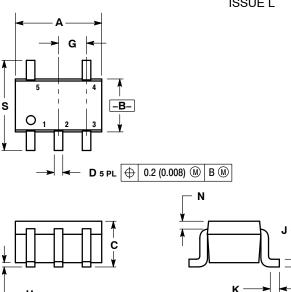
SIP6 1.45X1.0 CASE 127EB ISSUE O



OTHER LINE IN THE MARK CODE LAYOUT.

#### PACKAGE DIMENSIONS

SC-88A (SC-70-5/SOT-353) CASE 419A-02 ISSUE L



NOTES:

1. DIMENSIONING AND TOLERANCING

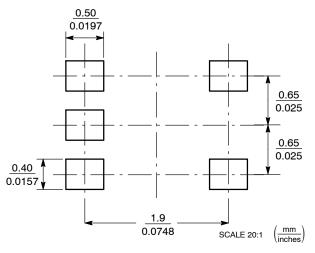
PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

 419A–01 OBSOLETE. NEW STANDARD 419A–02

 DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	INC	HES	MILLIM	ETERS
DIM	MIN	MAX	MIN	MAX
Α	0.071	0.087	1.80	2.20
В	0.045	0.053	1.15	1.35
C	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026	BSC	0.65 BSC	
Н		0.004		0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008	0.008 REF		REF
S	0.079	0.087	2.00	2.20

#### SOLDER FOOTPRINT



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