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## TinyLogic ULP-A 2-Input AND Gate

#### NC7SV08

The NC7SV08 is a single 2-Input AND Gate in tiny footprint packages. The device is designed to operate for  $V_{CC} = 0.9 \text{ V}$  to 3.6 V.

#### **Features**

- Designed for 0.9 V to 3.6 V V<sub>CC</sub> Operation
- 1.6 ns t<sub>PD</sub> at 3.3 V (Typ)
- Inputs/Outputs Over-Voltage Tolerant up to 3.6 V
- I<sub>OFF</sub> Supports Partial Power Down Protection
- Source/Sink 24 mA at 3.3 V
- Available in SC-88A and MicroPak<sup>TM</sup> Packages
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

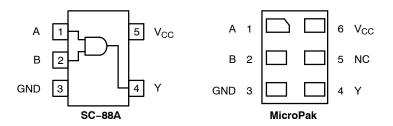


Figure 1. Pinout Diagrams (Top Views)

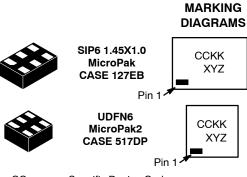


Figure 2. Logic Symbol

# ON

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CC = Specific Device Code

KK = 2-Digit Lot Run Traceability CodeXY = 2-Digit Date Code

Z = Assembly Plant Code



XXX = Specific Devic Code

M = Date Code

Pb-Free Package

#### **ORDERING INFORMATION**

See detailed ordering, marking and shipping information on page 6 of this data sheet.

#### **PIN ASSIGNMENT**

Pin	SC-88A	MicroPak
1	А	А
2	В	В
3	GND	GND
4	Υ	Υ
5	V <sub>CC</sub>	N.C.
6	-	V <sub>CC</sub>

N.C. = No Connect

#### **FUNCTION TABLE**

Inp	Output Y = AB	
Α	В	Υ
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

#### **MAXIMUM RATINGS**

Symbol	Cha	Value	Unit	
V <sub>CC</sub>	DC Supply Voltage		-0.5 to +4.3	V
V <sub>IN</sub>	DC Input Voltage		-0.5 to +4.3	V
V <sub>OUT</sub>	DC Output Voltage	Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode (V <sub>CC</sub> = 0 V)	-0.5 to V <sub>CC</sub> + 0.5 -0.5 to +4.3 -0.5 to +4.3	V
I <sub>IK</sub>	DC Input Diode Current	V <sub>IN</sub> < GND	-50	mA
I <sub>OK</sub>	DC Output Diode Current	V <sub>OUT</sub> < GND	-50	mA
I <sub>OUT</sub>	DC Output Source/Sink Current		±50	mA
I <sub>CC</sub> or I <sub>GND</sub>	DC Supply Current per Supply Pin	or Ground Pin	±50	mA
T <sub>STG</sub>	Storage Temperature Range		-65 to +150	°C
TL	Lead Temperature, 1 mm from Case	e for 10 Seconds	260	°C
TJ	Junction Temperature Under Bias		+150	°C
$\theta_{\sf JA}$	Thermal Resistance (Note 2)	SC-88A MicroPak	377 154	°C/W
$P_{D}$	Power Dissipation in Still Air	SC-88A MicroPak	332 812	mW
MSL	Moisture Sensitivity		Level 1	_
F <sub>R</sub>	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	-
V <sub>ESD</sub>	ESD Withstand Voltage (Note 3)	Human Body Model Charged Device Model	4000 2000	V
I <sub>Latchup</sub>	Latchup Performance (Note 4)		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- Applicable to devices with outputs that may be tri-stated.
   Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow per JESD51-7.
   HBM tested to EIA / JESD22-A114-A. CDM tested to JESD22-C101-A. JEDEC recommends that ESD qualification to EIA/JESD22-A115A (Machine Model) be discontinued.
- 4. Tested to EIA/JESD78 Class II.

#### **RECOMMENDED OPERATING CONDITIONS**

Symbol	Paran	neter	Min	Max	Unit
V <sub>CC</sub>	Positive DC Supply Voltage		0.9	3.6	V
V <sub>IN</sub>	DC Input Voltage		0	3.6	V
V <sub>OUT</sub>	DC Output Voltage	Active–Mode (High or Low State) Tri–State Mode (Note 1) Power–Down Mode ( $V_{CC}$ = 0 V)	0 0 0	V <sub>CC</sub> 3.6 3.6	
T <sub>A</sub>	Operating Temperature Range		-40	+85	°C
t <sub>r</sub> , t <sub>f</sub>	Input Transition Rise and Fall Time	$V_{CC}$ = 3.3 V $\pm$ 0.3 V	0	10	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

#### DC ELECTRICAL CHARACTERISTICS

				T <sub>A</sub> = 25°C			$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$					
Symbol	Parameter	Condition	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit			
V <sub>IH</sub>	High-Level Input		0.9	-	0.5	-	-	-	V			
	Voltage		1.1 to 1.3	0.65 x V <sub>CC</sub>	-	-	0.65 x V <sub>CC</sub>	-				
			1.4 to 1.6	0.65 x V <sub>CC</sub>	-	-	0.65 x V <sub>CC</sub>	-				
			1.65 to 1.95	0.65 x V <sub>CC</sub>	-	-	0.65 x V <sub>CC</sub>	_				
			2.3 to <2.7	1.6	-	-	1.6	-				
			2.7 to 3.6	2.0	-	-	2.0	-				
$V_{IL}$	Low-Level Input		0.9	-	0.5	-	-	-	V			
	Voltage		1.1 to 1.3	-	-	0.35 x V <sub>CC</sub>	-	0.35 x V <sub>CC</sub>				
			1.4 to 1.6	-	-	0.35 x V <sub>CC</sub>	-	0.35 x V <sub>CC</sub>				
			1.65 to 1.95	-	-	0.35 x V <sub>CC</sub>	-	0.35 x V <sub>CC</sub>				
			2.3 to <2.7	-	-	0.7	-	0.7				
			2.7 to 3.6	-	-	0.8	-	0.8				
V <sub>OH</sub>	High-Level Output	$V_{IN} = V_{IH}$ or $V_{IL}$							V			
	Voltage	I <sub>OH</sub> = -100 μA	0.9	_	V <sub>CC</sub> – 0.1	_	-	-				
				1.1 to 1.3	V <sub>CC</sub> – 0.1	-	-	V <sub>CC</sub> - 0.1	-			
			1.4 to 1.6	V <sub>CC</sub> – 0.1	-	-	V <sub>CC</sub> - 0.1	_				
			1.65 to 1.95	V <sub>CC</sub> – 0.2	_	-	V <sub>CC</sub> - 0.2	-				
					2.3 to <2.7	V <sub>CC</sub> – 0.2	_	-	V <sub>CC</sub> – 0.2	-		
			2.7 to 3.6	V <sub>CC</sub> – 0.2	_	-	V <sub>CC</sub> - 0.2	-				
					I <sub>OH</sub> = −2 mA	1.1 o 1.3	0.75 x V <sub>CC</sub>	-	-	0.75 x V <sub>CC</sub>	-	
		$I_{OH} = -4 \text{ mA}$	1.4 to 1.6	0.75 x V <sub>CC</sub>	_	-	0.75 x V <sub>CC</sub>	-				
		I <sub>OH</sub> = -6 mA	1.65 to 1.95	1.25	-	-	1.25	_				
			2.3 to <2.7	2.0	-	-	2.0	-				
		I <sub>OH</sub> = -12 mA	2.3 to <2.7	1.8	-	-	1.8	-				
		2.7 to 3.6	2.2	-	-	2.2	_	1				
		I <sub>OH</sub> = -18 mA	2.3 to <2.7	1.7	-	-	1.7	-				
			2.7 to 3.6	2.4	-	-	2.4	-				
<u> </u>		I <sub>OH</sub> = -24 mA	2.7 to 3.6	2.2	_	-	2.2	-				

#### DC ELECTRICAL CHARACTERISTICS (continued)

				T <sub>A</sub> = 25°C			$T_A = -40^{\circ}$	°C to +85°C	
Symbol	Parameter	Condition	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit
V <sub>OL</sub>	Low-Level	$V_{IN} = V_{IH}$ or $V_{IL}$							V
	Output Voltage	I <sub>OL</sub> = 100 μA	0.9	-	0.1	-	_	-	
			1.1 to 1.3	-	-	0.1	-	0.1	
			1.4 to 1.6	-	-	0.1	-	0.1	
			1.65 to 1.95	-	-	0.2	-	0.2	
			2.3 to < 2.7	-	-	0.2	-	0.2	
			2.7 to 3.6	-	-	0.2	-	0.2	
		I <sub>OL</sub> = 2 mA	1.1 o 1.3	-	_	0.25 x V <sub>CC</sub>	-	0.25 x V <sub>CC</sub>	
		I <sub>OL</sub> = 4 mA	1.4 to 1.6	-	-	0.25 x V <sub>CC</sub>	-	0.25 x V <sub>CC</sub>	
		I <sub>OL</sub> = 6 mA	1.65 to 1.95	-	_	0.3	-	0.3	
		I <sub>OL</sub> = 12 mA	2.3 to <2.7	-	-	0.4	-	0.4	
			2.7 to 3.6	-	-	0.4	-	0.4	
		I <sub>OL</sub> = 18 mA	2.3 to <2.7	-	-	0.6	-	0.6	
			2.7 to 3.6	-	-	0.4	-	0.4	
		I <sub>OL</sub> = 24 mA	2.7 to 3.6	-	-	0.55	-	0.55	
I <sub>IN</sub>	Input Leakage Current	V <sub>IN</sub> = 0 V to 3.6 V	0.9 to 3.6	-	-	±0.1	-	±0.5	μΑ
l <sub>OFF</sub>	Power Off Leakage Current	V <sub>IN</sub> = 0 V to 3.6 V or V <sub>OUT</sub> = 0 V to 3.6 V	0	_	-	0.5	-	0.5	μΑ
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = V <sub>CC</sub> or GND	0.9 to 3.6	-	_	0.9	-	0.9	μΑ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

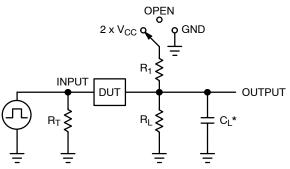
#### **AC ELECTRICAL CHARACTERISTICS**

				T <sub>A</sub> = 25°C		T <sub>A</sub> = -40°C to +85°C				
Symbol	Parameter	Condition	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit	
t <sub>PLH</sub> ,	t <sub>PLH</sub> , Propagation Delay, (A or B) to Y (Figures 3 and 4)	$R_L = 1 \text{ M}\Omega$ , $C_L = 15 \text{ pF}$	0.9	_	15.9	_	-	-	ns	
t <sub>PHL</sub>		$R_L = 2 \text{ k}\Omega$ , $C_L = 15 \text{ pF}$	1.1 to 1.3	_	6.8	11.6	-	14.6		
			,		1.4 to 1.6	_	3.6	6.0	-	7.2
		$R_L = 500 \ \Omega, \ C_L = 30 \ pF$	1.65 to 1.95	-	2.6	4.5	-	5.3		
			2.3 to 2.7	-	1.9	2.6	-	3.7		
			2.7 to 3.6	-	1.6	2.3	-	3.0		

#### **CAPACITIVE CHARACTERISTICS**

Symbol	Parameter	Test Condition	Typical (T <sub>A</sub> = 25°C)	Unit
C <sub>IN</sub>	Input Capacitance	V <sub>CC</sub> = 0 V	2.0	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 5)	f = 10 MHz, $V_{CC}$ = 0.9 to 3.6 V, $V_{IN}$ = 0 V or $V_{CC}$	8.0	pF

<sup>5.</sup>  $C_{PD}$  is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation  $I_{CC(OPR)} = C_{PD} \bullet V_{CC} \bullet f_{in} + I_{CC} \cdot C_{PD}$  is used to determine the no–load dynamic power consumption:  $P_D = C_{PD} \bullet V_{CC}^2 \bullet f_{in} + I_{CC} \bullet V_{CC}$ .

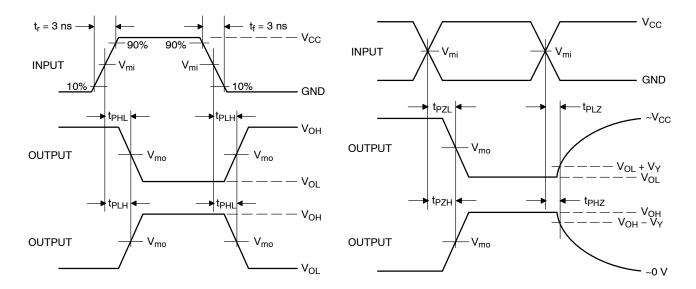


Test	Switch Position
t <sub>PLH</sub> / t <sub>PHL</sub>	Open
t <sub>PLZ</sub> / t <sub>PZL</sub>	2 x V <sub>CC</sub>
t <sub>PHZ</sub> / t <sub>PZH</sub>	GND

C<sub>L</sub> includes probe and jig capacitance

 $R_T$  is  $Z_{OUT}$  of pulse generator (typically 50  $\Omega$ ) f=1 MHz

Figure 3. Test Circuit



V <sub>CC</sub> , V	V <sub>mi</sub> , V	V <sub>mo</sub> , V	V <sub>Y</sub> , V
0.9	V <sub>CC</sub> /2	V <sub>CC</sub> /2	0.1
1.1 to 1.3	V <sub>CC</sub> /2	V <sub>CC</sub> /2	0.1
1.4 to 1.6	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.1
1.65 to 1.95	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.15
2.3 to 2.7	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.15
3.0 to 3.6	1.5	1.5	0.3

Figure 4. Switching Waveforms

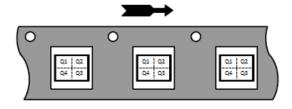
#### **ORDERING INFORMATION**

Device	Package	Marking	Pin 1 Orientation (See below)	Shipping <sup>†</sup>
NC7SV08P5X	SC-88A	V08	Q4	3000 / Tape & Reel
NC7SV08L6X	MicroPak	G3	Q4	5000 / Tape & Reel
NC7SV08FHX	MicroPak2	G3	Q4	5000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### Pin 1 Orientation in Tape and Reel

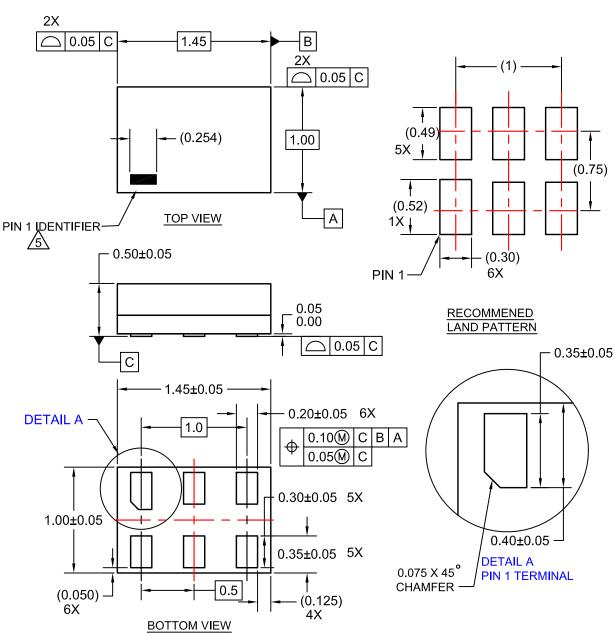
#### Direction of Feed



MicroPak is trademark of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries.

#### **PACKAGE DIMENSIONS**

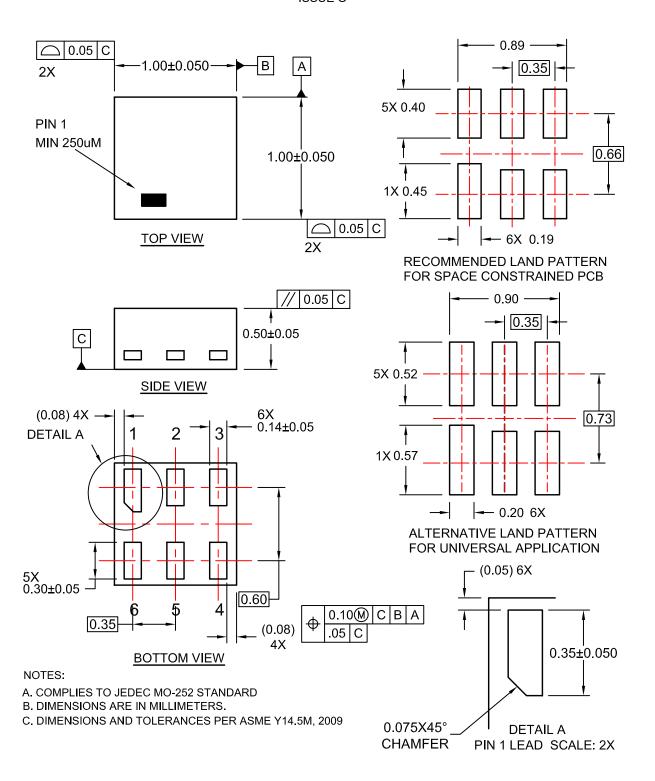
SIP6 1.45X1.0 CASE 127EB ISSUE O



- NOTES:
- 1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-2009
- 4.PIN ONE IDENTIFIER IS 2X LENGTH OF ANY
  - OTHER LINE IN THE MARK CODE LAYOUT.

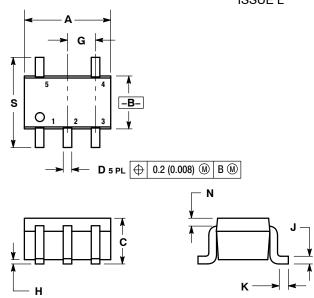
#### **PACKAGE DIMENSIONS**

## **UDFN6 1.0X1.0, 0.35P**CASE 517DP ISSUE O



#### PACKAGE DIMENSIONS

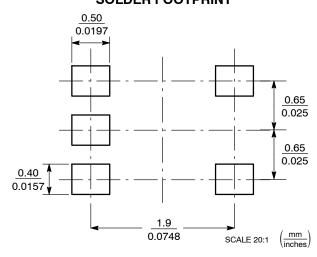
#### SC-88A (SC-70-5/SOT-353) CASE 419A-02 **ISSUE L**



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  CONTROLLING DIMENSION: INCH.
- 419A-01 OBSOLETE. NEW STANDARD 419A-02
- DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE

	INC	HES	MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.071	0.087	1.80	2.20	
В	0.045	0.053	1.15	1.35	
С	0.031	0.043	0.80	1.10	
D	0.004	0.012	0.10	0.30	
G	0.026	BSC	0.65 BSC		
Н		0.004		0.10	
J	0.004	0.010	0.10	0.25	
K	0.004	0.012	0.10	0.30	
N	0.008 REF		0.20 REF		
S	0.079	0.087	2.00	2.20	

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NLX1G11AMUTCG NLX1G97MUTCG 74LS38 74LVC32ADTR2G MC74HCT20ADTR2G NLV17SZ00DFT2G NLV17SZ02DFT2G
NLV74HC02ADR2G 74HC32S14-13 74LS133 74LVC1G32Z-7 M38510/30402BDA 74LVC1G86Z-7 74LVC2G08RA3-7
NLV74HC08ADTR2G NLV74HC14ADR2G NLV74HC20ADR2G NLX2G86MUTCG 5962-8973601DA 74LVC2G02HD4-7
NLU1G00AMUTCG 74LVC2G32RA3-7 74LVC2G00HD4-7 NL17SG02P5T5G 74LVC2G00HK3-7 74LVC2G86HK3-7
NLX1G99DMUTWG NLVVHC1G00DFT2G NLVHC1G08DFT2G NLV7SZ57DFT2G NLV74VHC04DTR2G NLV27WZ86USG
NLV27WZ00USG NLU1G86CMUTCG NLU1G08CMUTCG NL17SZ32P5T5G NL17SZ00P5T5G NL17SH02P5T5G 74AUP2G00RA3-7
NLV74HC02ADTR2G NLX1G332CMUTCG NL17SG86P5T5G NL17SZ05P5T5G