ON Semiconductor

Is Now



To learn more about onsemi™, please visit our website at www.onsemi.com

onsemi and ONSEMI. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/ or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application,

1.5 A, Very Low-Dropout (VLDO) Fast Transient **Response Regulator Series**

The NCP59150 series are high precision, very low dropout (VLDO), low ground current positive voltage regulators that are capable of providing an output current in excess of 1.5 A with a typical dropout voltage lower than 300 mV at 1.5 A load current. The devices are stable with ceramic output capacitors. This series consists of Adjustable output voltage and fixed voltage versions.

The NCP59150 series can withstand up to 18 V max input voltage. Internal protection features consist of output current limiting, built-in thermal shutdown and reverse output current protection. Logic level enable and error flag pins are available on the 5-pin and 8-pin versions.

The NCP59150 series Adjustable Voltage devices are available in D2PAK-5 and DFN8 packages, the Fixed Voltage option are available in D²PAK-5, D²PAK-3 and DFN8 packages.

Features

- Output Current in Excess of 1.5 A
- 300 mV Typical Dropout Voltage at 1.5 A
- Adjustable and Fixed Output Voltage Options
- Low Ground Current
- Fast Transient Response
- Stable with Ceramic Output Capacitor
- Logic Compatible Enable and Error Flag Pins
- Current Limit, Reverse Current and Thermal Shutdown Protection
- Operation up to 13.5 V Input Voltage
- NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable
- These are Pb-Free Devices

Applications

- Consumer and Industrial Equipment Point of Regulation
- Servers and Networking Equipment
- FPGA, DSP and Logic Power Supplies
- Switching Power Supply Post Regulation
- · Battery Chargers
- Functional Replacement for Industry Standard MIC29150, MIC39150, MIC37150

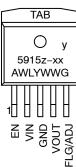


ON Semiconductor®

http://onsemi.com

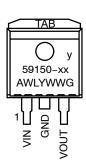
DIAGRAMS





MARKING







CASE 488AF



= Voltage Version = P (NCP), V (NCV) У = 1 (Fix Voltage), 2 (Adj) z = Assembly Location

L, WL = Wafer Lot Y, YY = Year W. WW = Work Week = Pb-Free Package

(*Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 9 of this data sheet.

TYPICAL APPLICATIONS

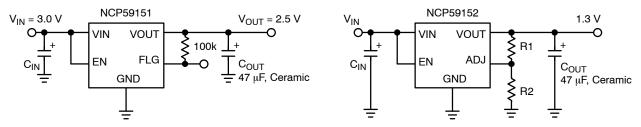


Figure 1. Fixed 2.5 Regulator with Error Flag

Figure 2. Adjustable Regulator

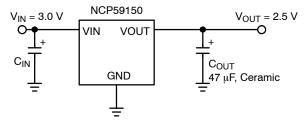


Figure 3. Fixed 2.5 Regulator in D²PAK-3 Package

PIN FUNCTION DESCRIPTION

Pin Number D2PAK-5	Pin Number D2PAK-3	Pin Number DFN8	Pin Name	Pin Function
1	-	2	EN	Enable Input: CMOS and TTL logic compatible. Logic high = enable; Logic low = shutdown.
2	1	3	VIN	Input voltage which supplies both the internal circuitry and the current to the output load.
3	2	1	GND	Ground
TAB	TAB	-	TAB	TAB is connected to ground.
4	3	6	VOUT Linear Regulator Output.	
5 (Fixed)	-	8	FLG	Error Flag Open collector output. Active-low indicates an output fault condition.
5 (Adj)	-	7 (Adj)	ADJ	Adjustable Regulator Feedback Input. Connect to output voltage resistor divider central node.
-	-	7 (Fixed)	VOUT SENSE	Fixed Voltage Regulator Feedback Input. Connect to output voltage node.
-	-	EP	EXPOSED PAD	PAD for removing heat from the device. Must be connected to GND.
-	_	4, 5	NC	Not internally connected.

ABSOLUTE MAXIMUM RATINGS

Symbol	Rating	Value	Unit	
V _{IN}	Supply Voltage		0 to 18	V
V _{EN}	Enable Input Voltage		0 to 18	V
V _{FLG}	Error Flag Open Collector Output Maximum V	0 to 18	V	
V _{OUT} – V _{IN}	Reverse $V_{OUT} - V_{IN}$ Voltage (EN = Shutdown	0 to 6.5	V	
P _D	Power Dissipation (Notes 2 and 5)		Internally Limited	
T _J	Junction Temperature		$-40 \le T_{J} \le +125$	°C
T _S	Storage Temperature		$-65 \le T_{J} \le +150$	°C
	ESD Rating (Notes 3 and 4)	Human Body Model Machine Model	2000 200	V

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

NOTE: All voltages are referenced to GND pin unless otherwise noted.

- 1. The ENABLE pin input voltage must be ≤ 0.8 V or Vin must be connected to ground potential.
- 2. $P_{D(max)} = (T_{J(max)} T_A) / R_{\theta JA}$, where $R_{\theta JA}$ depends upon the printed circuit board layout. 3. Devices are ESD sensitive. Handling precautions recommended..
- This device series incorporates ESD protection and is tested by the following methods: ESD Human Body Model (HBM) tested per AEC-Q100-002 (EIA/JESD22-A114C) ESD Machine Model (MM) tested per AEC-Q100-003 (EIA/JESD22-A115C) This device contains latch-up protection and exceeds 100 mA per JEDEC Standard JESD78.
- 5. This protection is not guaranteed outside the Recommended Operating Conditions.

RECOMMENDED OPERATING CONDITIONS (Note 6)

Symbol	Rating	Value	Unit
V _{IN}	Supply Voltage	2.24 to 13.5	V
V _{EN}	Enable Input Voltage	0 to 13.5	V
V _{FLG}	Error Flag Open Collector Voltage	0 to 13.5	V
TJ	Junction Temperature	$-40 \le T_{J} \le +125$	°C

^{6.} The device is not guaranteed to function outside it's Recommended operating conditions.

ELECTRICAL CHARACTERISTICS

 $T_{J} = 25^{\circ}C \text{ with } V_{IN} = V_{OUT \ nominal} + 1 \text{ V}; V_{EN} = V_{IN}; I_{L} = 10 \text{ mA}; \text{ bold values indicate } -40^{\circ}C < T_{J} < +125^{\circ}C, \text{ unless noted.}$

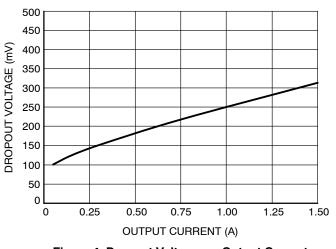
Parameter	Conditions	Min	Тур	Max	Unit
Output Voltage Accuracy	I _L = 10 mA	-1		1	%
DFN package	10 mA < I_{OUT} < 1.5 A , $V_{OUT \ nominal}$ + 1 \leq V_{IN} \leq 13.5 V	-2		2	%
Output Voltage Accuracy	IL = 10 mA	-1.5		1.5	%
D2PAK package	10 mA < I_{OUT} < 1.5 A , $V_{OUT \ nominal}$ + 1 \leq V_{IN} \leq 13.5 V	-2.5		2.5	%
Output Voltage Line Regulation	$V_{IN} = V_{OUT \text{ nominal}} + 1.0 \text{ V to } 13.5 \text{ V; } I_L = 10 \text{ mA}$		0.02	0.5	%
Output Voltage Load Regulation	I _L = 10 mA to 1.5 A		0.2	1.0	%
V _{IN} – V _{OUT} Dropout Voltage (Note 7)	I _L = 750 mA		175	350	mV
	I _L = 1.5 A		300	500	mV
Ground Pin Current (Note 8)	I _L = 1.5 A		40	60 80	mA
Ground Pin Current in Shutdown	$V_{EN} \leq 0.5 \text{ V}$		1.0	5.0	μΑ
Overload Protection Current Limit	V _{OUT} = 0 V		2.0	3.0	Α
Start-up Time	V_{EN} = V_{IN} , V_{OUT} nominal = 2.5 V, I_{OUT} = 10 mA, C_{OUT} = 47 μF		100	500	μs
Output Voltage Start-up Slope Fixed Voltage Devices	V_{EN} = V_{IN} , I_{OUT} = 10 mA, C_{OUT} = 47 μ F (Note 9)		40	200	μs/V
ENABLE INPUT					
Enable Input Signal Levels	Regulator Enable	1.8			V
	Regulator Shutdown			0.8	V
Enable Pin Input Current	V _{EN} ≤ 0.8 V (Regulator Shutdown)			2.0 4.0	μΑ
	$6.5 \text{ V} > \text{V}_{\text{EN}} \ge 1.8 \text{ V} \text{ (Regulator enable)}$	1.0	15	30 40	μΑ
FLAG OUTPUT		•			
I _{FLG(leak)}	V _{oh} = 13.5 V, Flag OFF			1.0 2.0	μΑ
$V_{FLG(LO)}$	V _{IN} = 2.24 V, I _{FLG} = 1 mA, Flag ON		210	400 500	mV
V_{FLG}	Low Threshold, % of particular V _{OUT}	93	95		%
	Hysteresis, % of particular V _{OUT}		2		%
	High Threshold, % of particular V _{OUT}		97	99.2	%
NCP/NCV59152 ADJ VOLTAGE DEV	ICES ONLY				
Reference Voltage DFN Package D2PAK Package		1.228 1.215 1.221 1.209	1.240 1.240	1.252 1.265 1.259 1.271	V
Adjust Pin Bias Current			100	200 350	nA

^{7.} $V_{DO} = V_{IN} - V_{OUT}$ when V_{OUT} decreases to 98% of its nominal output voltage with $V_{IN} = V_{OUT} + 1$ V. For output voltages below 1.74 V, dropout voltage specification does not apply due to a minimum input operating voltage of 2.24 V.

^{8.} I_{IN} = I_{GND} + I_{OUT}. 9. Fixed Voltage Device Start-up Time = Output Voltage Start-up Slope * V_{OUT} nominal.

Package	Conditions / PCB Footprint	Thermal Resistance
D2PAK-3, Junction-to-Case		$R_{\theta JC} = 2.1^{\circ}C/W$
D2PAK-5, Junction-to-Case		$R_{\theta JC} = 2.1^{\circ}C/W$
D2PAK-3, Junction-to-Air	PCB with 100 mm ² 2.0 oz Copper Heat Spreading Area	$R_{\theta JA} = 52^{\circ}C/W$
D2PAK-5, Junction-to-Air	PCB with 100 mm ² 2.0 oz Copper Heat Spreading Area	$R_{\theta JA} = 52^{\circ}C/W$
DFN8, Junction-to-Air	PCB with 500 mm ² 2.0 oz Copper Heat Spreading Area	$R_{\theta JA} = 75^{\circ}C/W$

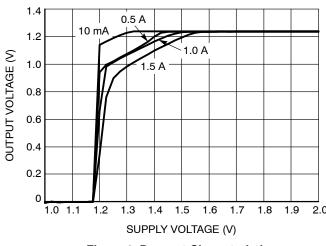
TYPICAL CHARACTERISTICS



450 400 DROPOUT VOLTAGE (mV) 350 300 250 200 150 100 50 -30 -10 10 30 50 70 110 130 TEMPERATURE (°C)

Figure 4. Dropout Voltage vs. Output Current (V_{OUTnom} = 2.5 V)

Figure 5. Dropout Voltage vs. Temperature $(V_{OUTnom} = 2.5 \text{ V}, I_{OUT} = 1.5 \text{ A})$



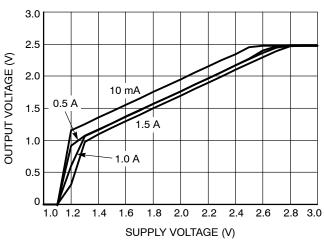
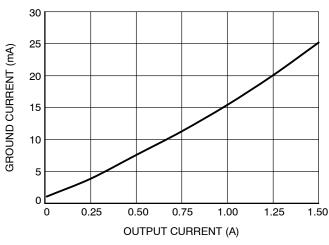


Figure 6. Dropout Characteristics (V_{OUTnom} = 1.24 V)

Figure 7. Dropout Characteristics (V_{OUTnom} = 2.5 V)



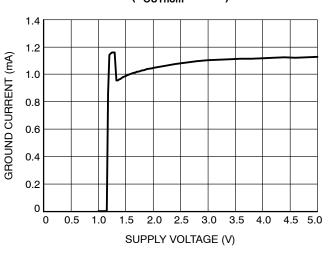


Figure 8. Ground Current vs. Output Current (V_{OUTnom} = 1.24 V)

Figure 9. Ground Current vs. Supply Voltage (V_{OUTnom} = 1.24 V, I_{OUT} = 10 mA)

TYPICAL CHARACTERISTICS

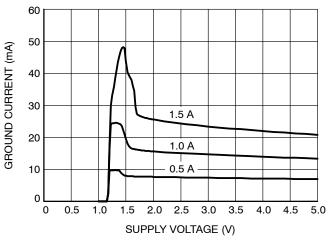


Figure 10. Ground Current vs. Supply Voltage (V_{OUTnom} = 1.24 V)

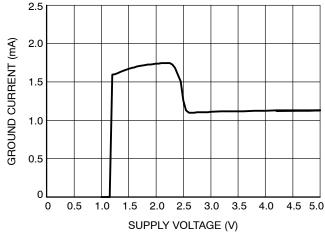


Figure 11. Ground Current vs. Supply Voltage (V_{OUTnom} = 2.5 V, I_{OUT} = 10 mA)

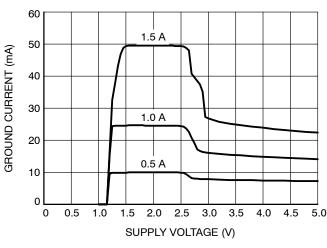


Figure 12. Ground Current vs. Supply Voltage (V_{OUTnom} = 2.5 V)

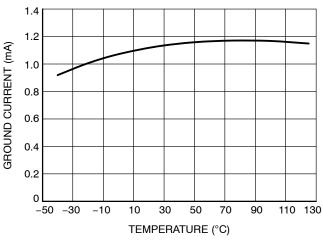


Figure 13. Ground Current vs. Temperature (V_{OUTnom} = 2.5 V, I_{OUT} = 10 mA, V_{IN} = 3.5 V)

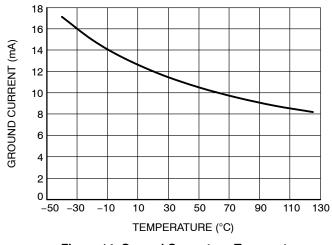


Figure 14. Ground Current vs. Temperature (V_{OUTnom} = 2.5 V, I_{OUT} = 0.75 A, V_{IN} = 3.5 V)

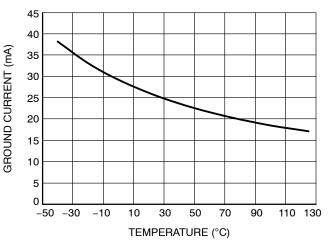


Figure 15. Ground Current vs. Temperature (V_{OUTnom} = 2.5 V, I_{OUT} = 1.5 A, V_{IN} = 3.5 V)

TYPICAL CHARACTERISTICS

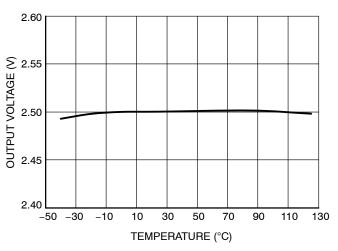


Figure 16. Output Voltage vs. Temperature (V_{OUTnom} = 2.5 V, I_{OUT} = 10 mA)

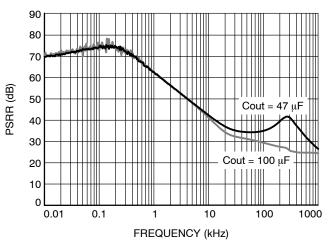


Figure 17. PSRR vs. Frequency, Vin = 3.5 V + 200 mVpp Modulation, Vout = 2.5 V, lout = 0.5 A

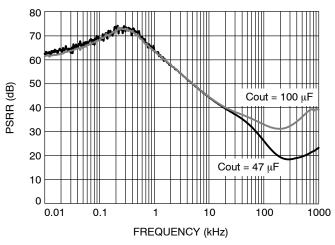


Figure 18. PSRR vs. Frequency, Vin = 3.5 V + 200 mVpp Modulation, Vout = 2.5 V, lout = 1.5 A

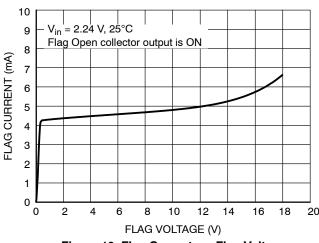


Figure 19. Flag Current vs. Flag Voltage

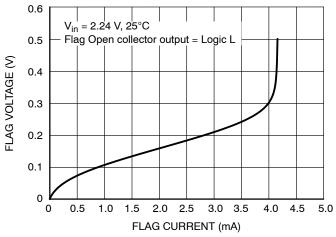


Figure 20. Flag Voltage vs. Flag Current

TYPICAL CHARACTERISTICS

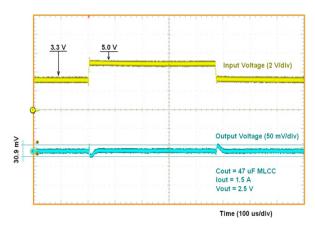


Figure 21. Line Transient Response

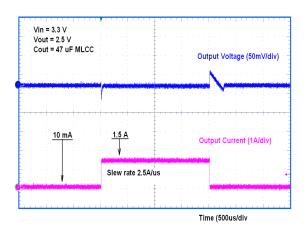


Figure 22. Load Transient Response

APPLICATIONS INFORMATION

Output Capacitor and Stability

The NCP59150 series requires an output capacitor for stable operation. The NCP59150 series is designed to operate with ceramic output capacitors. The recommended output capacitance value is 47 μ F or greater. Such capacitors help to improve transient response and noise reduction at high frequency.

Input Capacitor

An input capacitor of $1.0~\mu F$ or greater is recommended when the device is more than 4 inches away from the bulk supply capacitance, or when the supply is a battery. Small, surface–mount chip capacitors can be used for the bypassing. The capacitor should be place within 1 inch of the device for optimal performance. Larger values will help to improve ripple rejection by bypassing the input of the regulator, further improving the integrity of the output voltage.

Minimum Load Current

The NCP59150 regulator is specified between finite loads. A 5 mA minimum load current is necessary for proper operation.

Error Flag

Some NCP59150 series members feature an error flag circuit that monitors the output voltage and signals an error condition when the voltage is 5% below the nominal output voltage. The error flag is an open–collector output that can sink up to 5 mA typically during a V_{OUT} fault condition.

The FLG output is overload protected when a short circuit of the pullup load resistor occurs in the application. This is guaranteed in the full range of FLG output voltage Max ratings (see Max Ratings table). Please be aware operation in this mode is not recommended, power dissipated in the device can impact on output voltage precision and other device characteristics.

Enable Input

Some NCP59150 series members also feature an enable input for on/off control of the device. It's shutdown state draws "zero" current from input voltage supply (only microamperes of leakage). The enable input is TTL/CMOS compatible for simple logic interface, but can be connected up to $V_{\rm IN}$.

Overcurrent and Reverse Output Current Protection

The NCP59150 regulator is fully protected from damage due to output current overload and output short conditions. When NCP59150 output is overloaded, Output Current limiting is provided. This limiting is linear; output current during overload or output short conditions is constant. These features are advantageous for powering FPGAs and other ICs having current consumption higher than nominal during their startup.

Thermal shutdown disables the NCP59150 device when the die temperature exceeds the maximum safe operating temperature.

When NCP59150 is disabled and $(V_{OUT}-V_{IN})$ voltage difference is less than 6.5 V in the application, the output structure of these regulators is able to withstand output voltage (backup battery as example) to be applied without reverse current flow. Of course the additional current flowing through the feedback resistor divider inside the Fixed Voltage devices (30 μ A typically at nominal output voltage) needs to be included in the backup battery discharging calculations.

Adjustable Voltage Design

The NCP/NCV59152 Adjustable voltage Device Output voltage is set by the ratio of two external resistors as shown in Figure 23.

The device maintains the voltage at the ADJ pin at $1.24~\mathrm{V}$ referenced to ground. The current in R2 is then equal to

 $1.24\ V\ /\ R2,$ and the current in R1 is the current in R2 plus the ADJ pin bias current. The ADJ pin bias current flows from V_{OUT} through R1 into the ADJ pin.

The output voltage can be calculated using the formula shown in Figure 23.

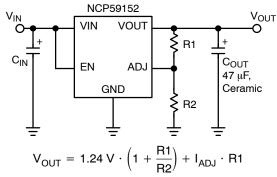


Figure 23. Adjustable Voltage Operation

Thermal Considerations

The power handling capability of the device is limited by the maximum rated junction temperature (125°C). The P_D total power dissipated by the device has two components, Input to output voltage differential multiplied by Output current and Input voltage multiplied by GND pin current.

$$P_{D} = (V_{IN} - V_{OUT}) \cdot I_{OUT} + V_{IN} \cdot I_{GND} \quad (eq. 1)$$

The GND pin current value can be found in Electrical Characteristics table and in Typical Characteristics graphs. The Junction temperature T_J is

$$T_{J} = T_{A} + P_{D} \cdot R_{\theta JA}$$
 (eq. 2)

where T_A is ambient temperature and $R_{\theta JA}$ is the Junction to Ambient Thermal Resistance of the NCP/NCV59150 device mounted on the specific PCB.

To maximize efficiency of the application and minimize thermal power dissipation of the device it is convenient to use the Input to output voltage differential as low as possible.

The static typical dropout characteristics for various output voltage and output current can be found in the Typical Characteristics graphs.

ORDERING INFORMATION

Device	Output Current	Output Voltage	Junction Temp. Range	Package	Shipping [†]
NCP59150DS18R4G	1.5 A	1.8 V	-40°C to +125°C	D ² PAK-3 (Pb-Free)	Contact Sales Office
NCP59150DS25R4G	1.5 A	2.5 V	-40°C to +125°C	D ² PAK-3 (Pb-Free)	Contact Sales Office
NCP59150DS28R4G	1.5 A	2.8 V	-40°C to +125°C	D ² PAK-3 (Pb-Free)	Contact Sales Office
NCP59150DS30R4G	1.5 A	3.0 V	-40°C to +125°C	D ² PAK-3 (Pb-Free)	Contact Sales Office
NCP59150DS33R4G	1.5 A	3.3 V	-40°C to +125°C	D ² PAK-3 (Pb-Free)	Contact Sales Office
NCP59150DS50R4G	1.5 A	5.0 V	-40°C to +125°C	D ² PAK-3 (Pb-Free)	Contact Sales Office
NCV59150DS18R4G*	1.5 A	1.8 V	-40°C to +125°C	D ² PAK-3 (Pb-Free)	Contact Sales Office
NCV59150DS25R4G*	1.5 A	2.5 V	-40°C to +125°C	D ² PAK-3 (Pb-Free)	Contact Sales Office
NCV59150DS28R4G*	1.5 A	2.8 V	-40°C to +125°C	D ² PAK-3 (Pb-Free)	Contact Sales Office
NCV59150DS30R4G*	1.5 A	3.0 V	-40°C to +125°C	D ² PAK-3 (Pb-Free)	Contact Sales Office
NCV59150DS33R4G*	1.5 A	3.3 V	-40°C to +125°C	D ² PAK-3 (Pb-Free)	Contact Sales Office
NCV59150DS50R4G*	1.5 A	5.0 V	-40°C to +125°C	D ² PAK-3 (Pb-Free)	Contact Sales Office
NCP59151DS18R4G	1.5 A	1.8 V	-40°C to +125°C	D ² PAK-5 (Pb-Free)	800 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{*}NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and PPAP Capable

ORDERING INFORMATION (continued)

Device	Output Current	Output Voltage	Junction Temp. Range	Package	Shipping [†]
NCP59151DS25R4G	1.5 A	2.5 V	-40°C to +125°C	D ² PAK-5 (Pb-Free)	800 / Tape & Reel
NCP59151DS28R4G	1.5 A	2.8 V	-40°C to +125°C	D ² PAK-5 (Pb-Free)	800 / Tape & Reel
NCP59151DS30R4G	1.5 A	3.0 V	-40°C to +125°C	D ² PAK-5 (Pb-Free)	800 / Tape & Reel
NCP59151DS33R4G	1.5 A	3.3 V	-40°C to +125°C	D ² PAK-5 (Pb-Free)	800 / Tape & Reel
NCP59151DS50R4G	1.5 A	5.0 V	-40°C to +125°C	D ² PAK-5 (Pb-Free)	800 / Tape & Reel
NCV59151DS18R4G*	1.5 A	1.8 V	-40°C to +125°C	D ² PAK-5 (Pb-Free)	800 / Tape & Reel
NCV59151DS25R4G*	1.5 A	2.5 V	-40°C to +125°C	D ² PAK-5 (Pb-Free)	800 / Tape & Reel
NCV59151DS28R4G*	1.5 A	2.8 V	-40°C to +125°C	D ² PAK-5 (Pb-Free)	800 / Tape & Reel
NCV59151DS30R4G*	1.5 A	3.0 V	-40°C to +125°C	D ² PAK-5 (Pb-Free)	800 / Tape & Reel
NCV59151DS33R4G	1.5 A	3.3 V	-40°C to +125°C	D ² PAK-5 (Pb-Free)	800 / Tape & Reel
NCV59151DS50R4G	1.5 A	5.0 V	-40°C to +125°C	D ² PAK-5 (Pb-Free)	800 / Tape & Reel
NCP59151MN18TYG	1.5 A	1.8 V	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCP59151MN25TYG	1.5 A	2.5 V	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCP59151MN28TYG	1.5 A	2.8 V	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCP59151MN30TYG	1.5 A	3.0 V	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCP59151MN33TYG	1.5 A	3.3 V	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCP59151MN50TYG	1.5 A	5.0 V	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCV59151MN18TYG	1.5 A	1.8 V	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCV59151MN25TYG	1.5 A	2.5 V	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCV59151MN28TYG	1.5 A	2.8 V	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCV59151MN30TYG	1.5 A	3.0 V	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCV59151MN33TYG	1.5 A	3.3 V	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCV59151MN50TYG	1.5 A	5.0 V	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCP59152MNADJTYG	1.5 A	ADJ	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel
NCV59152MNADJTYG	1.5 A	ADJ	-40°C to +125°C	DFN8-4x4 (Pb-Free)	4000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
*NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and

PPAP Capable

ORDERING INFORMATION (continued)

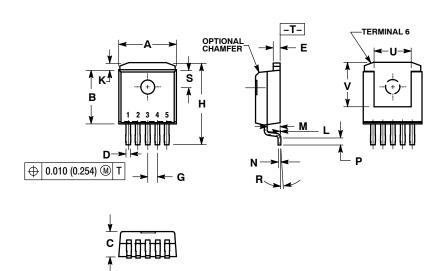
Device	Output Current	Output Voltage	Junction Temp. Range	Package	Shipping [†]
NCP59152DSADJR4G	1.5 A	ADJ	-40°C to +125°C	D2PAK-5 (Pb-Free)	800 / Tape & Reel
NCV59152DSADJR4G	1.5 A	ADJ	-40°C to +125°C	D2PAK-5 (Pb-Free)	800 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.
*NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q100 Qualified and

PPAP Capable

PACKAGE DIMENSIONS

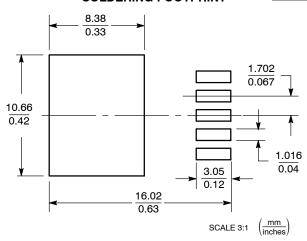
D²PAK 5 CASE 936A-02 **ISSUE C**



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. TAB CONTOUR OPTIONAL WITHIN DIMENSIONS A AND K.
 4. DIMENSIONS U AND V ESTABLISH A MINIMUM MOUNTING SURFACE FOR TERMINAL 6.
 5. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH OR GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED 0.025 (0.635) MAXIMUM.

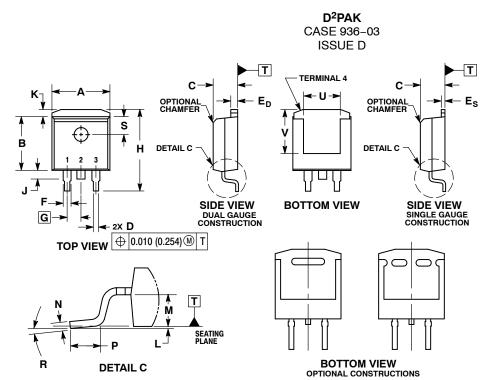
	INC	HES	MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.386	0.403	9.804	10.236
В	0.356	0.368	9.042	9.347
С	0.170	0.180	4.318	4.572
D	0.026	0.036	0.660	0.914
Е	0.045	0.055	1.143	1.397
G	0.067	BSC	1.702 BSC	
Н	0.539	0.579	13.691	14.707
K	0.050	REF	1.270 REF	
L	0.000	0.010	0.000	0.254
М	0.088	0.102	2.235	2.591
N	0.018	0.026	0.457	0.660
P	0.058	0.078	1.473	1.981
R	5° REF		5° REF	
S	0.116 REF		2.946 REF	
U	0.200	MIN	5.080 MIN	
٧	0.250	MIN	6.350	MIN

SOLDERING FOOTPRINT



5-LEAD D2PAK

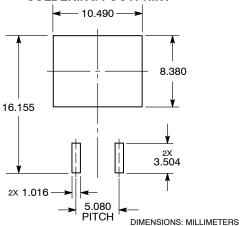
PACKAGE DIMENSIONS



- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI
- DIMENSIONING AND TOLEHANCING PEH ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCHES. TAB CONTOUR OPTIONAL WITHIN DIMENSIONS AND K. DIMENSIONS U AND Y ESTABLISH A MINIMUM
- MOUNTING SURFACE FOR TERMINAL 4.
 DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH OR GATE PROTRUSIONS. MOLD FLASH AND GATE PROTRUSIONS NOT TO EXCEED
- 0.025 (0.635) MAXIMUM.
 6. SINGLE GAUGE DESIGN WILL BE SHIPPED AFTER FPCN EXPIRATION IN OCTOBER 2011.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.386	0.403	9.804	10.236
В	0.356	0.368	9.042	9.347
С	0.170	0.180	4.318	4.572
D	0.026	0.036	0.660	0.914
ED	0.045	0.055	1.143	1.397
Es	0.018	0.026	0.457	0.660
F	0.051	REF	1.295 REF	
G	0.100	BSC	2.540 BSC	
Н	0.539	0.579	13.691	14.707
J	0.125	MAX	3.175 MAX	
K	0.050	REF	1.270 REF	
L	0.000	0.010	0.000	0.254
M	0.088	0.102	2.235	2.591
N	0.018	0.026	0.457	0.660
P	0.058	0.078	1.473	1.981
R	5° REF		5° REF	
S	0.116 REF		2.946 REF	
U	0.200	MIN	5.080	MIN (
٧	0.250	MIN	6.350	NIM C

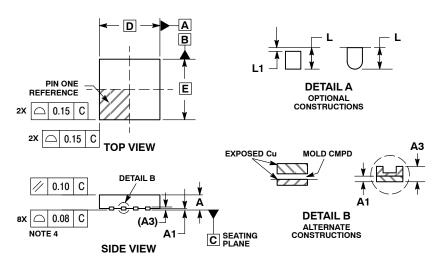
SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

DFN8, 4x4 CASE 488AF ISSUE C



NOTES

- DIMENSIONS AND TOLERANCING PER
- ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS.
- DIMENSION 6 APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30MM FROM TERMINAL TIP.
 COPLANARITY APPLIES TO THE EXPOSED
- PAD AS WELL AS THE TERMINALS
- DETAILS A AND B SHOW OPTIONAL CONSTRUCTIONS FOR TERMINALS

	MILLIMETERS			
DIM	MIN MAX			
Α	0.80	1.00		
A1	0.00	0.05		
А3	0.20	REF		
b	0.25	0.35		
D	4.00	BSC		
D2	1.91	2.21		
E	4.00	BSC		
E2	2.09	2.39		
е	0.80	BSC		
K	0.20			
L	0.30	0.50		
L1		0.15		

D2 ax L DETAIL A F2 е CAB 0.10 C NOTE 3 0.05 **BOTTOM VIEW**

SOLDERING FOOTPRINT* 0.63 430 239 PACKAGE OUTLINE 0.35 0.80 **PITCH**

DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and (III) are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking, pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303–675–2175 or 800–344–3860 Toll Free USA/Canada **Fax**: 303–675–2176 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative

X-ON Electronics

Largest Supplier of Electrical and Electronic Components

Click to view similar products for LDO Voltage Regulators category:

Click to view products by ON Semiconductor manufacturer:

Other Similar products are found below:

AP7363-SP-13 L79M05TL-E PT7M8202B12TA5EX TCR3DF185,LM(CT TCR3DF24,LM(CT TCR3DF285,LM(CT TCR3DF31,LM(CT TCR3DF31,LM(CT TCR3DF45,LM(CT MP2013GQ-33-Z 059985X NCP4687DH15T1G 701326R TCR2EN28,LF(S NCV8170AXV250T2G TCR3DF27,LM(CT TCR3DF19,LM(CT TCR3DF125,LM(CT TCR2EN18,LF(S AP2112R5A-3.3TRG1 AP7315-25W5-7 IFX30081LDVGRNXUMA1 NCV47411PAAJR2G AP2113KTR-G1 AP2111H-1.2TRG1 ZLDO1117QK50TC AZ1117IH-1.8TRG1 AZ1117ID-ADJTRG1 TCR3DG12,LF MIC5514-3.3YMT-T5 MIC5512-1.2YMT-T5 MIC5317-2.8YM5-T5 SCD7912BTG NCP154MX180270TAG SCD33269T-5.0G NCV8170BMX330TCG NCV8170AMX120TCG NCP706ABMX300TAG NCP153MX330180TCG NCP114BMX075TCG MC33269T-3.5G CAT6243-ADJCMT5T TCR3DG33,LF AP2127N-1.0TRG1 TCR4DG35,LF LT1117CST-3.3 LT1117CST-5 TAR5S15U(TE85L,F) TAR5S18U(TE85L,F) TCR3UG19A,LF TCR4DG105,LF