

# NCV8401A, NCV8401B

## Self-Protected Low Side Driver with Temperature and Current Limit

NCV8401A/B is a three terminal protected Low-Side Smart Discrete device. The protection features include overcurrent, overtemperature, ESD and integrated Drain-to-Gate clamping for overvoltage protection. This device offers protection and is suitable for harsh automotive environments.

### Features

- Short Circuit Protection
- Thermal Shutdown with Automatic Restart
- Over Voltage Protection
- Integrated Clamp for Inductive Switching
- ESD Protection
- dV/dt Robustness
- Analog Drive Capability (Logic Level Input)
- NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

### Typical Applications

- Switch a Variety of Resistive, Inductive and Capacitive Loads
- Can Replace Electromechanical Relays and Discrete Circuits
- Automotive / Industrial

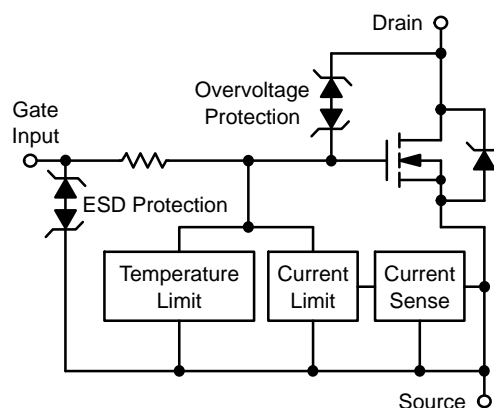


ON Semiconductor®

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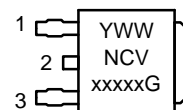
| V <sub>DSS</sub> (Clamped) | R <sub>DS(ON)</sub> TYP | I <sub>D</sub> MAX (Limited) |
|----------------------------|-------------------------|------------------------------|
| 42 V                       | 23 mΩ @ 10 V            | 33 A*                        |

\*Max current may be limited below this value depending on input conditions.



DPAK  
CASE 369C  
STYLE 2

### MARKING DIAGRAM



|       |                   |   |          |
|-------|-------------------|---|----------|
| Y     | = Year            | 1 | = Gate   |
| WW    | = Work Week       | 2 | = Drain  |
| xxxxx | = 8401A or 8401B  | 3 | = Source |
| G     | = Pb-Free Package |   |          |

### ORDERING INFORMATION

| Device        | Package        | Shipping†        |
|---------------|----------------|------------------|
| NCV8401ADTRKG | DPAK (Pb-Free) | 2500/Tape & Reel |
| NCV8401BDTRKG | DPAK (Pb-Free) | 2500/Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

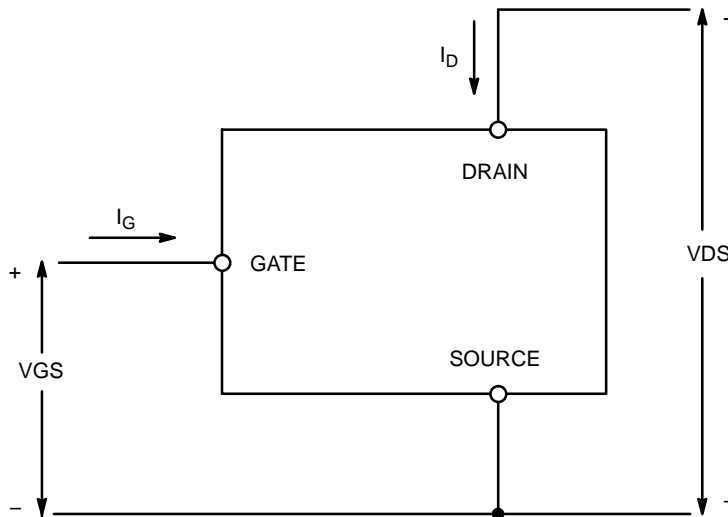
# NCV8401A, NCV8401B

## MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

| Rating   | Symbol   | Value              | Unit |
|--|--|--------------------|------|
| Drain-to-Source Voltage Internally Clamped   | V <sub>DSS</sub>   | 42                 | V    |
| Drain-to-Gate Voltage Internally Clamped (R <sub>GS</sub> = 1.0 MΩ)  | V <sub>DGR</sub>   | 42                 | V    |
| Gate-to-Source Voltage   | V <sub>GS</sub>  | ± 14               | V    |
| Drain Current – Continuous   | I <sub>D</sub>   | Internally Limited |      |
| Total Power Dissipation<br>@ T <sub>A</sub> = 25°C (Note 1)<br>@ T <sub>A</sub> = 25°C (Note 2)  | P <sub>D</sub>   | 1.1<br>2.0         | W    |
| Thermal Resistance,<br>Junction-to-Case<br>Junction-to-Ambient (Note 1)<br>Junction-to-Ambient (Note 2)  | R <sub>θJC</sub><br>R <sub>θJA</sub><br>R <sub>θJA</sub> | 1.6<br>110<br>60   | °C/W |
| Single Pulse Drain-to-Source Avalanche Energy<br>(V <sub>DD</sub> = 25 Vdc, V <sub>GS</sub> = 5.0 Vdc, I <sub>L</sub> = 3.65 Apk, L = 120 mH, R <sub>G</sub> = 25 Ω, T <sub>Jstart</sub> = 150°C) (Note 3) | E <sub>AS</sub>  | 800                | mJ   |
| Load Dump Voltage (V <sub>GS</sub> = 0 and 10 V, R <sub>I</sub> = 2.0 Ω, R <sub>L</sub> = 3.0 Ω, t <sub>d</sub> = 400 ms)  | V <sub>LD</sub>  | 65                 | V    |
| Operating Junction Temperature   | T <sub>J</sub>   | -40 to 150         | °C   |
| Storage Temperature  | T <sub>stg</sub>   | -55 to 150         | °C   |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Minimum FR4 PCB, steady state.
2. Mounted onto a 2" square FR4 board  
(1" square, 2 oz. Cu 0.06" thick single-sided, t = steady state).
3. Not subject to production testing.



**Figure 1. Voltage and Current Convention**

# NCV8401A, NCV8401B

## MOSFET ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted)

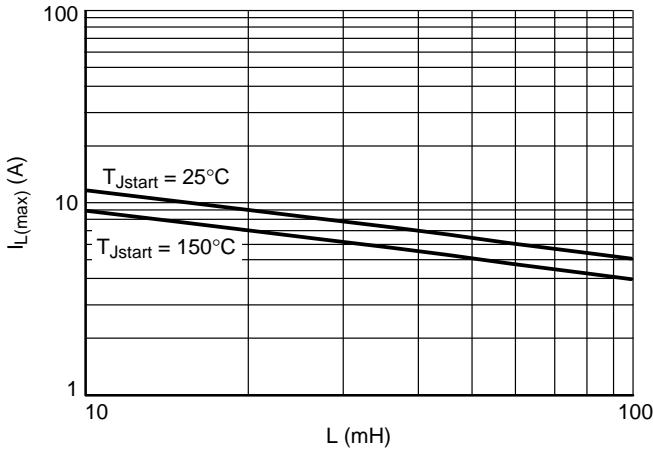
| Characteristic  | Symbol   | Min                                 | Typ         | Max      | Unit          |     |
|---|--|-------------------------------------|-------------|----------|---------------|-----|
| <b>OFF CHARACTERISTICS</b>  |  |                                     |             |          |               |     |
| Drain-to-Source Clamped Breakdown Voltage<br>(V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = 250 μAdc)<br>(V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = 250 μAdc, T <sub>J</sub> = 150°C) (Note 4)                              | V <sub>(BR)DSS</sub>   | 42<br>42                            | 46<br>44    | 50<br>50 | Vdc           |     |
| Zero Gate Voltage Drain Current<br>(V <sub>DS</sub> = 32 Vdc, V <sub>GS</sub> = 0 Vdc)<br>(V <sub>DS</sub> = 32 Vdc, V <sub>GS</sub> = 0 Vdc, T <sub>J</sub> = 150°C) (Note 4)  | I <sub>DSS</sub>   |                                     | 1.5<br>6.5  | 5.0      | μAdc          |     |
| Gate Input Current<br>(V <sub>GS</sub> = 5.0 Vdc, V <sub>DS</sub> = 0 Vdc)  | I <sub>GSSF</sub>  |                                     | 50          | 100      | μAdc          |     |
| <b>ON CHARACTERISTICS</b>   |  |                                     |             |          |               |     |
| Gate Threshold Voltage<br>(V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 1.2 mAdc)<br>Threshold Temperature Coefficient  | V <sub>GS(th)</sub>  | 1.0                                 | 1.8<br>5.0  | 2.0      | Vdc<br>-mV/°C |     |
| Static Drain-to-Source On-Resistance (Note 5)<br>(V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 5.0 Adc, T <sub>J</sub> @ 25°C)<br>(V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 5.0 Adc, T <sub>J</sub> @ 150°C) (Note 4)   | R <sub>DS(on)</sub>  |                                     | 23<br>43    | 29<br>55 | mΩ            |     |
| Static Drain-to-Source On-Resistance (Note 5)<br>(V <sub>GS</sub> = 5.0 Vdc, I <sub>D</sub> = 5.0 Adc, T <sub>J</sub> @ 25°C)<br>(V <sub>GS</sub> = 5.0 Vdc, I <sub>D</sub> = 5.0 Adc, T <sub>J</sub> @ 150°C) (Note 4) | R <sub>DS(on)</sub>  |                                     | 28<br>50    | 34<br>60 | mΩ            |     |
| Source-Drain Forward On Voltage<br>(I <sub>S</sub> = 5 A, V <sub>GS</sub> = 0 V)  | V <sub>SD</sub>  |                                     | 0.80        | 1.1      | V             |     |
| <b>SWITCHING CHARACTERISTICS (Note 4)</b>   |  |                                     |             |          |               |     |
| Turn-ON Time (10% V <sub>IN</sub> to 90% I <sub>D</sub> )   | V <sub>IN</sub> = 0 V to 5 V, V <sub>DD</sub> = 25 V<br>I <sub>D</sub> = 1.0 A, Ext R <sub>G</sub> = 2.5 Ω   | t <sub>ON</sub>                     | 41          | 50       | μs            |     |
| Turn-OFF Time (90% V <sub>IN</sub> to 10% I <sub>D</sub> )  |  | t <sub>OFF</sub>                    | 129         | 150      |               |     |
| Turn-ON Time (10% V <sub>IN</sub> to 90% I <sub>D</sub> )   | V <sub>IN</sub> = 0 V to 10 V, V <sub>DD</sub> = 25 V,<br>I <sub>D</sub> = 1.0 A, Ext R <sub>G</sub> = 2.5 Ω | t <sub>ON</sub>                     | 16          | 25       |               |     |
| Turn-OFF Time (90% V <sub>IN</sub> to 10% I <sub>D</sub> )  |  | t <sub>OFF</sub>                    | 164         | 180      |               |     |
| Slew-Rate ON (80% V <sub>DS</sub> to 50% V <sub>DS</sub> )  | V <sub>in</sub> = 0 to 10 V, V <sub>DD</sub> = 12 V,<br>R <sub>L</sub> = 4.7 Ω                               | -dV <sub>DS</sub> /dt <sub>ON</sub> | 1.27        | 2.0      | V/μs          |     |
| Slew-Rate OFF (50% V <sub>DS</sub> to 80% V <sub>DS</sub> )   |  | dV <sub>DS</sub> /dt <sub>OFF</sub> | 0.36        | 0.75     |               |     |
| <b>SELF PROTECTION CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted)</b>  |  |                                     |             |          |               |     |
| Current Limit   | V <sub>GS</sub> = 5.0 V, V <sub>DS</sub> = 10 V<br>V <sub>GS</sub> = 5.0 V, T <sub>J</sub> = 150°C (Note 4)  | I <sub>LIM</sub>                    | 25<br>11    | 30<br>16 | 35<br>21      | Adc |
|   | V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 10 V<br>V <sub>GS</sub> = 10 V, T <sub>J</sub> = 150°C (Note 4)    |                                     | 30<br>18    | 35<br>25 | 40<br>28      |     |
| Temperature Limit (Turn-off)  | V <sub>GS</sub> = 5.0 V (Note 4)   | T <sub>LIM(off)</sub>               | 150         | 175      | 200           | °C  |
| Thermal Hysteresis  | V <sub>GS</sub> = 5.0 V  | ΔT <sub>LIM(on)</sub>               |             | 15       |               | °C  |
| Temperature Limit (Turn-off)  | V <sub>GS</sub> = 10 V (Note 4)  | T <sub>LIM(off)</sub>               | 150         | 165      | 185           | °C  |
| Thermal Hysteresis  | V <sub>GS</sub> = 10 V   | ΔT <sub>LIM(on)</sub>               |             | 15       |               | °C  |
| <b>GATE INPUT CHARACTERISTICS (Note 4)</b>  |  |                                     |             |          |               |     |
| Device ON Gate Input Current  | V <sub>GS</sub> = 5 V I <sub>D</sub> = 1.0 A   | I <sub>GON</sub>                    |             | 50       | 100           | μA  |
|   | V <sub>GS</sub> = 10 V I <sub>D</sub> = 1.0 A  |                                     |             | 400      | 700           |     |
| Current Limit Gate Input Current  | V <sub>GS</sub> = 5 V, V <sub>DS</sub> = 10 V  | I <sub>GCL</sub>                    |             | 0.1      | 0.5           | mA  |
|   | V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 10 V   |                                     |             | 0.7      | 1.0           |     |
| Thermal Limit Fault Gate Input Current  | V <sub>GS</sub> = 5 V, V <sub>DS</sub> = 10 V  | I <sub>GTL</sub>                    |             | 0.6      | 1.0           | mA  |
|   | V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 10 V   |                                     |             | 2.0      | 4.0           |     |
| <b>ESD ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise noted) (Note 4)</b>  |  |                                     |             |          |               |     |
| Electro-Static Discharge Capability<br>Human Body Model (HBM)<br>Machine Model (MM)   | ESD  |                                     | 4000<br>400 |          |               | V   |

4. Not subject to production testing.

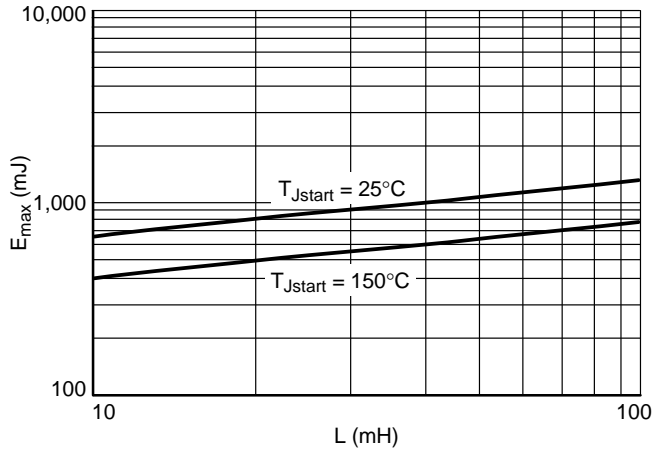
5. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.

# NCV8401A, NCV8401B

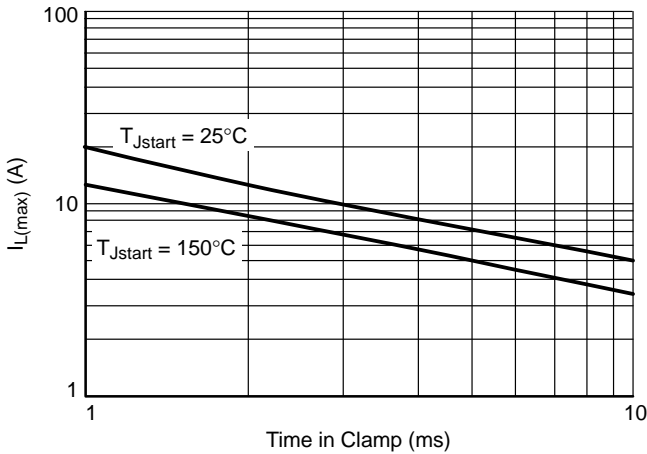
## TYPICAL PERFORMANCE CURVES



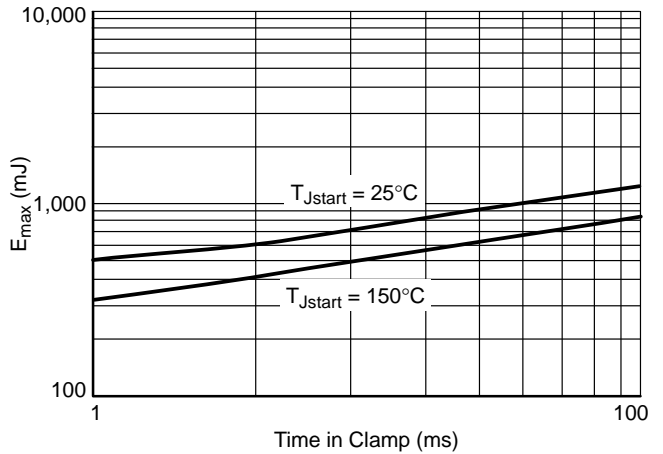
**Figure 2. Single Pulse Maximum Switch-off Current vs. Load Inductance**



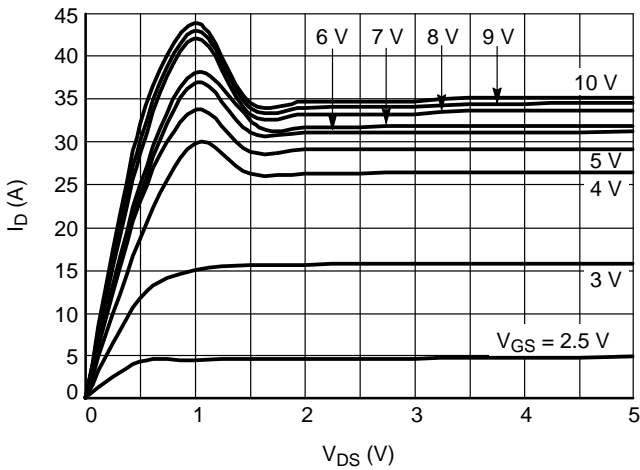
**Figure 3. Single Pulse Maximum Switching Energy vs. Load Inductance**



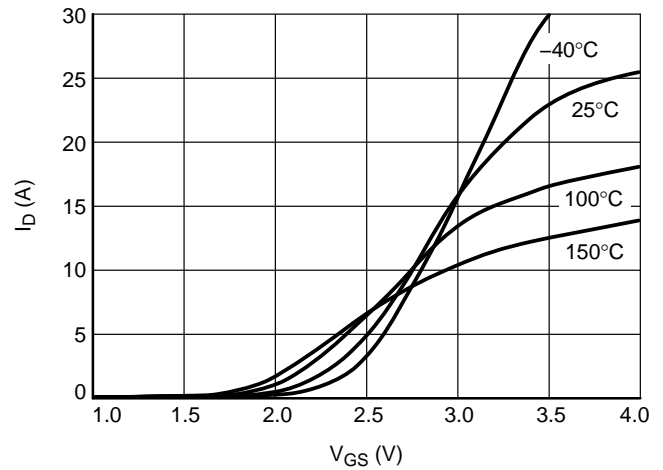
**Figure 4. Single Pulse Maximum Inductive Switch-off Current vs. Time in Clamp**



**Figure 5. Single Pulse Maximum Inductive Switching Energy vs. Time in Clamp**



**Figure 6. On-state Output Characteristics at 25°C**



**Figure 7. Transfer Characteristics ( $V_{DS} = 10\text{ V}$ )**

TYPICAL PERFORMANCE CURVES

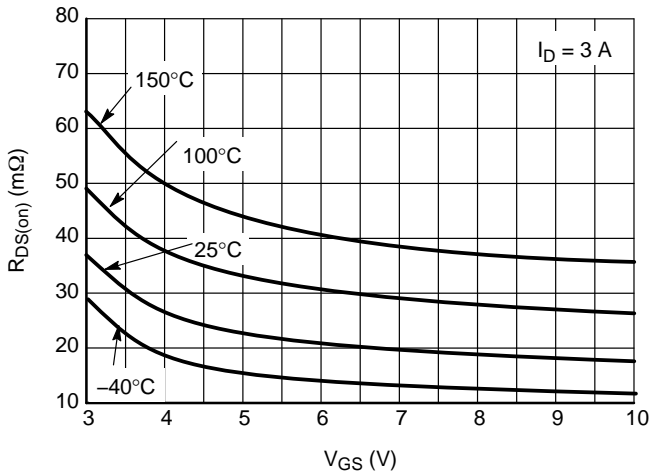


Figure 8.  $R_{DS(on)}$  vs. Gate-Source Voltage

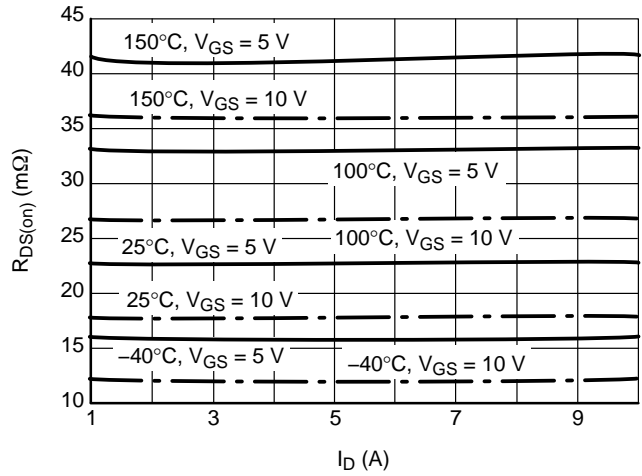


Figure 9.  $R_{DS(on)}$  vs. Drain Current

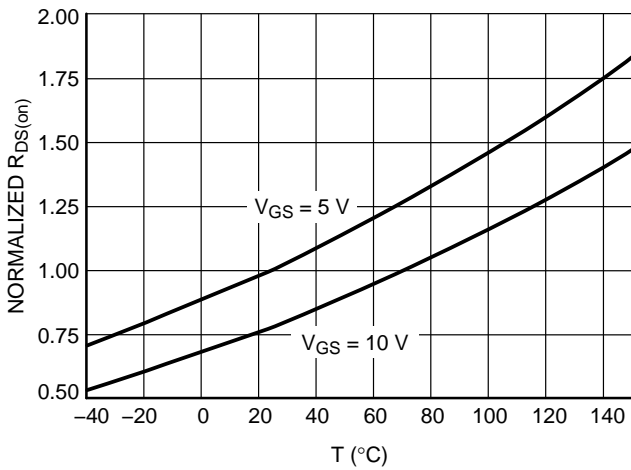


Figure 10. Normalized  $R_{DS(on)}$  vs. Temperature ( $I_D = 5 A$ )

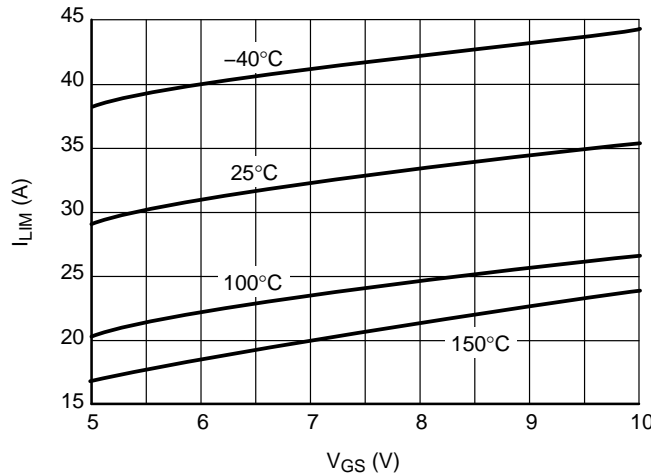


Figure 11. Current Limit vs. Gate-Source Voltage ( $V_{DS} = 10 V$ )

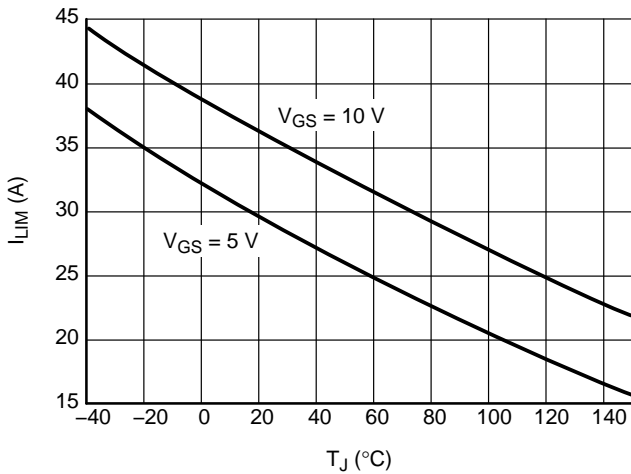


Figure 12. Current Limit vs. Junction Temperature ( $V_{DS} = 10 V$ )

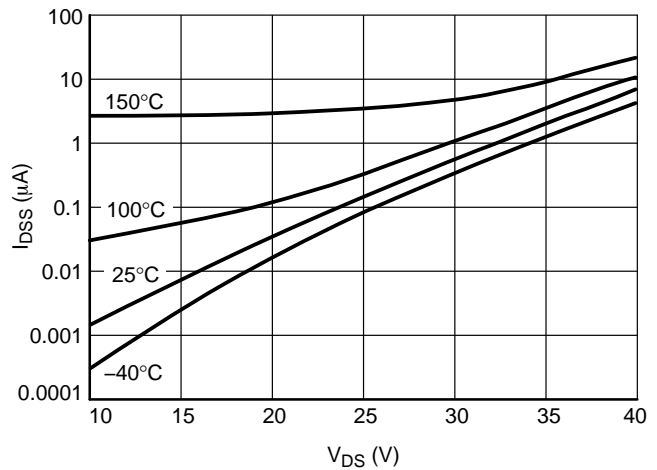


Figure 13. Drain-to-Source Leakage Current ( $V_{GS} = 0 V$ )

TYPICAL PERFORMANCE CURVES

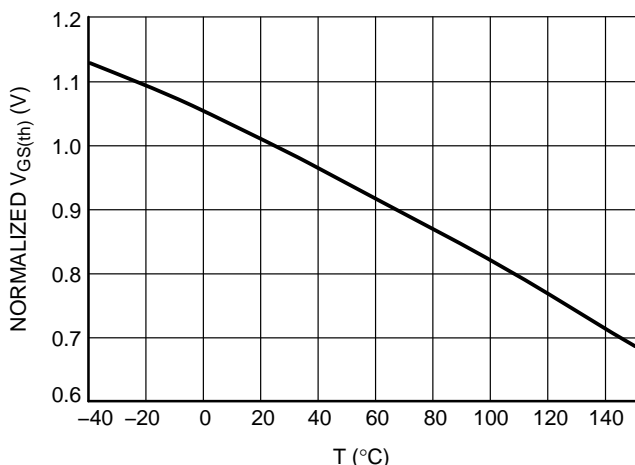


Figure 14. Normalized Threshold Voltage vs. Temperature ( $I_D = 1.2 \text{ mA}$ ,  $V_{DS} = V_{GS}$ )

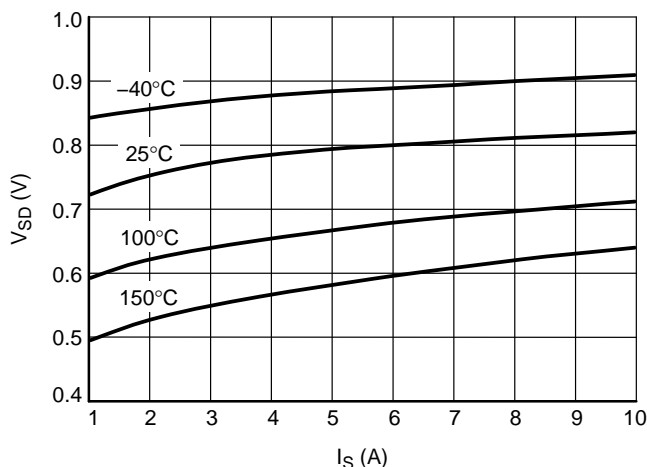


Figure 15. Source-Drain Diode Forward Characteristics ( $V_{GS} = 0 \text{ V}$ )

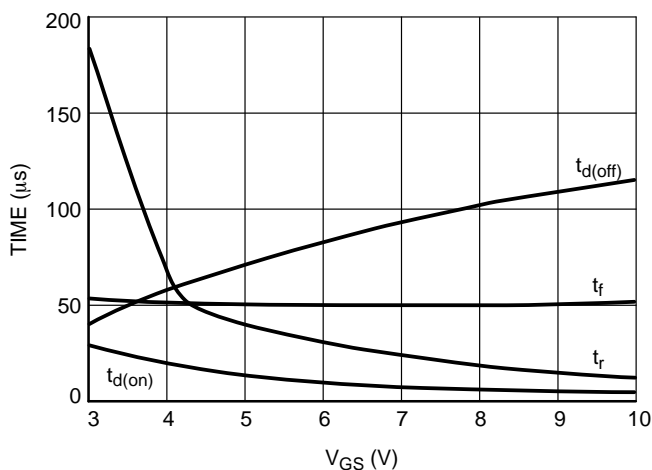


Figure 16. Resistive Load Switching Time vs. Gate-Source Voltage ( $V_{DD} = 25 \text{ V}$ ,  $I_D = 5 \text{ A}$ ,  $R_G = 0 \Omega$ )

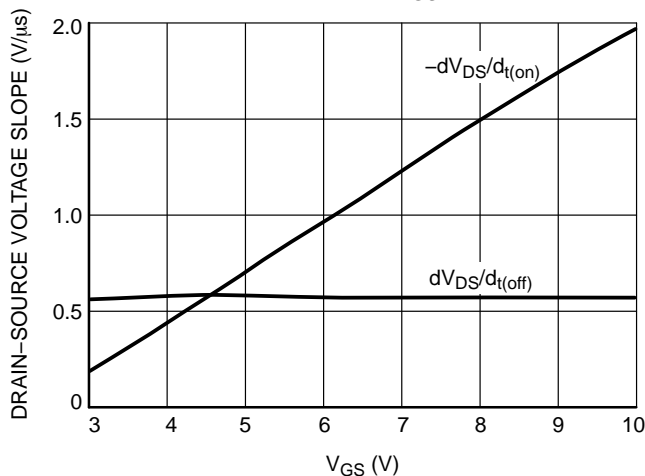


Figure 17. Resistive Load Switching Drain-Source Voltage Slope vs. Gate-Source Voltage ( $V_{DD} = 25 \text{ V}$ ,  $I_D = 5 \text{ A}$ ,  $R_G = 0 \Omega$ )

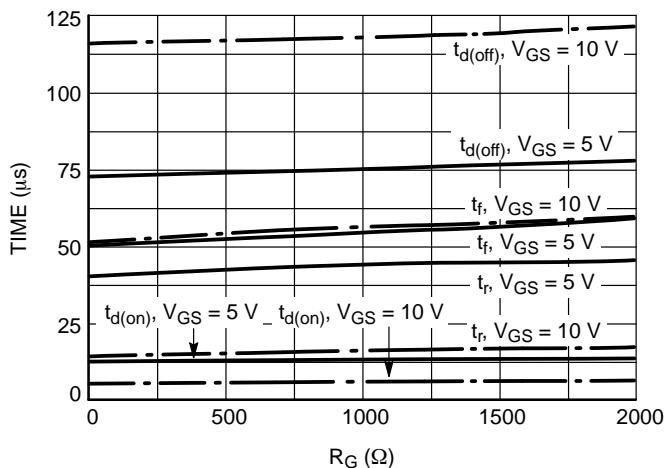


Figure 18. Resistive Load Switching Time vs. Gate Resistance ( $V_{DD} = 25 \text{ V}$ ,  $I_D = 5 \text{ A}$ )

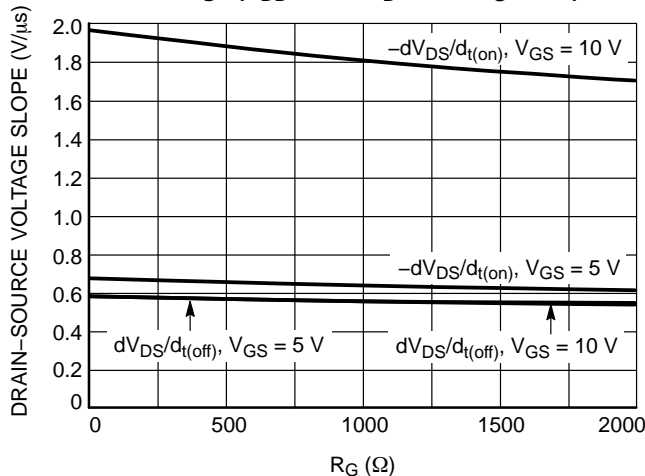


Figure 19. Drain-Source Voltage Slope during Turn On and Turn Off vs. Gate Resistance ( $V_{DD} = 25 \text{ V}$ ,  $I_D = 5 \text{ A}$ )

# NCV8401A, NCV8401B

## TYPICAL PERFORMANCE CURVES

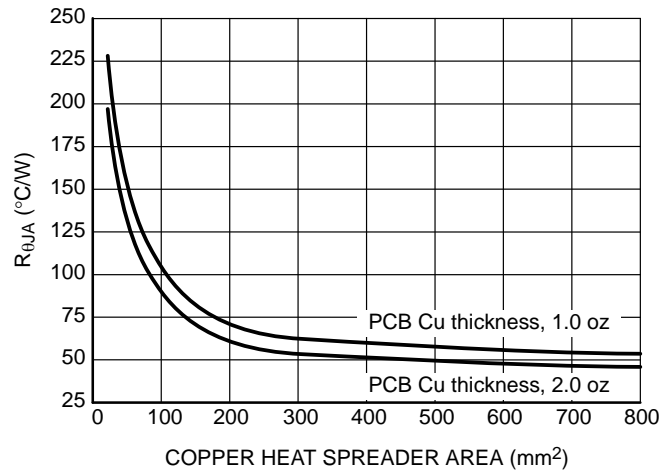


Figure 20.  $R_{\theta JA}$  vs. Copper Area

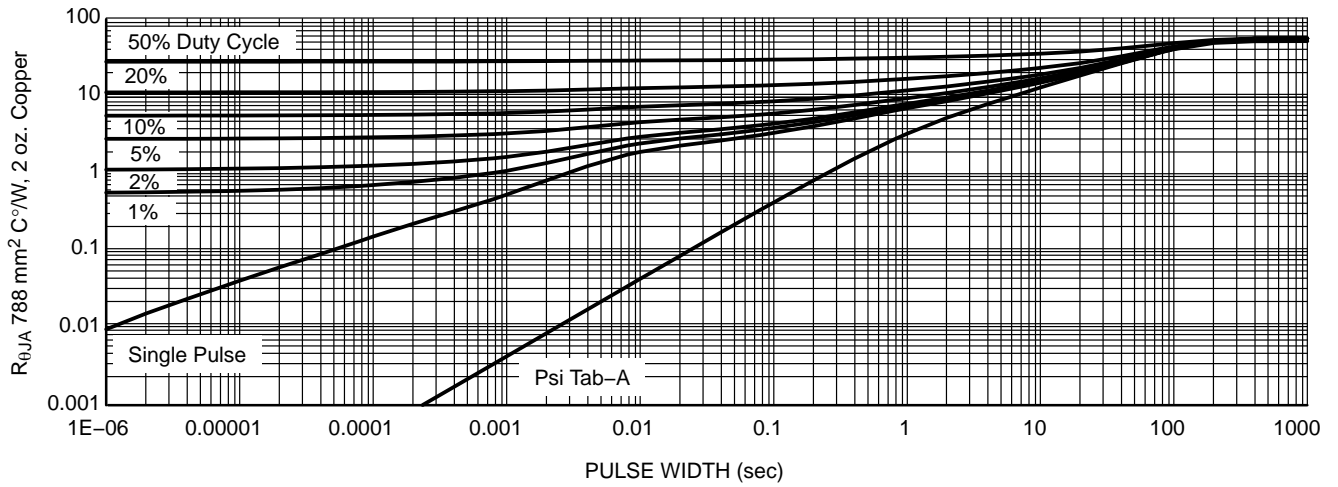


Figure 21. Transient Thermal Resistance

# NCV8401A, NCV8401B

## TEST CIRCUITS AND WAVEFORMS

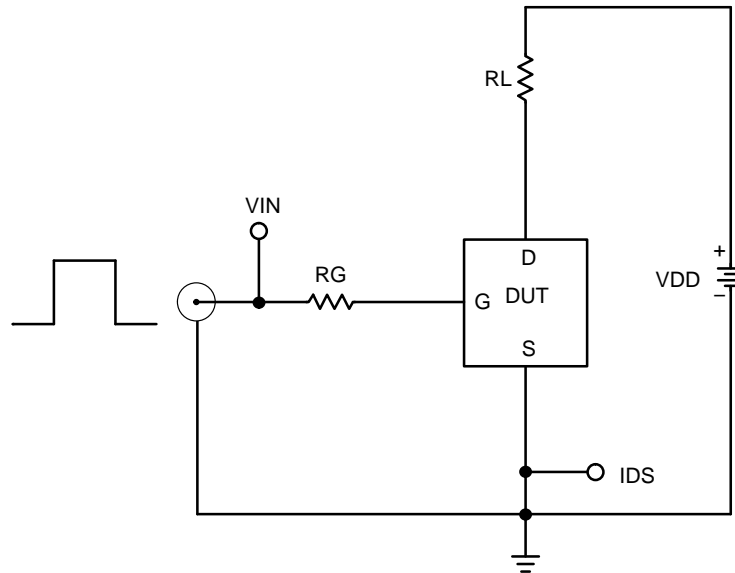


Figure 22. Resistive Load Switching Test Circuit

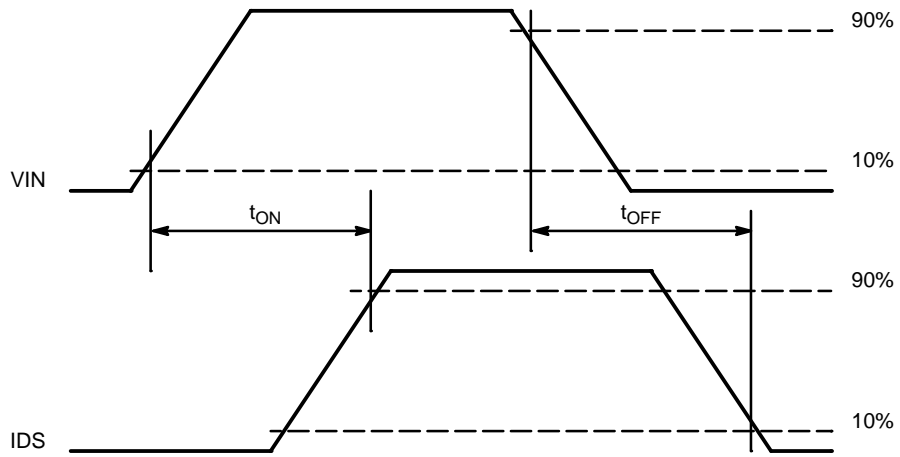


Figure 23. Resistive Load Switching Waveforms



# NCV8401A, NCV8401B

## TEST CIRCUITS AND WAVEFORMS

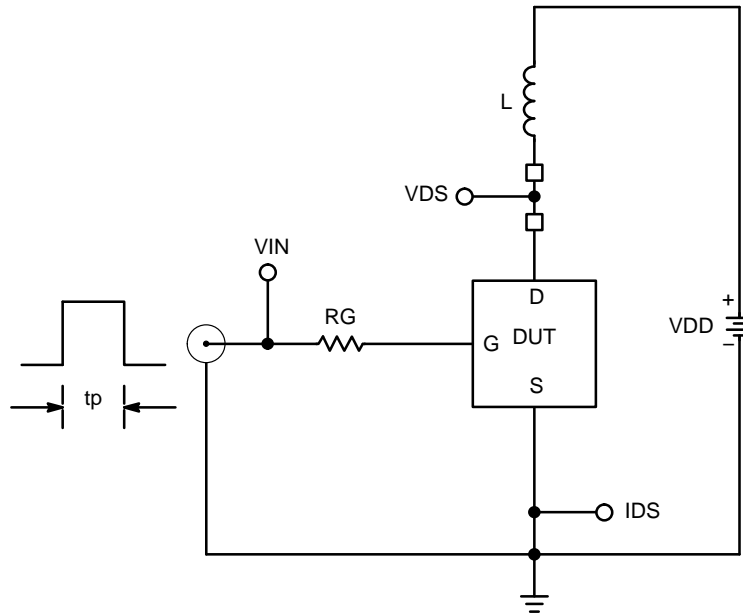


Figure 24. Inductive Load Switching Test Circuit

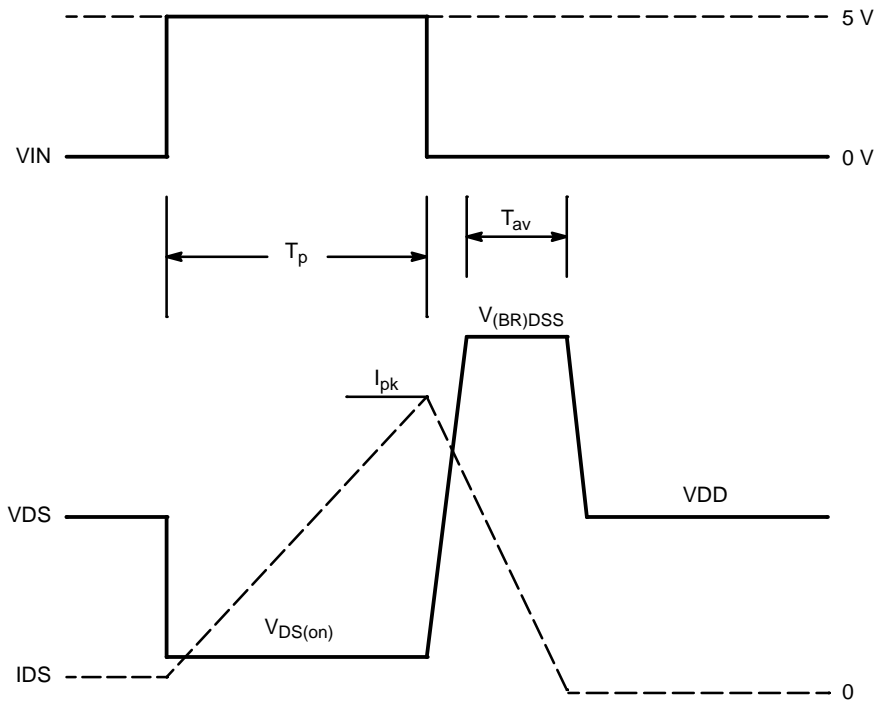


Figure 25. Inductive Load Switching Waveforms

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

ON Semiconductor®



SCALE 1:1

### DPAK (SINGLE GAUGE) CASE 369C ISSUE F

DATE 21 JUL 2015



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: INCHES.
3. THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS b3, L3 and Z.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE.
5. DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
6. DATUMS A AND B ARE DETERMINED AT DATUM PLANE H.
7. OPTIONAL MOLD FEATURE.

| DIM | INCHES    |       | MILLIMETERS |       |
|-----|-----------|-------|-------------|-------|
|     | MIN       | MAX   | MIN         | MAX   |
| A   | 0.086     | 0.094 | 2.18        | 2.38  |
| A1  | 0.000     | 0.005 | 0.00        | 0.13  |
| b   | 0.025     | 0.035 | 0.63        | 0.89  |
| b2  | 0.028     | 0.045 | 0.72        | 1.14  |
| b3  | 0.180     | 0.215 | 4.57        | 5.46  |
| c   | 0.018     | 0.024 | 0.46        | 0.61  |
| c2  | 0.018     | 0.024 | 0.46        | 0.61  |
| D   | 0.235     | 0.245 | 5.97        | 6.22  |
| E   | 0.250     | 0.265 | 6.35        | 6.73  |
| e   | 0.090 BSC |       | 2.29 BSC    |       |
| H   | 0.370     | 0.410 | 9.40        | 10.41 |
| L   | 0.055     | 0.070 | 1.40        | 1.78  |
| L1  | 0.114 REF |       | 2.90 REF    |       |
| L2  | 0.020 BSC |       | 0.51 BSC    |       |
| L3  | 0.035     | 0.050 | 0.89        | 1.27  |
| L4  | ---       | 0.040 | ---         | 1.01  |
| Z   | 0.155     | ---   | 3.93        | ---   |

### GENERIC MARKING DIAGRAM\*



- XXXXXX = Device Code
- A = Assembly Location
- L = Wafer Lot
- Y = Year
- WW = Work Week
- G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking.

- |  |  |   |   |  |
|--|--|---|---|--|
| <p>STYLE 1:<br/>PIN 1. BASE<br/>2. COLLECTOR<br/>3. EMITTER<br/>4. COLLECTOR</p> | <p>STYLE 2:<br/>PIN 1. GATE<br/>2. DRAIN<br/>3. SOURCE<br/>4. DRAIN</p>          | <p>STYLE 3:<br/>PIN 1. ANODE<br/>2. CATHODE<br/>3. ANODE<br/>4. CATHODE</p> | <p>STYLE 4:<br/>PIN 1. CATHODE<br/>2. ANODE<br/>3. GATE<br/>4. ANODE</p>              | <p>STYLE 5:<br/>PIN 1. GATE<br/>2. ANODE<br/>3. CATHODE<br/>4. ANODE</p>     |
| <p>STYLE 6:<br/>PIN 1. MT1<br/>2. MT2<br/>3. GATE<br/>4. MT2</p>                 | <p>STYLE 7:<br/>PIN 1. GATE<br/>2. COLLECTOR<br/>3. EMITTER<br/>4. COLLECTOR</p> | <p>STYLE 8:<br/>PIN 1. N/C<br/>2. CATHODE<br/>3. ANODE<br/>4. CATHODE</p>   | <p>STYLE 9:<br/>PIN 1. ANODE<br/>2. CATHODE<br/>3. RESISTOR ADJUST<br/>4. CATHODE</p> | <p>STYLE 10:<br/>PIN 1. CATHODE<br/>2. ANODE<br/>3. CATHODE<br/>4. ANODE</p> |

### SOLDERING FOOTPRINT\*



SCALE 3:1 (mm / inches)

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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